# 4-Bit D Flip-Flop

The MC10E/100E131 is a quad master-slave D-type flip-flop with differential outputs. Each flip-flop may be clocked separately by holding Common Clock ( $C_C$ ) LOW and using the Clock Enable ( $C_C$ ) inputs for clocking. Common clocking is achieved by holding the  $C_C$  inputs LOW and using  $C_C$  to clock all four flip-flops. In this case, the  $C_C$  inputs perform the function of controlling the common clock, to each flip-flop.

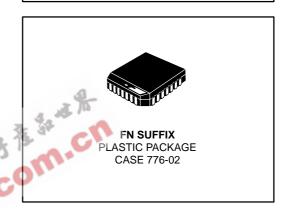
Individual asynchronous resets are provided (R). Asynchronous set controls (S) are ganged together in pairs, with the pairing chosen to reflect physical chip symmetry.

Data enters the master when both C<sub>C</sub> and CE are LOW, and transfers to the slave when either C<sub>C</sub> or CE (or both) go HIGH.

- 1100MHz Min. Toggle Frequency
- Differential Outputs
- · Individual and Common Clocks
- Individual Resets (asynchronous)
- · Paired Sets (asynchronous)
- Extended 100E VEE Range of 4.2V to 5.46V
- 75k $\Omega$  Input Pulldown Resistors

## MC10E131 MC100E131

## 4-BIT D FLIP-FLOP



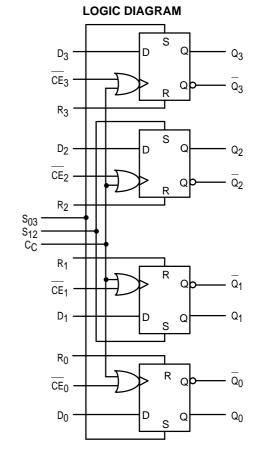
#### 18 Q Q<sub>2</sub> 17 🛮 Q<sub>2</sub> $D_3$ 16 V<sub>CC</sub> 15 Q<sub>1</sub> $V_{EE}$ CC 14 Q<sub>1</sub> 13 Q Q<sub>0</sub> S<sub>03</sub> D<sub>0</sub> 12 Q Q<sub>0</sub> NC Vcco $R_0$ $D_1$ CE<sub>1</sub> $R_1$

Pinout: 28-Lead PLCC (Top View)

 $^{\ast}$  All VCC and VCCO pins are tied together on the die.

#### **PIN NAMES**

Pin	Function							
<u>D</u> 0 – D <u>3</u>	Data Inputs							
CE <sub>0</sub> – CE <sub>3</sub>	Clock Enables (Individual)							
$R_0 - R_3$	Resets							
CC	Common Clock							
S <sub>03</sub> , S <sub>12</sub>	Sets (paired)							
$\underline{Q}_0 - \underline{Q}_3$	True Outputs							
$Q_0 - Q_3$	Inverting Outputs							



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### MC10E131 MC100E131

### **DC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

		-40°C		0°C		25°C			85°C						
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Cond
lн	Input HIGH													μΑ	
	Current C <sub>C</sub>			350			350			350			350		
	<u></u>			450			450			450			450		
	_ <u>\$</u> R, CE			300			300			300			300		
	D			150			150			150			150		
liee	Power Supply													mΑ	
	Current 10E		58	70		58	70		58	70		58	70		
	100E		58	70		58	70		58	70		67	81		

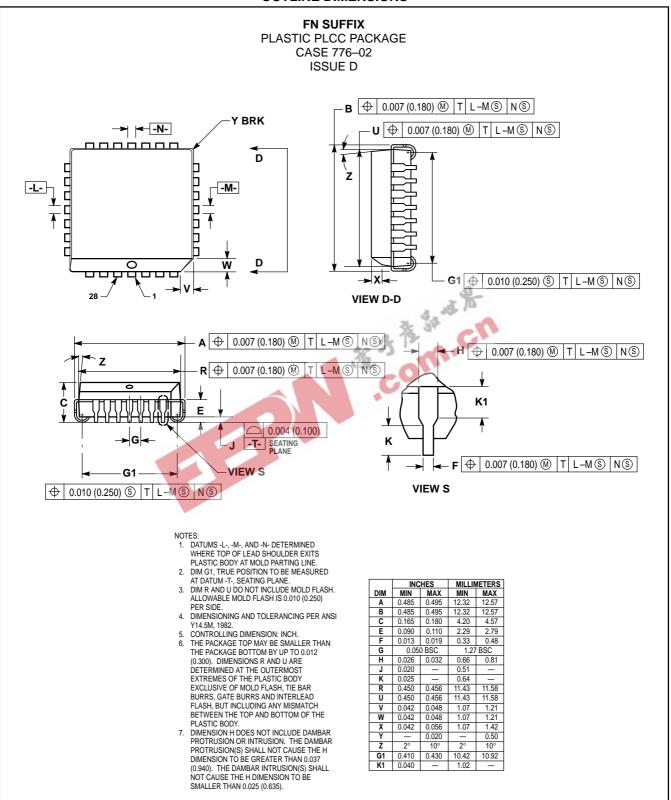
## **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

				–40°C		0°C to 85°C				
Symbol	Characteristic		Min	Тур	Max	Min	Тур	Max	Unit	Condition
fMAX	Maximum Toggle Frequency		1000	1400		1100	1400		MHz	
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output	CE C <sub>C</sub> R S	310 275 300 300	600 600 625 550	750 725 775 775	360 32 <b>5</b> 350 350	500 500 550 550	700 675 725 725	ps	
ts	Setup Time	D	200	20	3	150	20		ps	1
tH	Hold Time	D	225	<b>-</b> 20		175	-20		ps	1
<sup>t</sup> RR	Reset Recovery Time		450	150		400	150		ps	
tpW	Minimum Pulse Width	CLK R, S	400 400			400 400			ps	
tSKEW	Within-Device Skew			60			60		ps	2
t <sub>r</sub> /t <sub>f</sub>	Rise/Fall Time		275	460	725	300	480	675	ps	20-80%

**MOTOROLA** 2–2

Setup/hold times guaranteed for both C<sub>C</sub> and CE.
Within-device skew is defined as identical transitions on similar paths through a device.

#### **OUTLINE DIMENSIONS**



2–3 MOTOROLA

#### MC10E131 MC100E131



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#### How to reach us:

**USA/EUROPE/Locations Not Listed**: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



