DC-DC Converter (-20V, -4.0A)

RTQ040P02

Features

- 1) Low on-resistance. (110m Ω at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

DC-DC converter

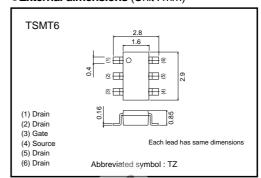
Structure

Silicon P-channel MOS FET

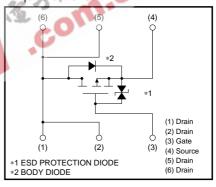
Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
RTQ040P02	0	

●External dimensions (Unit : mm)



●Equivalent circuit



●Absolute maximum ratings (Ta=25°C)

	Symbol	Limits	Unit
	VDSS	-20	V
	V _{GSS}	±12	V
Continuous	I _D	±4.0	Α
Pulsed	I _{DP}	±16	A *1
Continuous	Is	-1	A *1
Pulsed	I _{SP}	-16	Α
Total power dissipation		1.25	W *2
Channel temperature		150	°C
Range of Storage temperature		-55 to +150	°C
	Pulsed Continuous	VDSS VGSS	Voss −20 VGSS ±12 Continuous Ib ±4.0 Pulsed IbP ±16 Continuous Is −1 Pulsed IsP −16 PD 1.25 Tch 150

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±10	μΑ	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	-20	-	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	-	-1	μΑ	Vps= -20V, Vgs=0V
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	Vps= -10V, Ip= -1mA
Static drain-source on-state resistance		_	35	50	mΩ	I _D = -4A, V _G = -4.5V *
	R _{DS} (on)	_	40	55	mΩ	I _D = -4A, V _G = -4V *
		_	60	85	mΩ	I _D = -2.0A, V _{GS} = -2.5V *
Forward transfer admittance	Yfs	3.5	-	-	S	V _{DS} = -10V, I _D = -2.0A *
Input capacitance	Ciss	_	1350	-1	pF	V _{DS} = -10V
Output capacitance	Coss	_	210		pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	150	1 -	pF	f=1MHz
Turn-on delay time	td (on)		15), =/	ns	ID= -2.0A *
Rise time	tr		35	(-)	ns	VDD≒ -15V *
Turn-off delay time	t _{d (off)}		60	_	ns	$V_{GS} = -4.5V$ $R_{L} = 7.5\Omega$
Fall time	tr		3 0	_	ns	RGs= 10Ω
Total gate charge	Qg	_	12.2	_	nC	V _{DD} ≒−15V R _L ≒3.75Ω
Gate-source charge	Qgs	-	2.6	_	nC	$V_{GS}=-4.5V$ RGS=10 Ω
Gate-drain charge	Qgd	_	3.4	_	nC	I _D = -4.0A

Body diode characteristics (source-drain characteristics)

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Forward voltage	VSD	_	_	-1.2	V	I _S = -1A, V _{GS} =0V

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

•Electrical characteristic curves

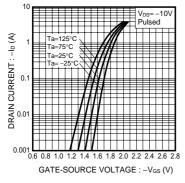


Fig.1 Typical Transfer Characteristics

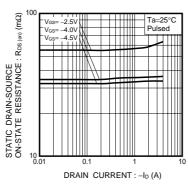


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

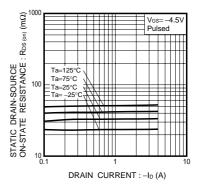


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

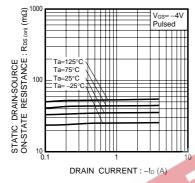


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

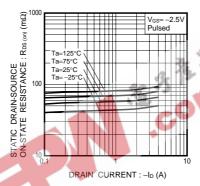


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

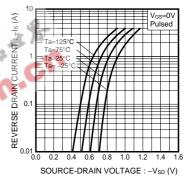


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

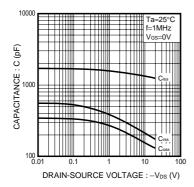


Fig.7 Typical Capacitance vs. Drain-Source Voltage

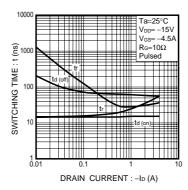


Fig.8 Switching Characteristics

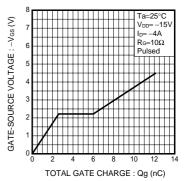


Fig.9 Dynamic Input Characteristics

●Measurement circuits

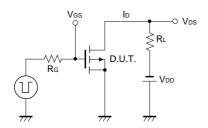


Fig.10 Switching Time Measurement Circuit

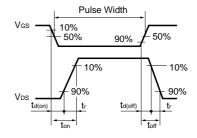


Fig.11 Switching Waveforms

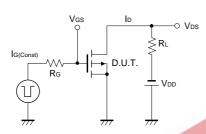


Fig.12 Gate Charge Measurement Circuit

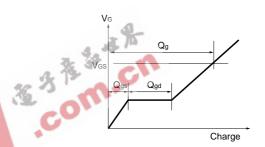


Fig.13 Gate Charge Waveforms

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