

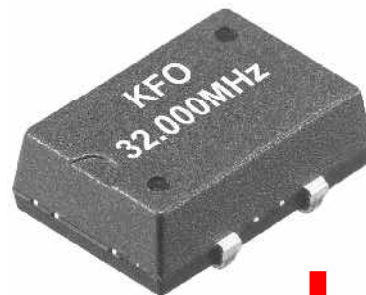
# 5.0V PLASTIC HCMOS/TTL SMD OSCILLATOR KFO SERIES

## FEATURES

- 5.0V Operation
- HCMOS/TTL Output
- Tri-State Enable/Disable
- Tape and Reel (1,000 pcs. STD)

## OPTIONS

- 3.3V Operation (KFO30AT)



# Discontinued

• MODEL NUMBER SELECTION			
Model Number	Frequency Stability <sup>1</sup>	Operating Temperature (°C)	Frequency Range (MHz)
KFO90AT	±100PPM	-10 ~ +70	1.500 ~ 67.000
KFO90FT	±100PPM	-40 ~ +85	1.500 ~ 67.000
KFO95AT	±50PPM	-10 ~ +70	1.500 ~ 67.000

• ELECTRICAL CHARACTERISTICS	
PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.500 ~ 67.000 MHz
Storage Temperature Range (TSTG)	-55°C ~ +125°C
Supply Voltage (VDD)	5.0V ± 10%
Input Current (IDD)	
1.500 ~ 32.000 MHz	27mA
32.000+ ~ 50.000 MHz	45mA
50.000+ ~ 67.000 MHz	60mA
Output Symmetry (50% VDD)	
1.500 ~ 50.000 MHz	45% ~ 55%
50.000+ ~ 67.000 MHz	40% ~ 60%
Rise Time (10% ~ 90% VDD) (TR)	7nS
Fall Time (90% ~ 10% VDD) (TF)	7nS
Output Voltage (VOL)	10% VDD
(VOH)	90% VDD Min
Output Current (IOL)	16mA Min
(IOH)	-16mA Min
Output Load	
TTL	10TTL
HCMOS	50pF
Start-up Time (Ts)	10mS
Enable/Disable Time <sup>2</sup>	100nS

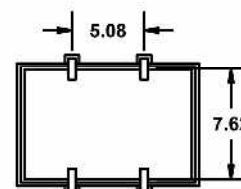
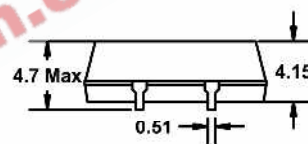
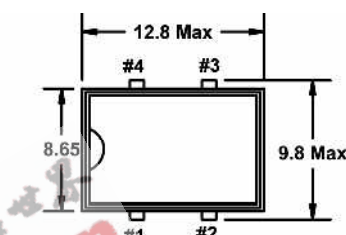
<sup>1</sup> Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

<sup>2</sup> An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

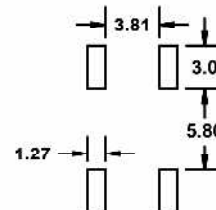
See page 30 for mechanical specifications, test circuits, and output waveform.

Note: A 0.01µF bypass capacitor should be placed between VDD (Pin 4) and GND (Pin 2) to minimize power supply line noise.

All specifications subject to change without notice. Rev. 02/10/03



### Recommended Solder Pad Layout



### Pin Connections

- #1 E/D
- #2 GND
- #3 Output
- #4 VDD

All dimensions are in millimeters.

• ENABLE / DISABLE FUNCTION	
INH (Pin 1)	OUTPUT (Pin 3)
OPEN <sup>2</sup>	ACTIVE
'1' Level VIH ≥ 2.2 V	ACTIVE
'0' Level VIL ≤ 0.8 V	High Z

See page 60 for tape and reel specifications.