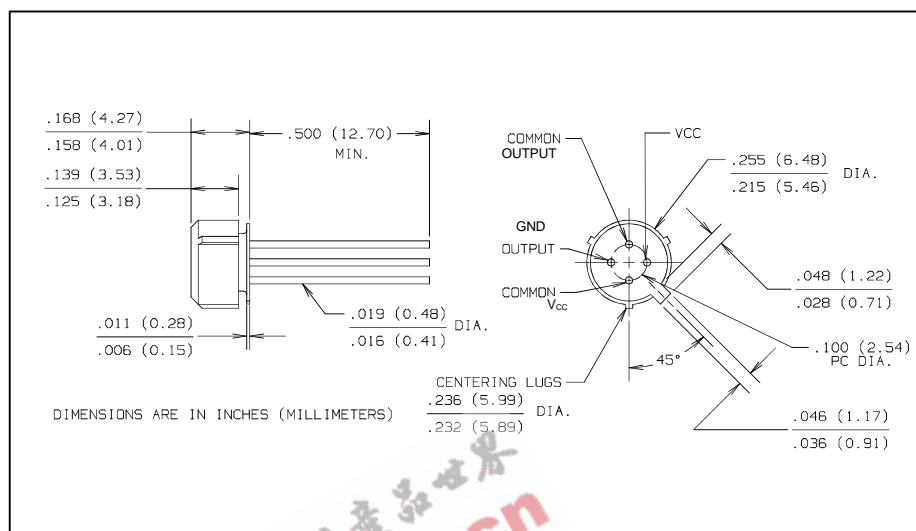
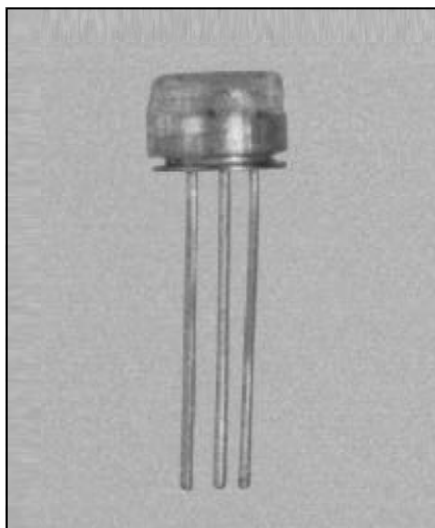


# 5 MBd Fiber Optic Receiver Type OPF520



## Features

- Low cost plastic cap package
- Designed to self align in the 0.228 diameter bore of standard fiber optic receptacles
- Press fit simplifies component installation
- Optimized for fiber optic applications using 50 to 200 micron fiber

## Description

The OPF520 contains a monolithic photo-IC comprised of a photodetector and DC amplifier driving an open collector output Schottky transistor. The output makes the OPF520 compatible with TTL and CMOS logic.

The receiver is designed to operate from a single +5 V supply. It is essential that a bypass capacitor be connected from V<sub>CC</sub> to Common of the receiver.

## Absolute Maximum Ratings (T<sub>A</sub> = 25° C unless otherwise noted)

Storage Temperature	-55° C to +115° C
Operating Temperature	-40° C to +85° C
Lead Soldering Temperature (for 10 sec.)	260° C
Supply Voltage	-0.5 to 7.0 V
Output Current	25 mA
Output Voltage	-0.5 to 18.0 V
Open Collector Power Dissipation	40 mW
Fan Out (TTL)	5 <sup>(1)</sup>

This component is susceptible to damage from electrostatic discharge (ESD). Normal static precautions should be taken in handling and assembly of this component to prevent ESD damage or degradation.

# Types OPF520

**Electrical Characteristics** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

**4.75 ≤ V<sub>CC</sub> ≤ 5.25, Fiber Sizes ≤ 100 Microns, N.A. ≤ 0.35, BER ≤ 10<sup>-9</sup>**

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITION
I <sub>OH</sub>	High Level Output Current		5	250	μA	V <sub>O</sub> = 18 V, P <sub>R</sub> < -40 dBm
V <sub>OL</sub>	Low Level Output Voltage		0.4	0.5	V	I <sub>O</sub> = 8 mA, P <sub>R</sub> > -24 dBm
I <sub>CCH</sub>	High Level Supply Current		3.5	6.3	mA	V <sub>CC</sub> = 5.25 V, P <sub>R</sub> < -40 dBm
I <sub>CCL</sub>	Low Level supply Current		6.2	10	mA	V <sub>CC</sub> = 5.25 V, P <sub>R</sub> > -24 dBm
P <sub>RH</sub>	Peak Input Power Level Logic HIGH			-40	dBm	λ <sub>p</sub> = 840 nm <sup>(2)</sup>
				0.1	μW	
P <sub>RL</sub>	Peak Input Power Level Logic LOW	-25.4		-9.2	dBm	λ <sub>p</sub> = 840 nm, I <sub>OL</sub> = 8 mA <sup>(2)</sup>
		2.9		120	μW	
		-24		-10	dBm	-40° C ≤ T <sub>A</sub> ≤ +85° C
		4.0		100	μW	
t <sub>PLHR</sub>	Propagation Delay LOW to HIGH		65		ns	P <sub>R</sub> = -21 dBm, Data Rate = 5 MBd
t <sub>PHLR</sub>	Propagation Delay HIGH to LOW		49		ns	

**Notes:**

(1) 8 mA load (5 x 1.6 mA), R<sub>L</sub> = 560 Ω.

(2) Measured at the end of 100/140 μm fiber cable with a large area detector.



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