

54125/DM54125/DM74125 Quad TRI-STATE® Buffers

General Description

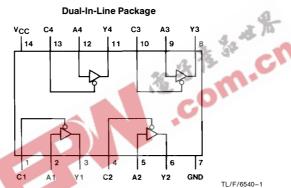
This device contains four independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard TTL output with additional drive capability at the high Logic level to permit the driving of bus lines without external pull-up resistors. When disabled, both the output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver.

To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Features

 Alternate Military/Aerospace device (54125) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Connection Diagram



Order Number 54125DMQB, 54125FMQB, DM54125J, DM54125W or DM74125N See NS Package Number J14A, N14A or W14B

Function Table

Y = A							
Inp	outs	Output					
Α	С	Υ					
L	L	L					
Н	L	Н					
X	Н	Hi-Z					

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level

Hi-Z = TRI-STATE (Outputs are disabled)

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage Input Voltage 5.5V Operating Free Air Temperature Range

 -55°C to $+125^{\circ}\text{C}$ DM54 and 54 DM74 0° C to $+70^{\circ}$ C

-65°C to +150°C Storage Temperature Range

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54125			DM74125			Units
	i didilictei	Min	Nom	Max	Min	Nom	Max	00
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
Гон	High Level Output Current			-2			-5.2	mA
l _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Symbol	Parameter		Condi	tions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V _{CC} =	= Min, I _I = -	-12 mA				-1.5	>
V _{OH}	High Level Output Voltage		= Min, I _{OH} = Max, V _{IH} =			2.4	3.3		>
V _{OL}	Low Level Output Voltage		= Min, I _{OL} = Min, V _{IL} =				0.2	0.4	٧
lı	Input Current @ Max Input Voltage	V _{CC} =	= Max, V _I =	5.5V				1	mA
l _{IH}	High Level Input Current	V _{CC} =	Max, V _I =	2.4V				40	μΑ
I _{IL}	Low Level Input Current	V _{CC} =	Max, V _I =	0.4V				-1.6	mA
lizL	Off-State Input Current with Low Level Input Voltage Applied	V _{CC} =	= Max, V _I =	0.4V				-40	μΑ
lozн	Off-State Output Current with High Level Output Voltage Applied		= Max, V _O = Min, V _{IL} =					40	μΑ
OZL	Off-State Output Current with Low Level Output Voltage Applied		= Max, V _O = Min, V _{IL} =					-40	μΑ
100	Short Circuit	V _{CC} = Max (Note 2)			DM54	-30		-70	mA
	Output Current				DM74	-28		-70	
lcc	Supply Current	V _{CC} =	Max (Note	3)			36	54	n

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

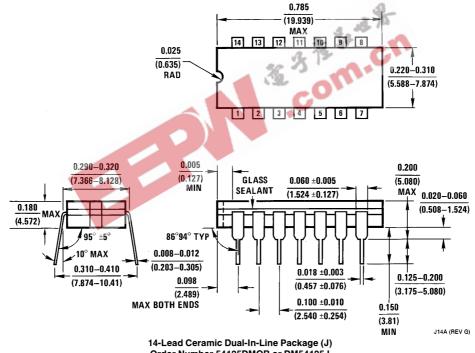
Note 2: Not more than one output should be shorted at a time.

Note 3: I_{CC} is measured with the output control (C) inputs at 4.5V, the data inputs grounded, and the outputs open.

$\textbf{Switching Characteristics} \text{ at V}_{CC} = 5 \text{V and T}_{A} = 25^{\circ}\text{C (See Section 1 for Test Waveforms and Output Load)}$

Symbol	Parameter					
		C _L = 5 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output				15	ns
t _{PHL}	Propagation Delay Time High to Low Level Output				18	ns
t _{PZH}	Output Enable Time to High Level Output				18	ns
t _{PZL}	Output Enable Time to Low Level Output				25	ns
t _{PHZ}	Output Disable Time from High Level Output		8			ns
t _{PLZ}	Output Disable Time from Low Level Output		14			ns

Physical Dimensions inches (millimeters)



14-Lead Ceramic Dual-In-Line Package (J) Order Number 54125DMQB or DM54125J NS Package Number J14A

Physical Dimensions inches (millimeters) (Continued) 3.14-0.709 1.15.2-1.319 1.15.2

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

National Semiconductor Europe

Europe Fax: (+49) 0-180-530 85 86
Email: cnjwge@tevm2.nsc.com
Deutsch Tel: (+49) 0-180-530 85 85
English Tel: (+49) 0-180-532 78 32
Français Tel: (+49) 0-180-532 33 18
Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductor Japan Ltd. Tel: 81-043-299-2309