## **8-Input Multiplexer**

The TTL/MSI SN74LS151 is a high speed 8-input Digital Multiplexer. It provides, in one package, the ability to select one bit of data from up to eight sources. The LS151 can be used as a universal function generator to generate any logic function of four variables. Both assertion and negation outputs are provided.

- Schottky Process for High Speed
- Multifunction Capability
- On-Chip Select Logic Decoding
- Fully Buffered Complementary Outputs
- Input Clamp Diodes Limit High Speed Termination Effects



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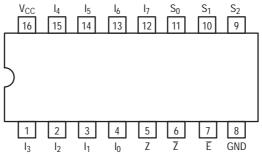
## LOW POWER SCHOTTKY

Symbol	Parameter	Min	Тур	Max	Unit	A
V <sub>CC</sub>	Supply Voltage	4.75	5.0	5.25	V	A AND Draw
Τ <sub>Α</sub>	Operating Ambient Temperature Range	0	25	70	°C	
I <sub>OH</sub>	Output Current – High			-0.4	mA	
I <sub>OL</sub>	Output Current – Low			8.0	mA	PLASTIC N SUFFIX
		-				16 16 1

#### **ORDERING INFORMATION**

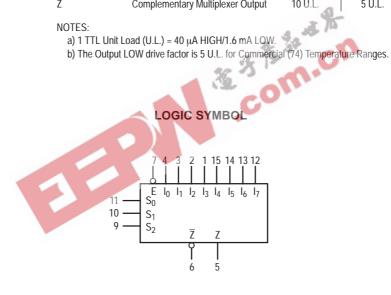
Device	Package	Shipping		
SN74LS151N	16 Pin DIP	2000 Units/Box		
SN74LS151D	16 Pin	2500/Tape & Reel		



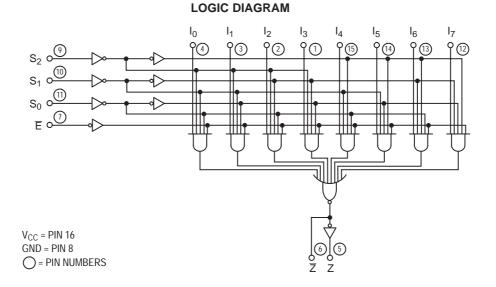


		LOADING	(Note a)
PIN NAMES		HIGH	LOW
$S_0 - S_2$	Select Inputs	0.5 U.L.	0.25 U.L.
Ē	Enable (Active LOW) Input	0.5 U.L.	0.25 U.L.
$I_0 - I_7$	Multiplexer Inputs	0.5 U.L.	0.25 U.L.
Z	Multiplexer Output	10 U.L.	5 U.L.
Z	Complementary Multiplexer Output	10 U.L.	5 U.L.

#### NOTES:

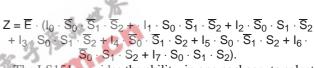


V<sub>CC</sub> = PIN 16 GND = PIN 8



#### FUNCTIONAL DESCRIPTION

The LS151 is a logical implementation of a single pole, 8-position switch with the switch position controlled by the state of three Select inputs,  $S_0$ ,  $S_1$ ,  $S_2$ . Both assertion and negation outputs are provided. The Enable input (E) is active LOW. When it is not activated, the negation output is HIGH and the assertion output is LOW regardless of all other inputs. The logic function provided at the output is:



The LS151 provides the ability, in one package, to select from eight sources of data or control information. By proper manipulation of the inputs, the LS151 can provide any logic function of four variables and its negation.

1	TRUTH TABLE													
	E	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>	I <sub>0</sub>	I <sub>1</sub>	l <sub>2</sub>	I <sub>3</sub>	I <sub>4</sub>	I <sub>5</sub>	I <sub>6</sub>	I <sub>7</sub>	Z	Z
	Н	X	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	н	L
	L	L	L	L	L	Х	Х	Х	Х	Х	Х	Х	н	L
	L	L	L	L	н	Х	Х	Х	Х	Х	Х	Х	L	Н
	L	L	L	Н	Х	L	Х	Х	Х	Х	Х	Х	Н	L
	L	L	L	Н	Х	Н	Х	Х	Х	Х	Х	Х	L	Н
	L	L	Н	L	Х	Х	L	Х	Х	Х	Х	Х	Н	L
	L	L	Н	L	Х	Х	Н	Х	Х	Х	Х	Х	L	Н
	L	L	Н	Н	Х	Х	Х	L	Х	Х	Х	Х	н	L
	L	L	Н	Н	Х	Х	Х	Н	Х	Х	Х	Х	L	Н
	L	н	L	L	Х	Х	Х	Х	L	Х	Х	Х	н	L
	L	н	L	L	Х	Х	Х	Х	Н	Х	Х	Х	L	Н
	L	н	L	Н	Х	Х	Х	Х	Х	L	Х	Х	н	L
	L	н	L	Н	Х	Х	Х	Х	Х	Н	Х	Х	L	Н
	L	н	Н	L	Х	Х	Х	Х	Х	Х	L	Х	н	L
	L	н	Н	L	Х	Х	Х	Х	Х	Х	Н	Х	L	Н
	L	н	Н	Н	Х	Х	Х	Х	Х	Х	Х	L	н	L
	L	н	Н	Н	Х	Х	Х	Х	Х	Х	Х	Н	L	Н

H = HIGH Voltage Level L = LOW Voltage Level

X = Don't Care

			Limits					
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
V <sub>IH</sub>	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs		
V <sub>IL</sub>	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage for All Inputs		
V <sub>IK</sub>	Input Clamp Diode Voltage		-0.65	-1.5	V	$V_{CC} = MIN, I_{IN} = -18 \text{ mA}$		
V <sub>OH</sub>	Output HIGH Voltage	2.7	3.5		V	$V_{CC} = MIN$ , $I_{OH} = MAX$ , $V_{IN} = V_{IH}$ or $V_{IL}$ per Truth Table		
M			0.25	0.4	V	I <sub>OL</sub> = 4.0 mA	$V_{CC} = V_{CC} MIN,$	
V <sub>OL</sub>	Output LOW Voltage		0.35	0.5	V	l <sub>OL</sub> = 8.0 mA	V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> per Truth Table	
				20	μΑ	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 2.7 V		
IIH	Input HIGH Current			0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V		
IIL	Input LOW Current			-0.4	mA	$V_{CC} = MAX, V_{IN} = 0.4 V$		
I <sub>OS</sub>	Short Circuit Current (Note 1)	-20		-100	mA	$V_{CC} = MAX$		
I <sub>CC</sub>	Power Supply Current			10	mA	V <sub>CC</sub> = MAX		

#### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

### AC CHARACTERISTICS (T<sub>A</sub> = $25^{\circ}$ C)

		Limits			1 St.	-11-		
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Select to Output Z		27 18	43 30	ns			
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Select to Output $\overline{Z}$		14 20	23 32	ns			
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Enable to Output Z	X,	26 20	42 32	ns	V <sub>CC</sub> = 5.0 V		
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Enable to Output Z		15 18	24 30	ns	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 15 pF		
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Data to Output Z		20 16	32 26	ns			
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay Data to Output $\overline{Z}$		13 12	21 20	ns			

#### AC WAVEFORMS

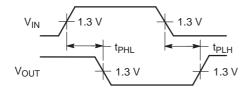


Figure 1.

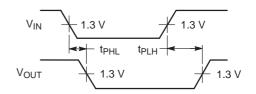
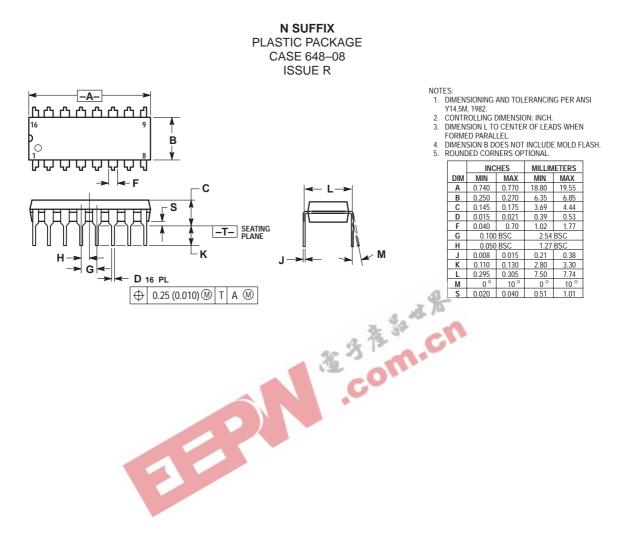


Figure 2.

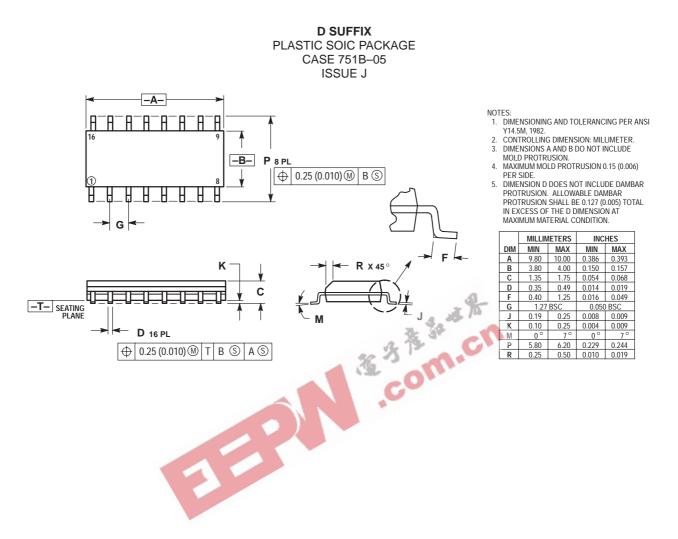


#### PACKAGE DIMENSIONS





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## **Notes**





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