

74VCXH245

Low Voltage Bidirectional Transceiver with Bushold

General Description

The VCXH245 contains eight non-inverting bidirectional buffers with 3-STATE outputs and is intended for bus oriented applications. The T/R input determines the direction of data flow. The $\overline{OE}$ input disables both the A and B Ports by placing them in a high impedance state. The VCXH245 data inputs include active bushold circuitry, eliminating the need for external pull-up resistors to hold unused or floating data inputs at a valid logic level.

The 74VCXH245 is designed for low voltage (1.65V to 3.6V) V_{CC} applications.

The 74VCXH245 is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

Features

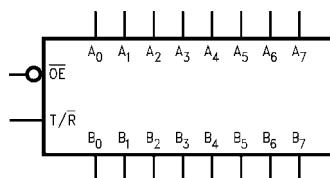
- 1.65V-3.6V V_{CC} supply operation
- Bushold on data inputs eliminates the need for external pull-up/pull-down resistors
- t_{PD}
 - 3.5 ns max for 3.0V to 3.6V V_{CC}
 - 4.2 ns max for 2.3V to 2.7V V_{CC}
 - 8.4 ns max for 1.65V to 1.95V V_{CC}
- Static Drive (I_{OH}/I_{OL})
 - ± 24 mA @ 3.0V V_{CC}
 - ± 18 mA @ 2.3V V_{CC}
 - ± 6 mA @ 1.65V V_{CC}
- Uses patented Quiet Series noise/EMI reduction circuitry
- Latchup performance exceeds 300 mA
- ESD performance:
 - Human body model > 2000V
 - Machine model > 200V

Ordering Code:

Order Number	Package Number	Package Description
74VCXH245WM	M20B	20-Lead Small Outline Integrated Circuit, JEDEC MS-013, 0.300" Wide Body
74VCXH245MTC	MTC20	20-Lead Thin Shrink Small Outline Package, JEDEC MO-153, 4.4mm Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Logic Symbol

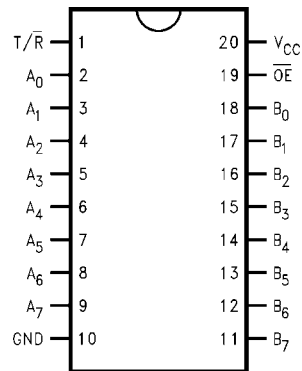


Pin Descriptions

Pin Names	Description
$\overline{OE}$	Output Enable Input (Active LOW)
$T/\overline{R}$	Transmit/Receive Input
A_0 - A_7	Side A Bushold Inputs or 3-STATE Outputs
B_0 - B_7	Side B Bushold Inputs or 3-STATE Outputs

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Connection Diagram

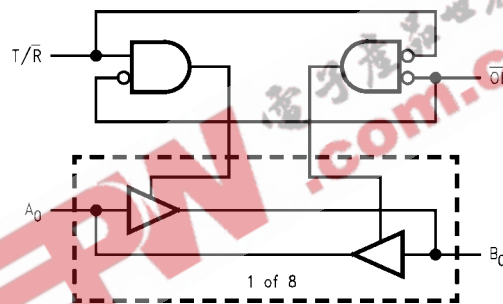


Truth Table

Inputs		Outputs
$\overline{OE}$	$T/\overline{R}$	
L	L	Bus B ₀ –B ₇ Data to Bus A ₀ –A ₇
L	H	Bus A ₀ –A ₇ Data to Bus B ₀ –B ₇
H	X	HIGH Z State on A ₀ –A ₇ , B ₀ –B ₇

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial
 Z = High Impedance

Logic Diagram



Absolute Maximum Ratings(Note 1)

Supply Voltage (V_{CC})	-0.5V to +4.6V
DC Input Voltage (V_I)	
$\overline{T/R}, \overline{OE}$	-0.5V to +4.6V
I/O Ports	-0.5V to $V_{CC} + 0.5V$
DC Output Voltage (V_O)(Note 2)	-0.5V to $V_{CC} + 0.5V$
DC Input Diode Current (I_{IK}) $V_I < 0V$	-50 mA
DC Output Diode Current (I_{OK})	
$V_O < 0V$	-50 mA
$V_O > V_{CC}$	+50 mA
DC Output Source/Sink Current (I_{OH}/I_{OL})	± 50 mA
DC V_{CC} or Ground Current	± 100 mA
Storage Temperature (T_{STG})	-65°C to +150°C

Recommended Operating Conditions (Note 3)

Power Supply	
Operating	1.65V to 3.6V
Data Retention Only	1.2V to 3.6V
Input Voltage	-0.3V to V_{CC}
Output Voltage (V_O)	0V to V_{CC}
Output Current in I_{OH}/I_{OL}	
$V_{CC} = 3.0V$ to 3.6V	± 24 mA
$V_{CC} = 2.3V$ to 2.7V	± 18 mA
$V_{CC} = 1.65V$ to 2.3V	± 6 mA
Free Air Operating Temperature (T_A)	-40°C to +85°C
Minimum Input Edge Rate ($\Delta t/\Delta V$)	
$V_{IN} = 0.8V$ to 2.0V, $V_{CC} = 3.0V$	10 ns/V

Note 1: The Absolute Maximum Ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the Absolute Maximum Ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: I_O Absolute Maximum Rating must be observed.

Note 3: Floating or unused control inputs must be held HIGH or LOW.

DC Electrical Characteristics ($2.7V < V_{CC} \leq 3.6V$)

Symbol	Parameter	Conditions	V_{CC} (V)	Min	Max	Units
V_{IH}	HIGH Level Input Voltage		2.7-3.6	2.0		V
V_{IL}	LOW Level Input Voltage		2.7-3.6		0.8	V
V_{OH}	HIGH Level Output Voltage	$I_{OH} = -100 \mu A$ $I_{OH} = -12$ mA $I_{OH} = -18$ mA $I_{OH} = -24$ mA	2.7-3.6 2.7 3.0 3.0	$V_{CC} - 0.2$ 2.2 2.4 2.2		V
V_{OL}	LOW Level Output Voltage	$I_{OL} = 100 \mu A$ $I_{OL} = 12$ mA $I_{OL} = 18$ mA $I_{OL} = 24$ mA	2.7-3.6 2.7 3.0 3.0		0.2 0.4 0.4 0.55	V
I_I	Input Leakage Current	$V_{IN} = V_{CC}$ or GND	2.7-3.6		± 5.0	μA
$I_{I(HOLD)}$	Bushold Input Minimum Drive Hold Current	$V_{IN} = 0.8V$ $V_{IN} = 2.0V$	3.0 3.0	75 -75		μA
$I_{I(OD)}$	Bushold Input Over-Drive Current to Change State	(Note 4) (Note 5)	3.6 3.6	450 -450		μA
I_{OZ}	3-STATE Output Leakage	$V_O = V_{CC}$ or GND $V_I = V_{IH}$ or V_{IL}	2.7-3.6		± 10	μA
I_{CC}	Quiescent Supply Current	$V_I = V_{CC}$ or GND	2.7-3.6		20	μA
ΔI_{CC}	Increase in I_{CC} per Input	$V_{IH} = V_{CC} - 0.6V$	2.7-3.6		750	μA

Note 4: An external driver must source at least the specified current to switch from LOW-to-HIGH.

Note 5: An external driver must sink at least the specified current to switch from HIGH-to-LOW.

DC Electrical Characteristics (2.3V ≤ V_{CC} ≤ 2.7V)

Symbol	Parameter	Conditions	V _{CC} (V)	Min	Max	Units
V _{IH}	HIGH Level Input Voltage		2.3–2.7	1.6		V
V _{IL}	LOW Level Input Voltage		2.3–2.7		0.7	V
V _{OH}	HIGH Level Output Voltage	I _{OH} = –100 μA I _{OH} = –6 mA I _{OH} = –12 mA I _{OH} = –18 mA	2.3–2.7 2.3 2.3 2.3	V _{CC} – 0.2 2.0 1.8 1.7		V
V _{OL}	LOW Level Output Voltage	I _{OL} = 100 μA I _{OL} = 12 mA I _{OL} = 18 mA	2.3–2.7 2.3 2.3		0.2 0.4 0.6	V
I _I	Input Leakage Current	V _{IN} = V _{CC} or GND	2.3–2.7		±5.0	μA
I _{I(HOLD)}	Bushold Input Minimum Drive Hold Current	V _{IN} = 0.7V V _{IN} = 1.6V	2.3 2.3	45 –45		μA
I _{I(OD)}	Bushold Input Over-Drive Current to Change State	(Note 6) (Note 7)	2.7 2.7	300 –300		μA
I _{OZ}	3-STATE Output Leakage	V _O = V _{CC} or GND V _I = V _{IH} or V _{IL}	2.3–2.7		±10	μA
I _{CC}	Quiescent Supply Current	V _I = V _{CC} or GND	2.3–2.7		20	μA

Note 6: An external driver must source at least the specified current to switch from LOW-to-HIGH.

Note 7: An external driver must sink at least the specified current to switch from HIGH-to-LOW.

DC Electrical Characteristics (1.65V ≤ V_{CC} < 2.3V)

Symbol	Parameter	Conditions	V _{CC} (V)	Min	Max	Units
V _{IH}	HIGH Level Input Voltage		1.65–2.3	0.65 × V _{CC}		V
V _{IL}	LOW Level Input Voltage		1.65–2.3		0.35 × V _{CC}	V
V _{OH}	HIGH Level Output Voltage	I _{OH} = –100 μA I _{OH} = –6 mA	1.65–2.3 1.65	V _{CC} – 0.2 1.25		V
V _{OL}	LOW Level Output Voltage	I _{OL} = 100 μA I _{OL} = 6 mA	1.65–2.3 1.65		0.2 0.3	V
I _I	Input Leakage Current	V _{IN} = V _{CC} or GND	1.65–2.3		±5.0	μA
I _{I(HOLD)}	Bushold Input Minimum Drive Hold Current	V _{IN} = 0.57V V _{IN} = 1.07V	1.65 1.65	25 –25		μA
I _{I(OD)}	Bushold Input Over-Drive Current to Change State	(Note 8) (Note 9)	1.95 1.95	200 –200		μA
I _{OZ}	3-STATE Output Leakage	V _O = V _{CC} or GND	1.65–2.3		±10	μA
I _{CC}	Quiescent Supply Current	V _I = V _{CC} or GND	1.65–2.3		20	μA

Note 8: An external driver must source at least the specified current to switch from LOW-to-HIGH.

Note 9: An external driver must sink at least the specified current to switch from HIGH-to-LOW.

AC Electrical Characteristics (Note 10)

Symbol	Parameter	T _A = -40°C to +85°C, C _L = 30 pF, R _L = 500Ω						Units
		V _{CC} = 3.3V ± 0.3V		V _{CC} = 2.5V ± 0.2V		V _{CC} = 1.8V ± 0.15V		
		Min	Max	Min	Max	Min	Max	
t _{PHL}	Propagation Delay	0.6	3.5	0.8	4.2	1.5	8.4	ns
t _{PLH}	A _n to B _n or B _n to A _n							
t _{PZL}	Output Enable Time	0.6	4.5	0.8	5.6	1.5	9.8	ns
t _{PZH}								
t _{PLZ}	Output Disable Time	0.6	3.6	0.8	4.0	1.5	7.2	ns
t _{PHZ}								
t _{OSHL}	Output to Output Skew		0.5		0.5		0.75	ns
t _{OSLH}	(Note 11)							

Note 10: For $C_L = 50\text{ pF}$, add approximately 300 ps to the AC maximum specification.

Note 11: Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW (t_{OSHL}) or LOW-to-HIGH (t_{OSLH}).

Dynamic Switching Characteristics

Symbol	Parameter	Conditions	V_{CC} (V)	$T_A = 25^{\circ}\text{C}$	Units
				Typical	
V_{OLP}	Quiet Output Dynamic Peak V_{OL}	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	0.3 0.7 1.0	V
V_{OLV}	Quiet Output Dynamic Valley V_{OL}	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	-0.3 -0.7 -1.0	V
V_{OHV}	Quiet Output Dynamic Valley V_{OH}	$C_L = 30\text{ pF}, V_{IH} = V_{CC}, V_{IL} = 0V$	1.8 2.5 3.3	1.3 1.7 2.0	V

Capacitance

Symbol	Parameter	Conditions	$T_A = +25^{\circ}\text{C}$	Units
			Typical	
C_{IN}	Input Capacitance	$V_I = 0V$ or V_{CC} , $V_{CC} = 1.8V, 2.5V$ or $3.3V$	6	pF
$C_{I/O}$	Input/Output Capacitance	$V_I = 0V$ or V_{CC} , $V_{CC} = 1.8V, 2.5V$ or $3.3V$	7	pF
C_{PD}	Power Dissipation Capacitance	$V_I = 0V$ or V_{CC} , $f = 10\text{ MHz}$, $V_{CC} = 1.8V, 2.5V$ or $3.3V$	20	pF

AC Loading and Waveforms

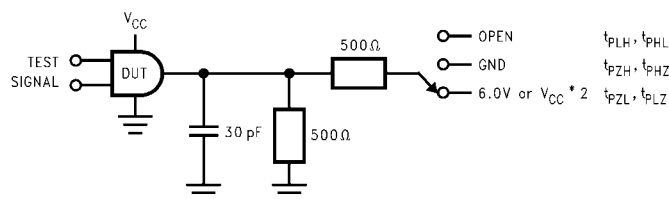


FIGURE 1. AC Test Circuit

TEST	SWITCH
t_{PLH} , t_{PHL}	Open
t_{PZL} , t_{PLZ}	6V at $V_{CC} = 3.3 \pm 0.3V$; $V_{CC} \times 2$ at $V_{CC} = 2.5V \pm 0.2V$; $1.8V \pm 0.15V$
t_{PZH} , t_{PHZ}	GND



FIGURE 2. Waveform for Inverting and Non-inverting Functions

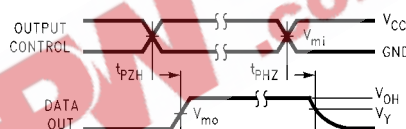


FIGURE 3. 3-STATE Output High Enable and Disable Times for Low Voltage Logic

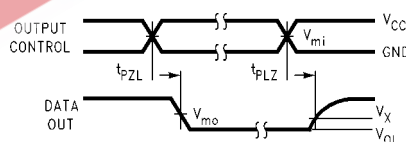
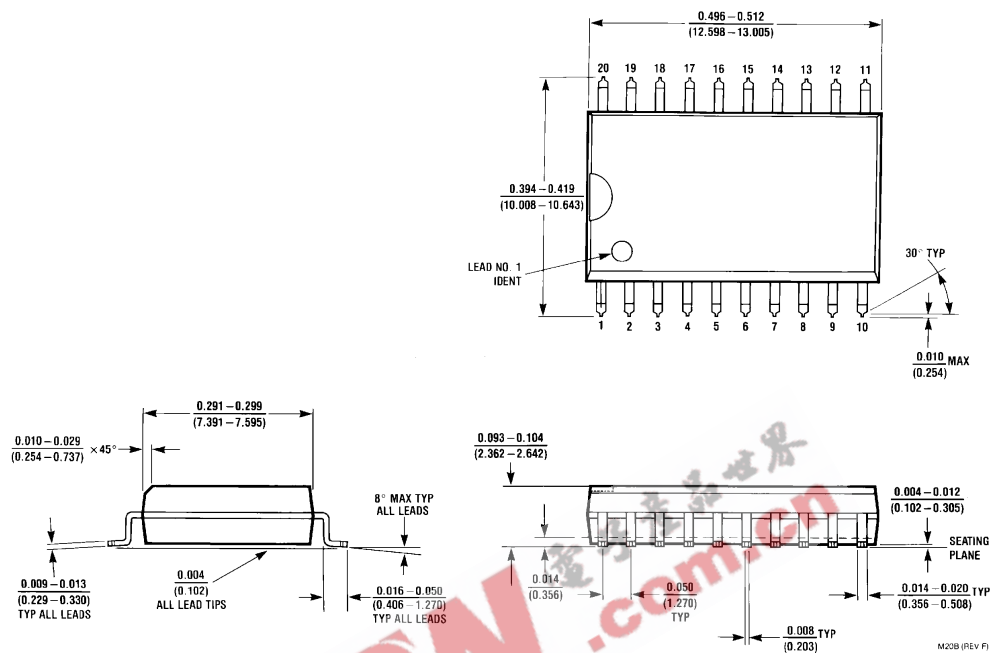


FIGURE 4. 3-STATE Output Low Enable and Disable Times for Low Voltage Logic

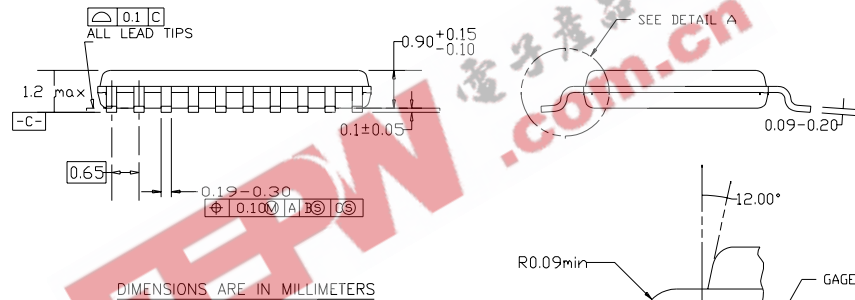
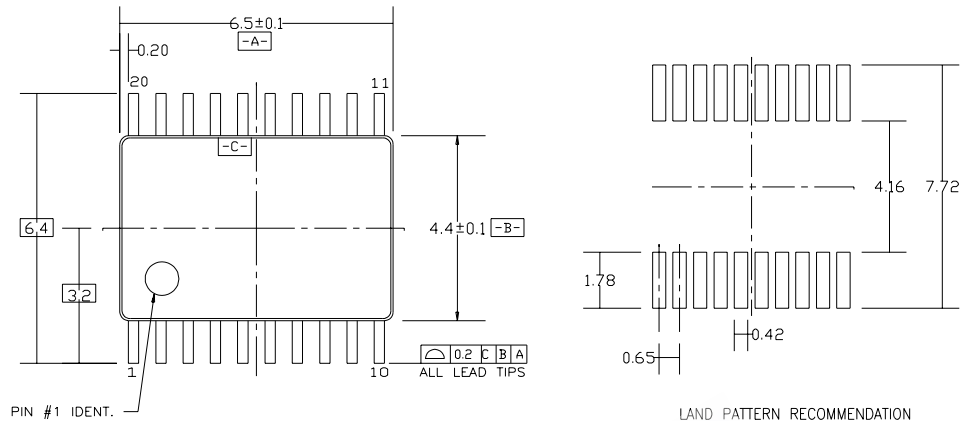
Symbol	V_{CC}		
	$3.3V \pm 0.3V$	$2.5V \pm 0.2V$	$1.8V \pm 0.15V$
V_{mi}	1.5V	$V_{CC}/2$	$V_{CC}/2$
V_{mo}	1.5V	$V_{CC}/2$	$V_{CC}/2$
V_x	$V_{OL} + 0.3V$	$V_{OL} + 0.15V$	$V_{OL} + 0.15V$
V_y	$V_{OH} - 0.3V$	$V_{OH} - 0.15V$	$V_{OH} - 0.15V$

Physical Dimensions inches (millimeters) unless otherwise noted



20-Lead Small Outline Integrated Circuit, JEDEC MS-013, 0.300" Wide Body
Package Number M20B

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



NOTES:

- CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION AC, REF NOTE 6, DATE 7/93.
- DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLDS FLASH, AND TIE BAR EXTRUSIONS.
- DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1982.

20-Lead Thin Shrink Small Outline Package, JEDEC MO-153, 4.4mm Wide Package Number MTC20

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