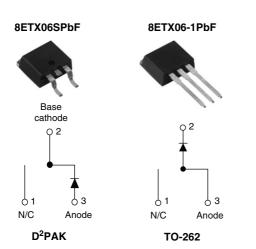


Vishay High Power Products

Hyperfast Rectifier, 8 A FRED PtTM



PRODUCT SUMMARY				
t _{rr} (typical)	15 ns			
I _{F(AV)}	8 A			
V _R	600 V			

FEATURES

- · Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for Q101 level

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

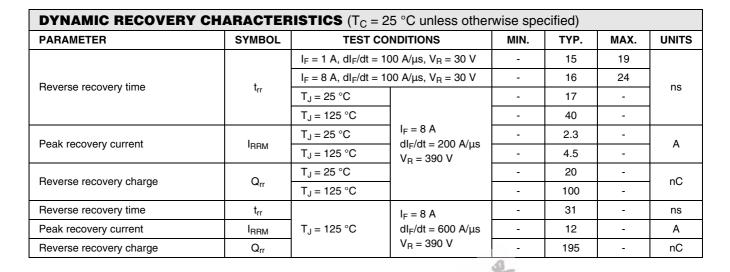
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V _{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	8		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	110	А	
Peak repetitive forward current	I _{FM}		18		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	C°	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	N.
Forward voltage	V _F	I _F = 8 A	-	2.3	3.0	V
		I _F = 8 A, T _J = 150 °C	-	1.4	1.7	
Reverse leakage current	1	$V_{R} = V_{R}$ rated	-	0.3	50	
	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	35	500	μΑ
Junction capacitance	CT	V _R = 600 V	-	17	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

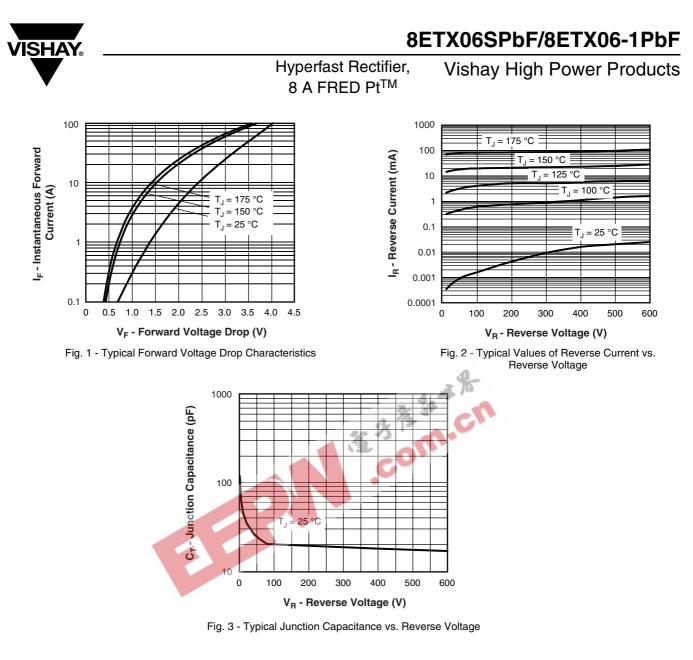
* Pb containing terminations are not RoHS compliant, exemptions may apply

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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}	COM	- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}	R _{thJC}		1.4	2	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Maddanadarian		Case style D ² PAK 8ETX06S		X06S		
Marking device		Case style TO-262	8ETX06-1			



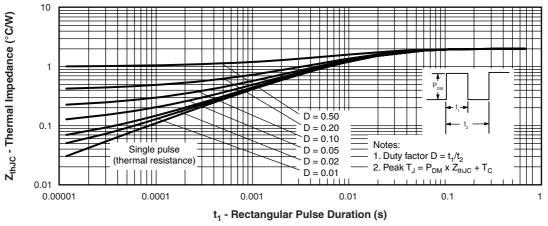
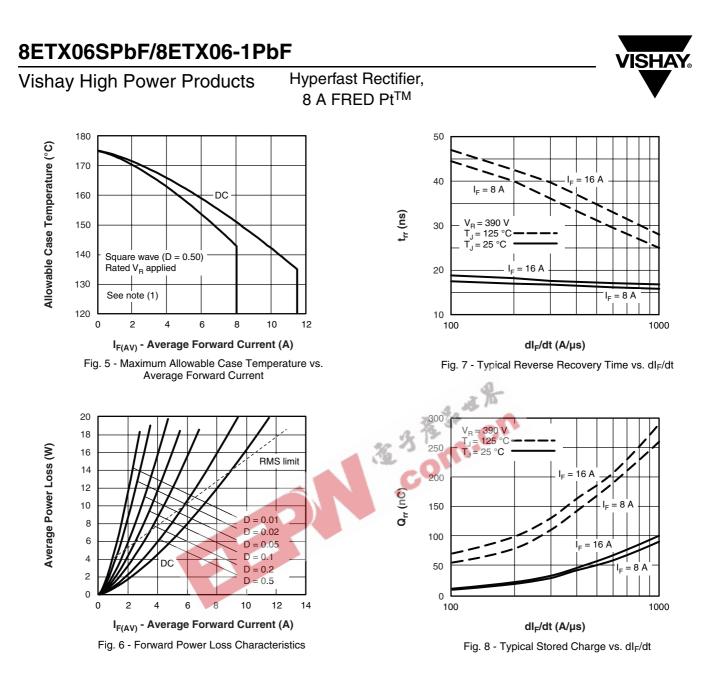


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{th,JC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} =$ Inverse power loss = $V_{R1} \times I_R (1 D)$; I_R at $V_{R1} =$ Rated V_R



Hyperfast Rectifier, 8 A FRED Pt[™]

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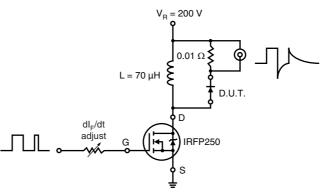


Fig. 9 - Reverse Recovery Parameter Test Circuit

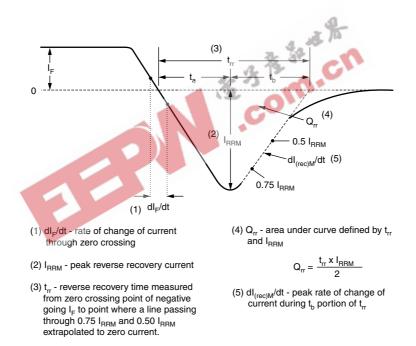


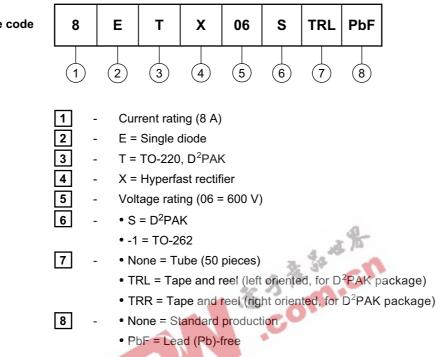
Fig. 10 - Reverse Recovery Waveform and Definitions

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Device code



LINKS TO RELATED DOCUMENTS				
Dimensions			http://www.vishay.com/doc?95014	
Part marking information			http://www.vishay.com/doc?95008	
Packaging information			http://www.vishay.com/doc?95032	



Vishay

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