

## **Digital-to-Analog Selection Table**

| Generic<br>Part #   | Resolution<br>(Bits) | Settling<br>Time (ns<br>typ) | # DACs<br>Per Pkg | Output (I<br>or V) | Data<br>Interface | FS Out<br>Range                           | REF<br>(Int/Ext) | Power Supply (V nom)  | Power<br>Dissipation<br>(mW max) |
|---|----------------------|------------------------------|-------------------|--------------------|-------------------|---|------------------|---|----------------------------------|
| 8-Bit, 10 ns to 100 ns, Sorted by Resolution, Update Rate and # of DACs Per Package |                      |                              |                   |                    |                   |   |                  |   |                                  |
| <u>AD9748</u>   | 8                    | 11                           | 1                 | I                  | Par               | Diff (0 to 2mA),<br>Diff (0 to 20mA)      | Int/Ext          | Range-Single (+2.7 to<br>+3.6), Single (+3),<br>Single (+3.3)                 | 135                              |
| <u>AD9708</u>   | 8                    | 35                           | 1                 | l                  | Par               | Diff Uni 2mA, Diff<br>Uni 20 mA           | Int/Ext          | Range-Single (+2.7 to<br>+5.5), Single (+3),<br>Single (+3.3), Single<br>(+5) | 180                              |
| <u>AD9709</u>   | 8                    | 35                           | 2                 | I                  | Par               | Adjustable Uni<br>(2mA) to Uni (20<br>mA) | Int/Ext          | Single (+3 to +5),<br>Range-Single (+2.7 to<br>+5.5)                          | 410                              |
| DAC08   | 8                    | 85                           | 1                 | I                  | Par               | Diff (0 to 2 mA)                          | Ext              | Dual (+15, -15)   | 174                              |

Last Updated: 2/2003

