



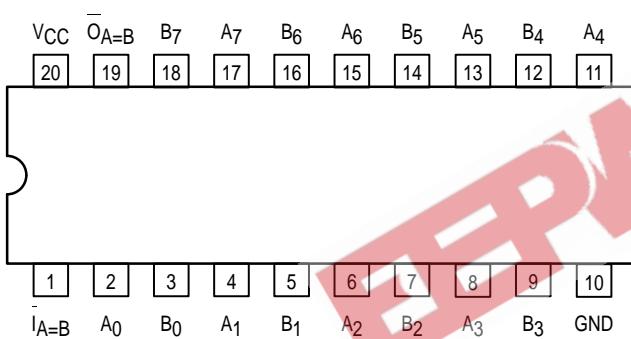
MOTOROLA

**MC74AC521
MC74ACT521**

8-Bit Identity Comparator

The MC74AC521/74ACT521 is an expandable 8-bit comparator. It compares two words of up to eight bits each and provides a LOW output when the two words match bit for bit. The expansion input $I_{A=B}$ also serves as an active LOW enable input.

- Compares Two 8-Bit Words in 6.5 ns Typ
- Expandable to Any Word Length
- 20-Pin Package
- Outputs Source/Sink 24 mA
- 'ACT521 has TTL-Compatible Inputs



PIN NAMES

- A₀-A₇ Word A Inputs
B₀-B₇ Word B Inputs
 $I_{A=B}$ Expansion or Enable Input
 $O_{A=B}$ Identity Output

TRUTH TABLE

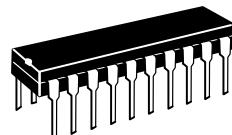
Inputs		Outputs
$I_{A=B}$	A, B	$\bar{O}_{A=B}$
L	A = B*	L
L	A ≠ B	H
H	A = B*	H
H	A ≠ B	H

H = HIGH Voltage Level

L = LOW Voltage Level

*A₀=B₀, A₁=B₁, A₂=B₂, etc.

8-BIT IDENTITY COMPARATOR

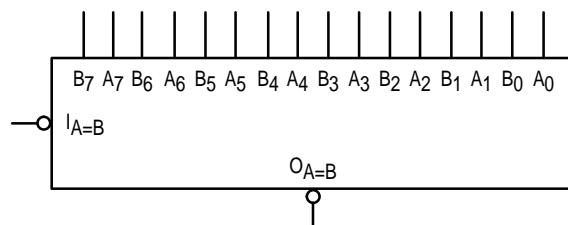


N SUFFIX
CASE 738-03
PLASTIC



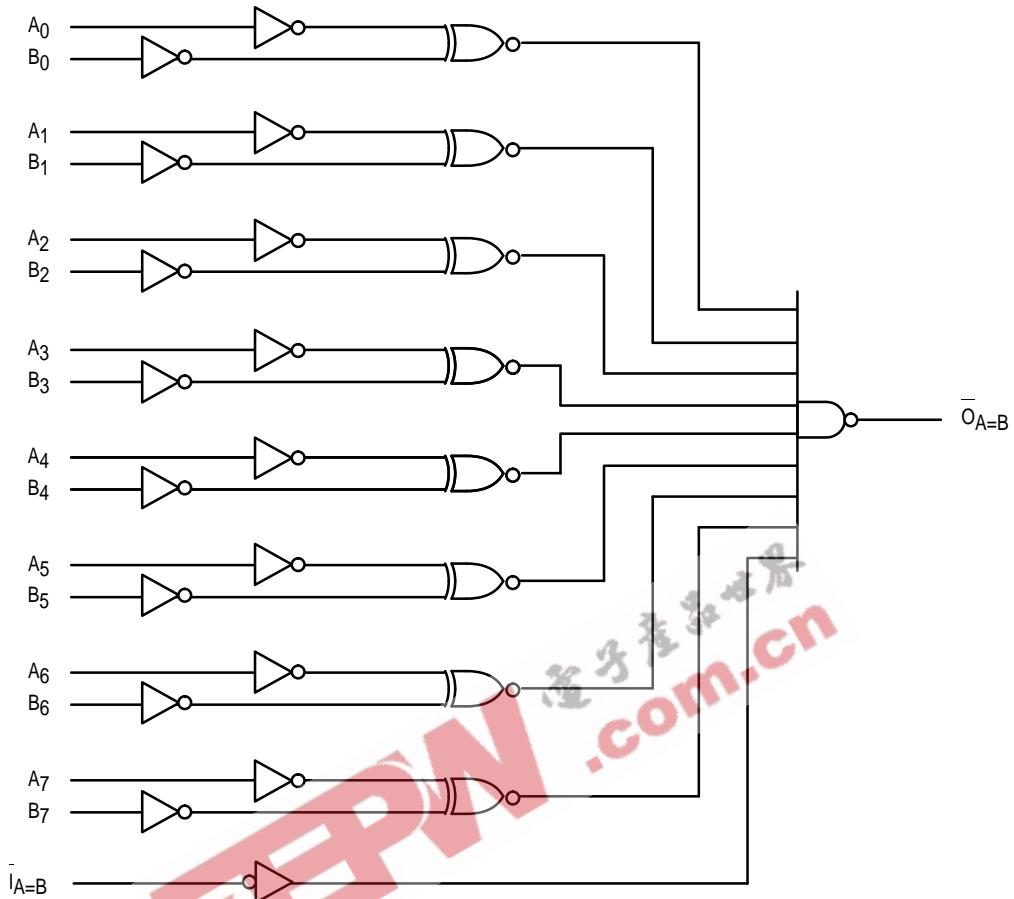
DW SUFFIX
CASE 751D-04
PLASTIC

LOGIC SYMBOL



MC74AC521 MC74ACT521

LOGIC DIAGRAM



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
V _{in}	DC Input Voltage (Referenced to GND)	-0.5 to V _{CC} +0.5	V
V _{out}	DC Output Voltage (Referenced to GND)	-0.5 to V _{CC} +0.5	V
I _{in}	DC Input Current, per Pin	±20	mA
I _{out}	DC Output Sink/Source Current, per Pin	±50	mA
I _{CC}	DC V _{CC} or GND Current per Output Pin	±50	mA
T _{stg}	Storage Temperature	-65 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

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RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Typ	Max	Unit
V _{CC}	Supply Voltage	'AC	2.0	5.0	6.0	V
		'ACT	4.5	5.0	5.5	
V _{in} , V _{out}	DC Input Voltage, Output Voltage (Ref. to GND)		0		V _{CC}	V
t _r , t _f	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V _{CC} @ 3.0 V		150		ns/V
		V _{CC} @ 4.5 V		40		
		V _{CC} @ 5.5 V		25		
t _r , t _f	Input Rise and Fall Time (Note 2) 'ACT Devices except Schmitt Inputs	V _{CC} @ 4.5 V		10		ns/V
		V _{CC} @ 5.5 V		8.0		
T _J	Junction Temperature (PDIP)				140	°C
T _A	Operating Ambient Temperature Range		-40	25	85	°C
I _{OH}	Output Current — High				-24	mA
I _{OL}	Output Current — Low				24	mA

1. V_{in} from 30% to 70% V_{CC}; see individual Data Sheets for devices that differ from the typical input rise and fall times.

2. V_{in} from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

DC CHARACTERISTICS

Symbol	Parameter	V _{CC} (V)	74AC		Unit	Conditions		
			T _A = +25°C					
			Typ	Guaranteed Limits				
V _{IH}	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V		
V _{IL}	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	V	V _{OUT} = 0.1 V or V _{CC} - 0.1 V		
V _{OH}	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	V	I _{OUT} = -50 μA		
		3.0 4.5 5.5		2.56 3.86 4.86	V	*V _{IN} = V _{IL} or V _{IH} I _{OH} -12 mA -24 mA -24 mA		
V _{OL}	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	V	I _{OUT} = 50 μA		
		3.0 4.5 5.5		0.36 0.36 0.36	V	*V _{IN} = V _{IL} or V _{IH} I _{OL} 12 mA 24 mA 24 mA		
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	μA	V _I = V _{CC} , GND		
I _{OLD}	†Minimum Dynamic Output Current	5.5		75	mA	V _{OLD} = 1.65 V Max		
I _{OHD}		5.5		-75	mA	V _{OHD} = 3.85 V Min		
I _{CC}	Maximum Quiescent Supply Current	5.5		8.0	80	μA		
						V _{IN} = V _{CC} or GND		

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

Note: I_{IN} and I_{CC} @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V_{CC}.

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AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

Symbol	Parameter	V _{CC} * (V)	74AC			74AC		Unit	Fig. No.		
			T _A = +25°C C _L = 50 pF			T _A = -40°C to +85°C C _L = 50 pF					
			Min	Typ	Max	Min	Max				
t _{PLH}	Propagation Delay A _n or B _n to O _{A=B}	3.3 5.0	3.5 2.5		11 8.0	3.0 2.0	12 9.0	ns	3-6		
t _{PHL}	Propagation Delay A _n or B _n to O _{A=B}	3.3 5.0	4.5 3.0		11.5 8.5	3.5 2.5	12.5 9.0	ns	3-6		
t _{PLH}	Propagation Delay I _{A=B} to O _{A=B}	3.3 5.0	3.0 2.5		8.0 6.0	2.5 2.0	9.0 7.0	ns	3-6		
t _{PHL}	Propagation Delay I _{A=B} to O _{A=B}	3.3 5.0	3.0 2.0		8.0 6.0	2.5 2.0	9.0 7.0	ns	3-6		

* Voltage Range 3.3 V is 3.3 V ±0.3 V.

Voltage Range 5.0 V is 5.0 V ±0.5 V.

DC CHARACTERISTICS

Symbol	Parameter	V _{CC} (V)	74ACT		T _A = +25°C -40°C to +85°C	Unit	Conditions			
			Guaranteed Limits							
			Typ	Guaranteed Limits						
V _{IH}	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V			
V _{IL}	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V			
V _{OH}	Minimum High Level Output Voltage	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V	I _{OUT} = -50 μA			
		4.5 5.5		3.86 4.86	3.76 4.76	V	*V _{IN} = V _{IL} or V _{IH} –24 mA I _{OH} –24 mA			
V _{OL}	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V	I _{OUT} = 50 μA			
		4.5 5.5		0.36 0.36	0.44 0.44	V	*V _{IN} = V _{IL} or V _{IH} 24 mA I _{OL} 24 mA			
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	±1.0	μA	V _I = V _{CC} , GND			
ΔI _{CCT}	Additional Max. I _{CC} /Input	5.5	0.6		1.5	mA	V _I = V _{CC} – 2.1 V			
I _{OLD}	†Minimum Dynamic Output Current	5.5			75	mA	V _{OLD} = 1.65 V Max			
		5.5			-75	mA	V _{OHD} = 3.85 V Min			
I _{CC}	Maximum Quiescent Supply Current	5.5		8.0	80	μA	V _{IN} = V _{CC} or GND			

* All outputs loaded; thresholds on input associated with output under test.

† Maximum test duration 2.0 ms, one output loaded at a time.

MC74AC521 MC74ACT521

AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

Symbol	Parameter	V_{CC}^* (V)	74ACT			74ACT		Unit	Fig. No.		
			$T_A = +25^\circ C$ $C_L = 50 \text{ pF}$			$T_A = -40^\circ C$ $\text{to } +85^\circ C$ $C_L = 50 \text{ pF}$					
			Min	Typ	Max	Min	Max				
t_{PLH}	Propagation Delay A_n or B_n to $O_{A=B}$	5.0	3.0		9.0	2.5	9.5	ns	3-6		
t_{PHL}	Propagation Delay A_n or B_n to $O_{A=B}$	5.0	3.0		10	2.5	11	ns	3-6		
t_{PLH}	Propagation Delay $I_{A=B}$ to $O_{A=B}$	5.0	2.0		6.5	2.0	7.0	ns	3-6		
t_{PHL}	Propagation Delay $I_{A=B}$ to $O_{A=B}$	5.0	2.5		7.5	2.0	8.0	ns	3-6		

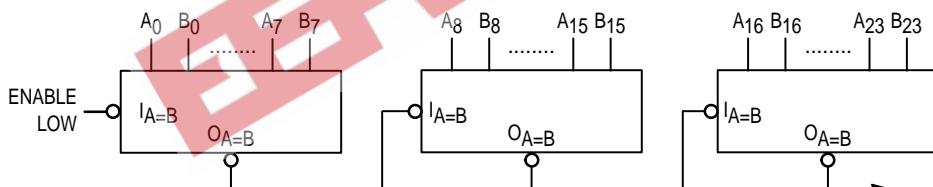
* Voltage Range 5.0 V is $5.0 \text{ V} \pm 0.5 \text{ V}$.

CAPACITANCE

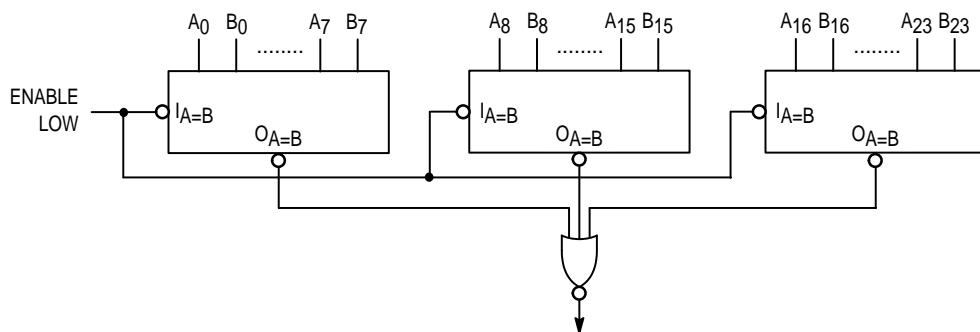
Symbol	Parameter	Value Typ	Unit	Test Conditions
C_{IN}	Input Capacitance	4.5	pF	$V_{CC} = 5.0 \text{ V}$
C_{PD}	Power Dissipation Capacitance	40	pF	$V_{CC} = 5.0 \text{ V}$

APPLICATIONS

RIPPLE EXPANSION



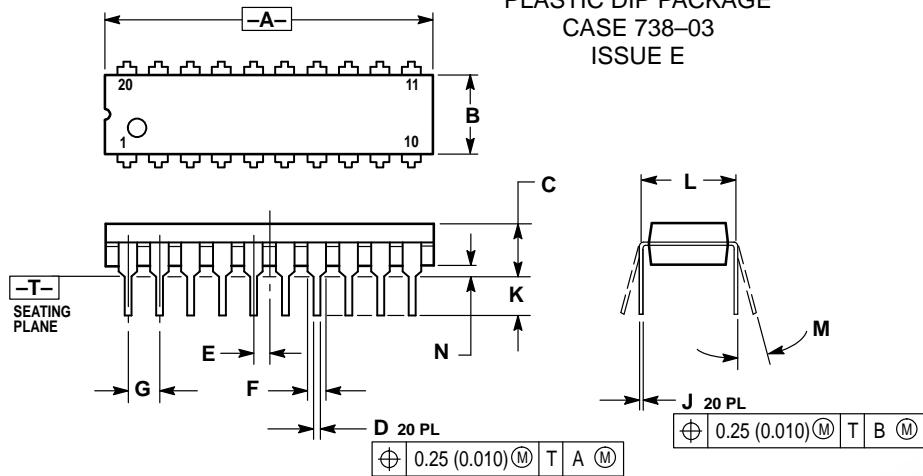
PARALLEL EXPANSION



MC74AC521 MC74ACT521

OUTLINE DIMENSIONS

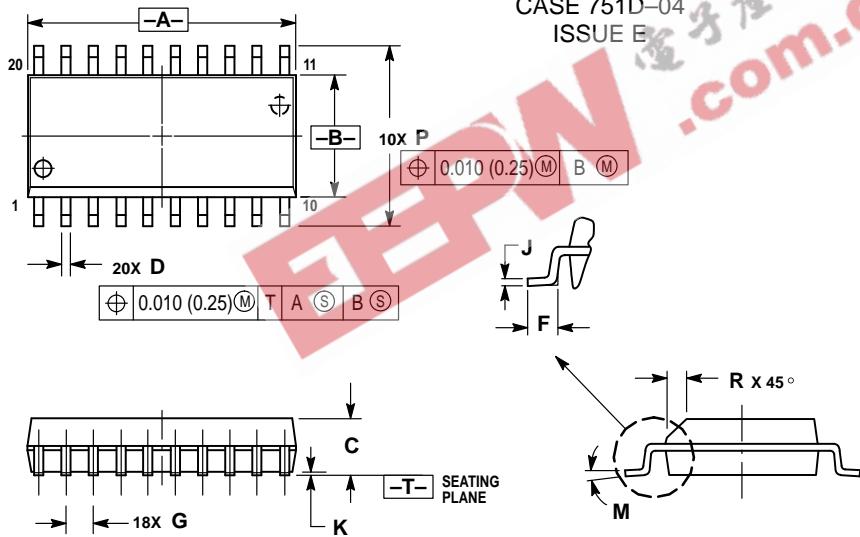
N SUFFIX
PLASTIC DIP PACKAGE
CASE 738-03
ISSUE E



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.010	1.070	25.66	27.17
B	0.240	0.260	6.10	6.60
C	0.150	0.180	3.81	4.57
D	0.015	0.022	0.39	0.55
E	0.050 BSC		1.27	
F	0.050	0.070	1.27	1.77
G	0.100 BSC		2.54	
J	0.008	0.015	0.21	0.38
K	0.110	0.140	2.80	3.55
L	0.300 BSC		7.62	
M	0°	15°	0°	15°
N	0.020	0.040	0.51	1.01

DW SUFFIX
PLASTIC SOIC PACKAGE
CASE 751D-04
ISSUE E



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.150 (0.006) PER SIDE.
 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 (0.005) TOTAL IN EXCESS OF D DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	12.65	12.95	0.499	0.510
B	7.40	7.60	0.292	0.299
C	2.35	2.65	0.093	0.104
D	0.35	0.49	0.014	0.019
F	0.50	0.90	0.020	0.035
G	1.27 BSC		0.050 BSC	
J	0.25	0.32	0.010	0.012
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	10.05	10.55	0.395	0.415
R	0.25	0.75	0.010	0.029

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MC74AC521/D

