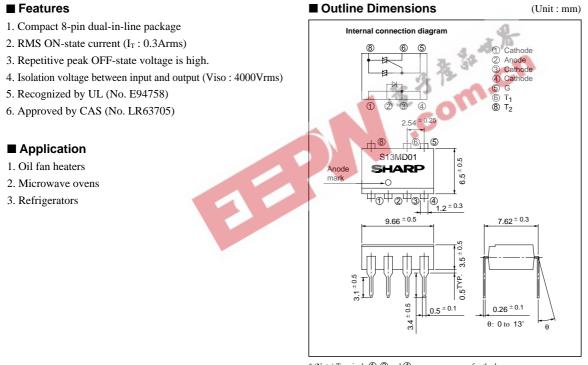
# S13MD01

# 8-pin DIP Type SSR for Low Power Control

S13MD01



\* (Note) Terminals ①, ③ and ④ are common ones of cathode. To radiate the heat, solder all of the lead pins on the pattern of PWB.

Absolute Maximum Ratings					
	Parameter	Symbol	Rating	Unit	
Innut	Forward current	$I_F$	50	mA	
Input	Reverse voltage	V <sub>R</sub>	6	V	
	RMS ON-state current	IT	0.3	Arms	
Output	*1 Peak one cycle surge current	Isurge	3	Α	
	Repetitive peak OFF-state voltage	V <sub>DRM</sub>	400	V	
	*2 Isolation voltage		4 000	Vrms	
	Operating temperature		- 25 to +80	°C	
	Storage temperature		- 40 to +125	°C	
	*3 Soldering temperature		260	°C	

\*1 50Hz sine wave \*2 40 to 60% RH, AC for 1 minute, f=60Hz \*3 For 10 seconds

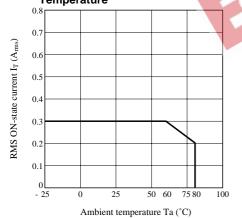
" In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that occur in equipment using any of SHARP's devices, shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest version of the device specification sheets before using any SHARP's device."

#### SHARP

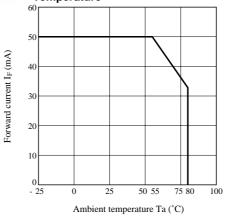
# S13MD01

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	VF	$I_F = 20 m A$	-	1.2	1.4	V
	Reverse current	IR	$V_R = 3V$	-	-	10	μA
Output	Repetitive peak OFF-state current	IDRM	V <sub>DRM</sub> = Rated	-	-	100	μA
	ON-state voltage	VT	$I_T = 0.3A$	-	-	3.0	V
	Holding current	IH	$V_D = 6V$	-	-	25	mA
	Critical rate of rise of OFF-state voltage	dv/dt	$V_{DRM} = (1/\sqrt{2}) \bullet Rated$	100		<u>r</u> n	V/µs
Transfer characteristics	Minimum trigger current	IFT	$V_D = 6V, R_L = 100\Omega$	- 2	- 54	10	mA
	Insulation resistance	RISO	DC500V, 40 to 60% RH	5 x 10 <sup>10</sup>	1 x 10 <sup>11</sup>	6	Ω
	Turn-on time	ton	$V_D = 6V, R_L = 100\Omega$ $I_F = 20mA$	27	5	100	μs
		-511	I <sub>F</sub> = 20mA	C		- 30	<u> </u>

#### Fig. 1 RMS ON-state Current vs. Ambient Temperature

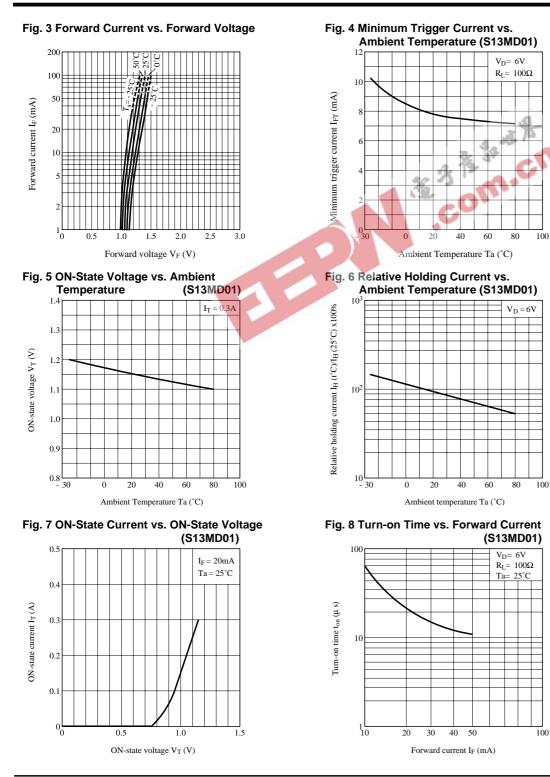


# Fig. 2 Forward Current vs. Ambient Temperature



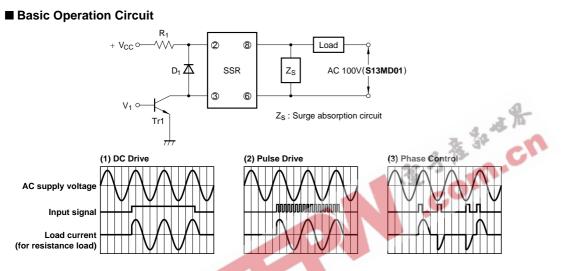
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## S13MD01



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## S13MD01



- Notes (1) If large amount of surge is loaded onto  $V_{cc}$  or the driver circuit, add a diode  $D_1$  between terminals 2 and 3 to prevent reverse bias from being applied to the infrared LED.
  - (2) Be sure to install a surge absorption circuit. An appropriate circuit must be chosen according to the load (for CR, choose its constant). This must be carefully done especially for an inductive load.
  - (3) For phase control, adjust such that the load current immediately after the input signal is applied will be more than 30mA.

#### Precautions for Use

- All pins must be soldered since they are also used as heat sinks (heat radiation fins). In designing, consider the heat radiation from the mounted SSR.
- (2) For higher radiation efficiency that allows wider thermal margin, secure a wider round pattern for Pin No. 8 when designing mounting pattern. The rounded part of Pin No. 5 (gate) must be as small as possible. Pulling the gate pattern around increases the change of being affected by external noise.
- As for other general cautions, refer to the chapter "Precautions for Use" (Page 78 to 93).