

SELECTABLE FREQUENCY SYNTHESIZER

SURFACE MOUNT MODEL: FCPL1114-5

OPTIMIZED LOOP PARAMETERS

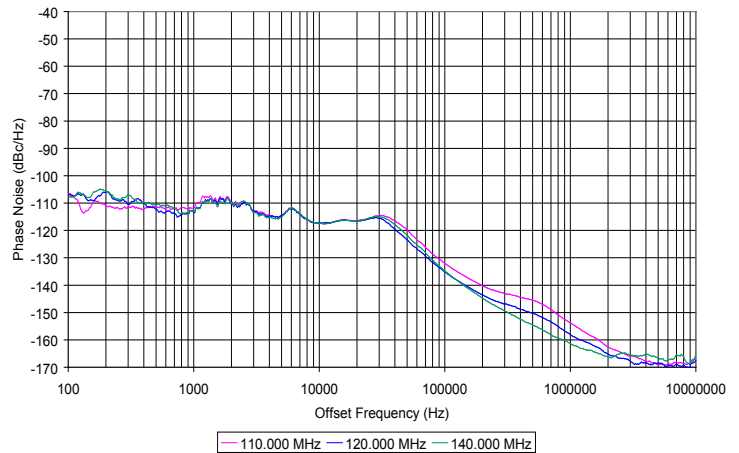
112.5 - 140 MHz

FEATURES:

- ▶ Exceptionally Low Phase Noise
- ▶ No Programming Required
- ▶ Lead Free - RoHS Compliant
- ▶ Patent Pending REL-PRO® Technology
- ▶ Small Size, Surface Mount
- ▶ Buffered Output



Phase Noise

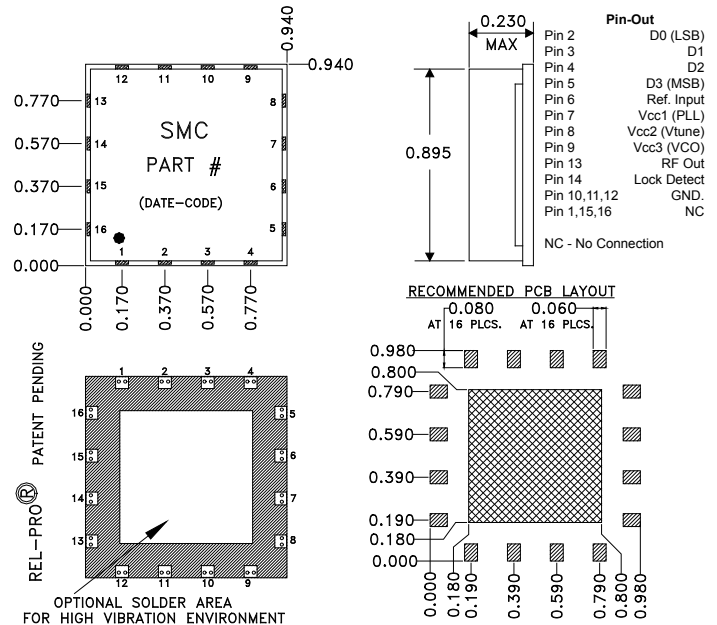


SPECIFICATIONS (Rev. C 08/24/07)

Frequency	112.5 - 140 MHz		
Reference Input Frequency	10 MHz		
Reference Input Voltage	1.0 V p-p to 3.3 V p-p		
Bias Voltage	Vcc	V (Vdc)	I (mA Max.)
	Vcc1 (PLL)	+5	15
	Vcc3 (VCO)	+5	50
	Vcc2 (Vtune)	+5	15
Output Power	+7 dBm (Min.)		
Spurious Suppression	75 dB (Typ.)		
Harmonic Suppression	10 dB (Typ.)		
Power Up Time	90 mSec (Max.) *		
Output Impedance	50 Ohms (Nom.)		
Typical Phase Noise	Offset	Phase Noise	
	@ 1 kHz	-110 dBc/Hz	
	@ 10 kHz	-115 dBc/Hz	
	@ 100 kHz	-132 dBc/Hz	
Operating Temperature Range	-40 to +85 °C		

* From stable power on to stable lock detect

Package # 273LF



PACKAGE MOUNTING: SEE APPLICATION NOTE AN7200

Binary Frequency Selection Table

D3	D2	D1	D0	Freq. (MHz)
1	0	0	0	112.5
1	0	0	1	120
1	0	1	0	140
1	1	1	1	125

This product is manufactured under one or more of the following patents: Approved: 5,160,810; 5,122,621; 5,390,346; 5,416,446; 5,650,754; 5,805,431; 6,302,623; 6,806,573; 7,088,189; 7,180,381; 7,185,591; 7,262,670; 7,265,640; 7,292,113
 Pending: 6,648,307; 6,650,137; 6,656,799; 6,657,937; 6,653,672; 6,626,348; 6,656,417; 6,658,083; 6,665,823; 6,660,579; EV3128803; 11,029,768; 6,662,348; 6,671,031; 10,937,325; 6,673,691; 6,673,073



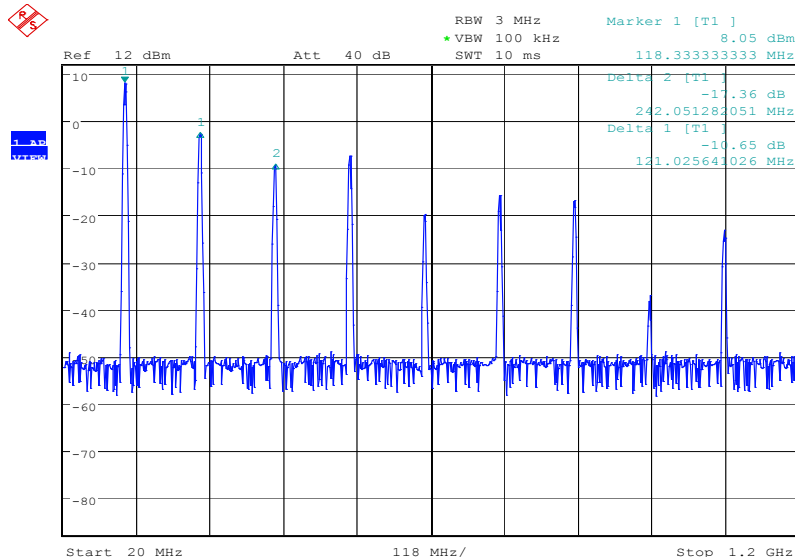
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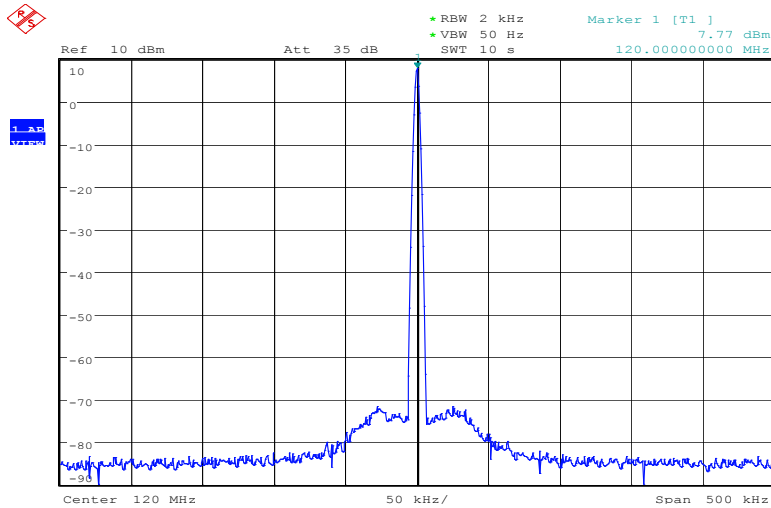
OPTIMIZED LOOP PARAMETERS

112.5 - 140 MHz

HARMONIC SUPPRESSION



REFERENCE SIDEBAND SUPPRESSION



PERFORMANCE PLOTS

This product is manufactured under one or more of the following patents: Approved: 5,160,810; 5,122,621; 5,390,346; 5,416,446; 5,630,754; 5,805,431; 6,305,623; 6,856,573; 7,088,189; 7,180,381; 7,185,591; 7,282,670; 7,285,642; 7,292,113
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 201 McLean Boulevard • Paterson, New Jersey 07604 • Tel: (973) 881-8800 • Fax: (973) 881-8361
 E-Mail: sales@synergymw.com • World Wide Web: http://www.synergymw.com



