

FAST / TTL Logic Triple & Quad Delays 14-Pin DIP & SMD

Part Number Description **FAIXD-XXX X**
 14-Pin Logic Buffered Multi-Line Delays:
 FAI3D, FAI4D
 Delay Per Line in nanoseconds (ns)
 Lead Style: Blank = Auto-Insertable DIP
 G = "Gull Wing" Surface Mount
 J = "J" Bend Surface Mount

Examples: FAI4D-20 = 20ns Quadruple 74F Buffered, DIP
 FAI3D-5G = 5ns Triple 74F Buffered, G-SMD

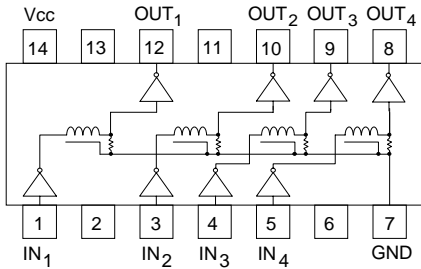
GENERAL: For Operating Specifications and Test Conditions, see Tables I and VI on page 5 of this catalog. Delays specified for the Leading Edge.

Minimum Input Pulse Width 100% of total delay
 Operating Temp. Range 0°C to +70°C
 Temperature Coefficient 800ppm/°C typical
 Supply Current, I_{cc} : FAI3D 45 mA typ., 95 mA max.
 FAI4D 65 mA typ., 130 mA max.

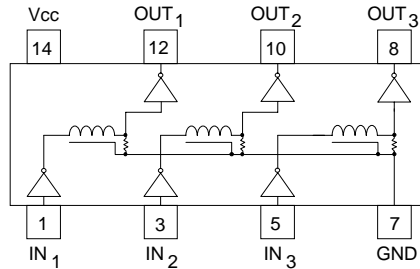
Electrical Specifications at 25°C

| Delay (ns) | FAST Buffered Multi-Line | |
|------------|--------------------------|---------------|
| | Triple P/N | Quadruple P/N |
| 4 ± 1.00 | FAI3D-4 | FAI4D-4 |
| 5 ± 1.00 | FAI3D-5 | FAI4D-5 |
| 6 ± 1.00 | FAI3D-6 | FAI4D-6 |
| 7 ± 1.00 | FAI3D-7 | FAI4D-7 |
| 8 ± 1.00 | FAI3D-8 | FAI4D-8 |
| 10 ± 1.50 | FAI3D-10 | FAI4D-10 |
| 15 ± 2.00 | FAI3D-15 | FAI4D-15 |
| 16 ± 2.00 | FAI3D-16 | FAI4D-16 |
| 20 ± 2.00 | FAI3D-20 | FAI4D-20 |
| 25 ± 2.00 | FAI3D-25 | FAI4D-25 |
| 30 ± 2.00 | FAI3D-30 | FAI4D-30 |
| 50 ± 2.50 | FAI3D-50 | FAI4D-50 |

Quad 14-Pin Schematic



Triple 14-Pin Schematic



Dimensions in Inches (mm)

