

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

FEATURES

- $V_{DSS}=600V$, $I_D=8A$
- Drain-Source ON Resistance :
 $R_{DS(ON)}(\text{Max})=1.05 \Omega$ @ $V_{GS}=10V$
- $Q_g(\text{typ.})=24nC$

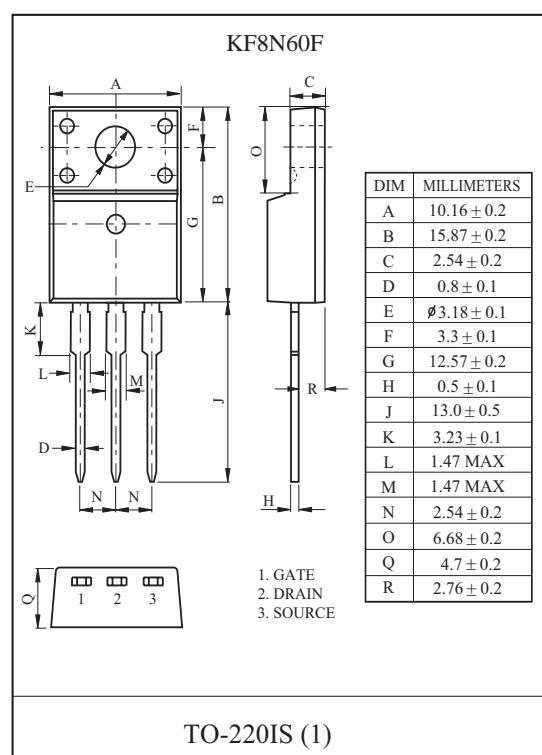
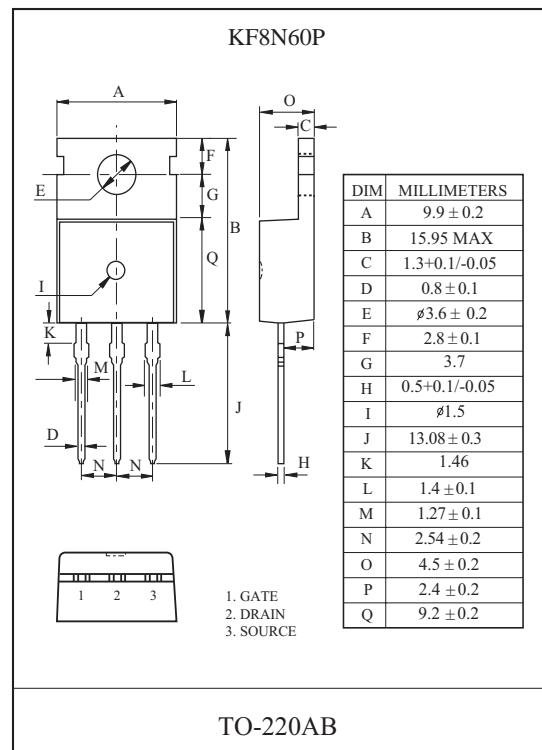
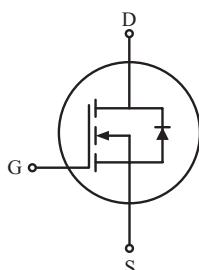
MAXIMUM RATING (Tc=25 °C)

CHARACTERISTIC	SYMBOL	RATING		UNIT
		KF8N60P	KF8N60F	
Drain-Source Voltage	V_{DSS}	600		V
Gate-Source Voltage	V_{GSS}	± 30		V
Drain Current	I_D @ $T_C=25^\circ\text{C}$	8	8*	A
	I_D @ $T_C=100^\circ\text{C}$	5	5*	
	I_{DP} Pulsed (Note1)	20	20*	
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	230		mJ
Repetitive Avalanche Energy (Note 1)	E_{AR}	14.7		mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5		V/ns
Drain Power Dissipation	P_D Tc=25 °C	154	50	W
	P_D Derate above 25 °C	1.23	0.4	W/°C
Maximum Junction Temperature	T_j	150		°C
Storage Temperature Range	T_{stg}	-55 ~ 150		°C

Thermal Characteristics

Thermal Resistance, Junction-to-Case	R_{thJC}	0.81	2.5	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	62.5	°C/W

* : Drain current limited by maximum junction temperature.

PIN CONNECTION

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ELECTRICAL CHARACTERISTICS (Tc=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	600	-	-	V
Breakdown Voltage Temperature Coefficient	ΔBV _{DSS} /ΔT _j	I _D =250μA, Referenced to 25 °C	-	0.65	-	V/°C
Drain Cut-off Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	10	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250μA	2	-	4	V
Gate Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =4A	-	0.85	1.05	Ω
Dynamic						
Total Gate Charge	Q _g	V _{DS} =480V, I _D =8A V _{GS} =10V (Note4,5)	-	24	-	nC
Gate-Source Charge	Q _{gs}		-	5.4	-	
Gate-Drain Charge	Q _{gd}		-	10	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =300V R _L =37.5 Ω R _G =25 Ω (Note4,5)	-	27	-	ns
Turn-on Rise time	t _r		-	32	-	
Turn-off Delay time	t _{d(off)}		-	72	-	
Turn-off Fall time	t _f		-	30	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	865	-	pF
Output Capacitance	C _{oss}		-	111	-	
Reverse Transfer Capacitance	C _{rss}		-	14	-	
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} <V _{th}	-	-	8	A
Pulsed Source Current	I _{SP}		-	-	32	
Diode Forward Voltage	V _{SD}	I _S =8.0A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	I _S =8.0A, V _{GS} =0V, dIs/dt=100A/μs	-	370	-	ns
Reverse Recovery Charge	Q _{rr}		-	3.9	-	μC

Note 1) Repetitvity rating : Pulse width limited by junction temperature.

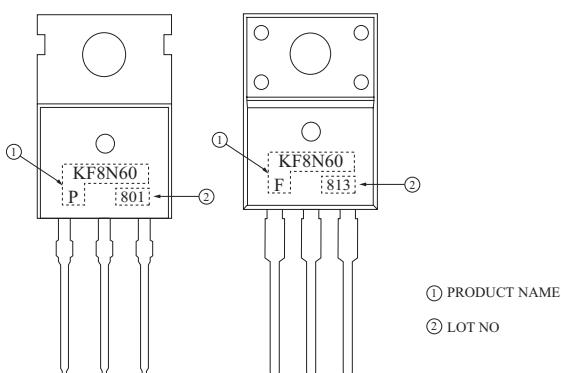
Note 2) L =6.6mH, I_S=8A, V_{DD}=50V, R_G=25 Ω, Starting T_j=25 °C.

Note 3) I_S≤8.0A, dI/dt≤200A/μs, V_{DD}≤BV_{DSS}, Starting T_j=25 °C.

Note 4) Pulse Test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%.

Note 5) Essentially independent of operating temperature.

Marking



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Fig1. I_D - V_{DS}

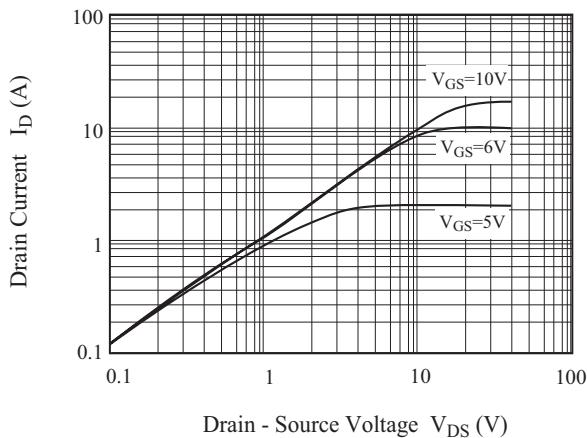


Fig2. I_D - V_{GS}

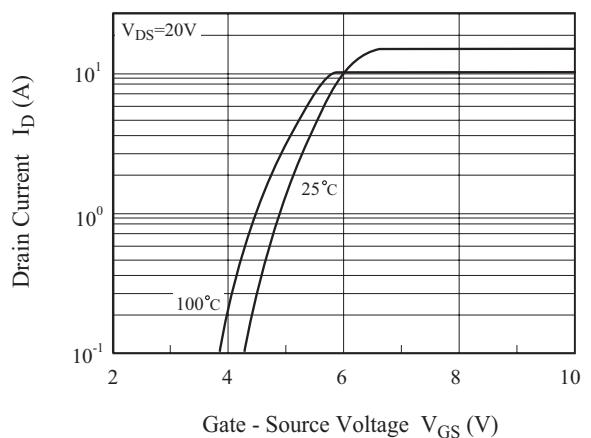


Fig3. BV_{DSS} - T_j

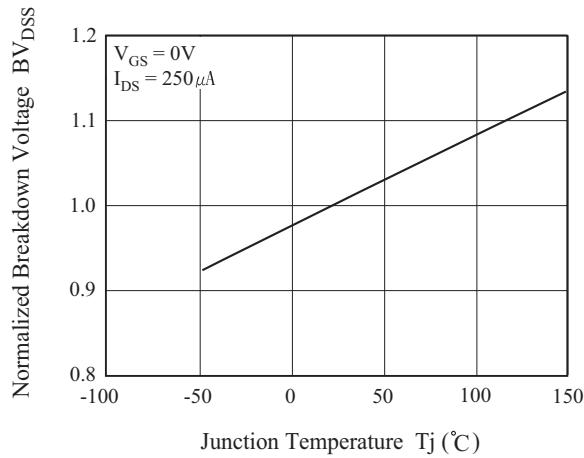


Fig4. $R_{DS(ON)}$ - I_D

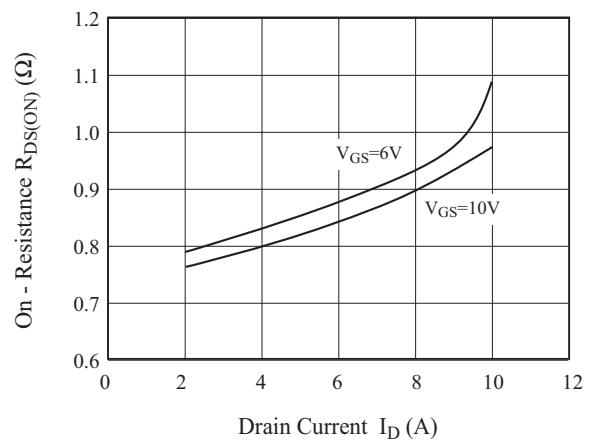


Fig5. I_S - V_{SD}

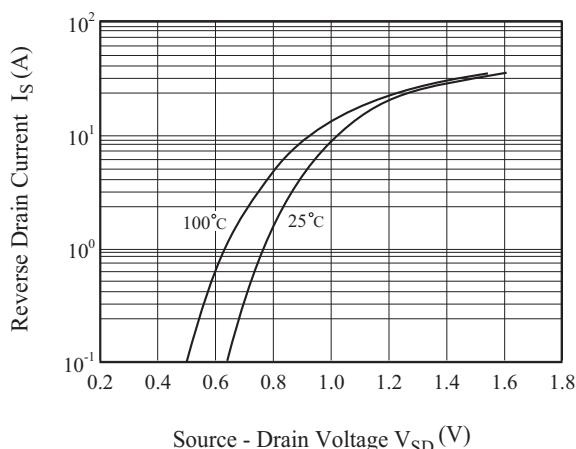
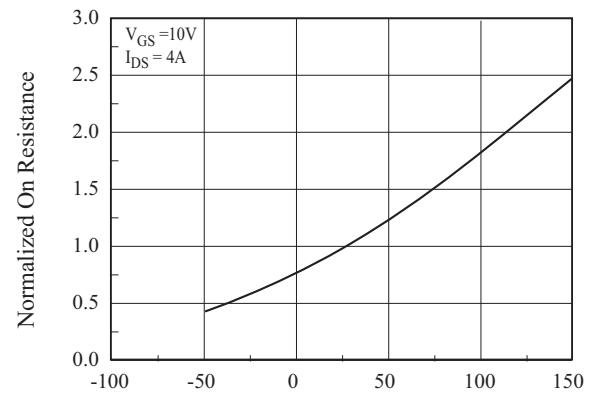


Fig6. $R_{DS(ON)}$ - T_j



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Fig 7. C - V_{DS}

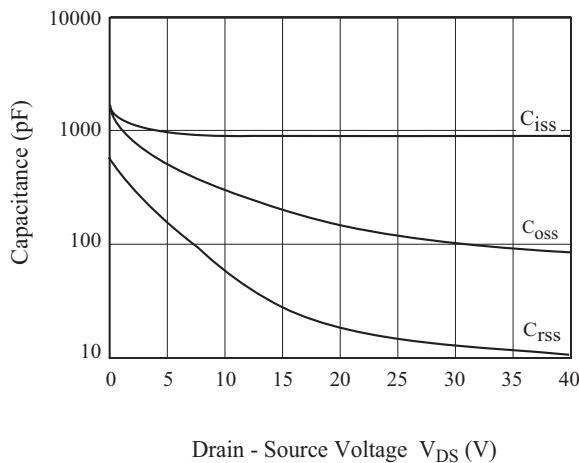


Fig8. Q_g- V_{GS}

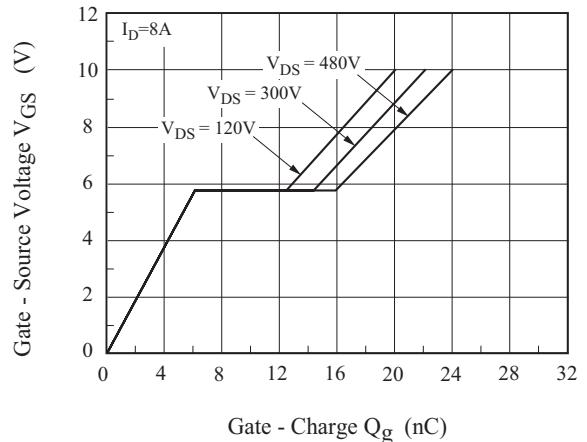


Fig9. Safe Operation Area

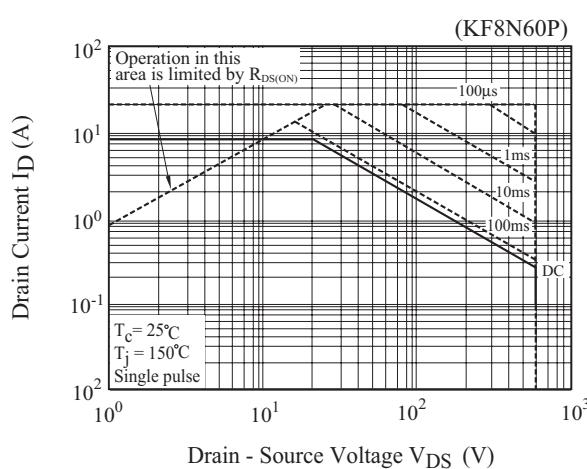


Fig10. Safe Operation Area

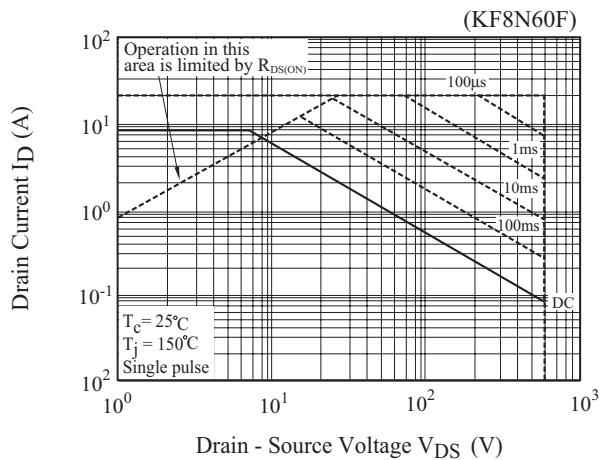
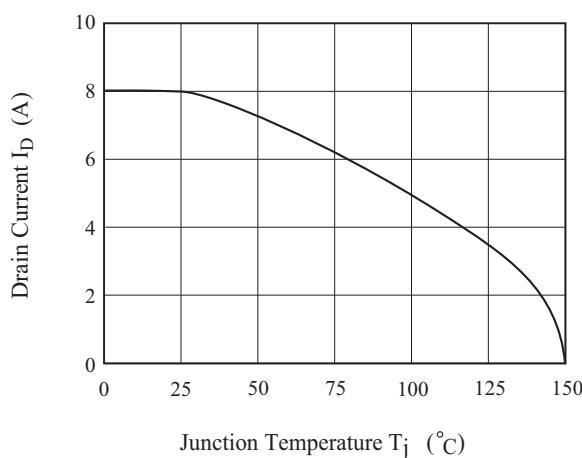


Fig11. I_D - T_j



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Fig12. Transient Thermal Response Curve

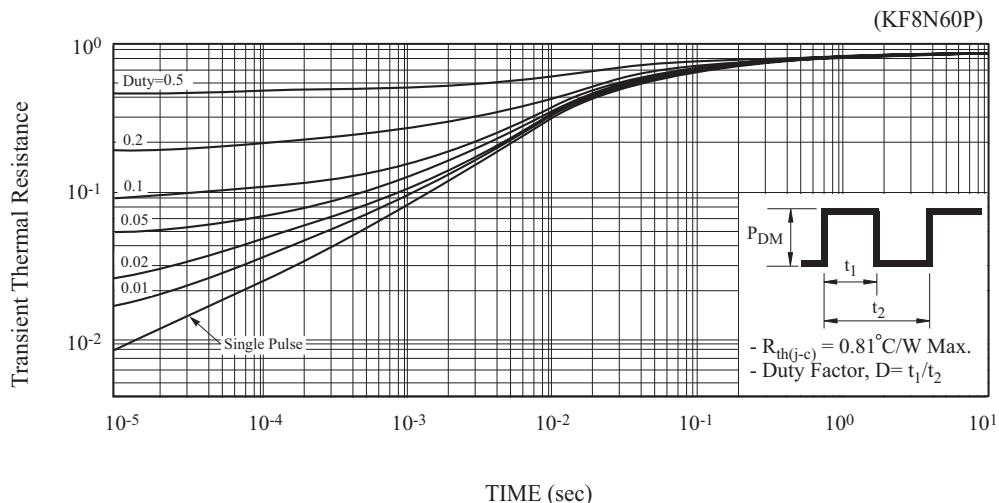
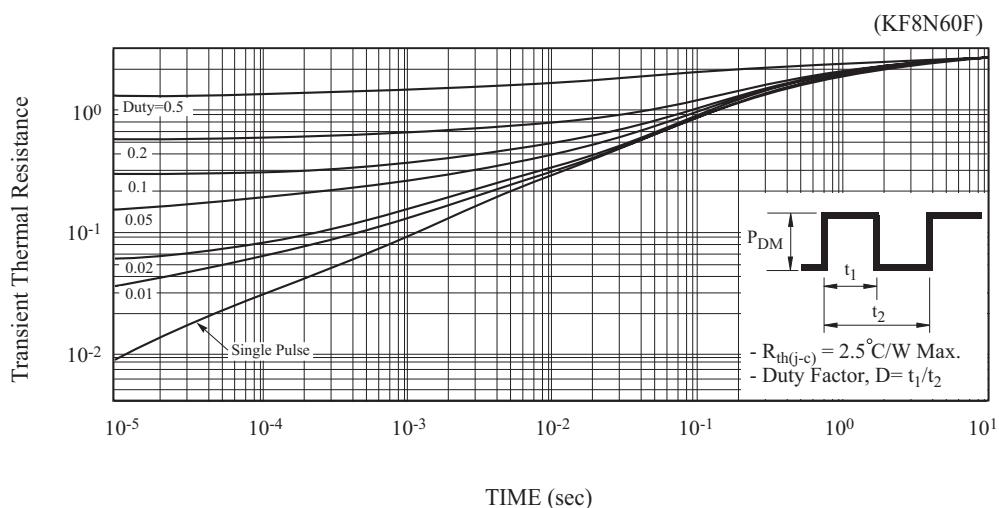


Fig13. Transient Thermal Response Curve



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Fig14. Gate Charge

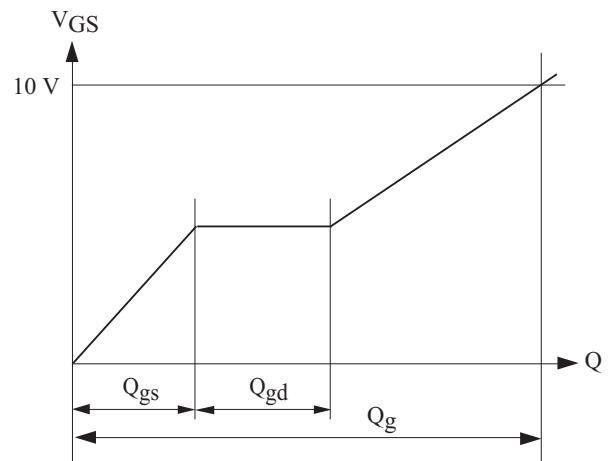
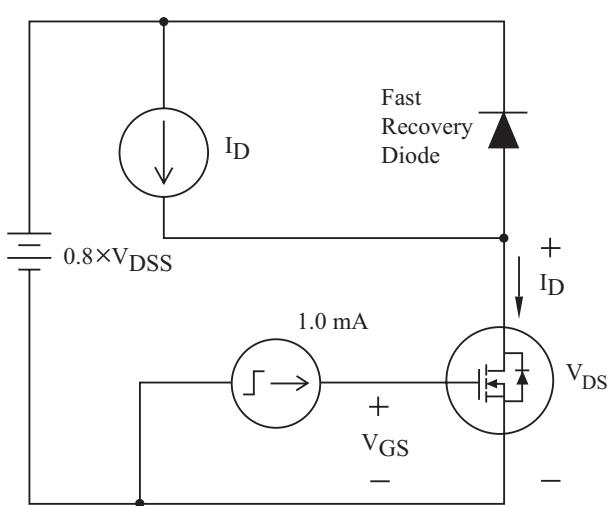
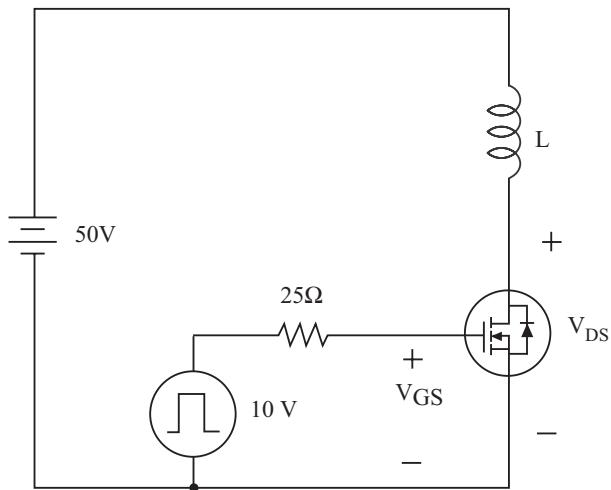


Fig15. Single Pulsed Avalanche Energy



$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

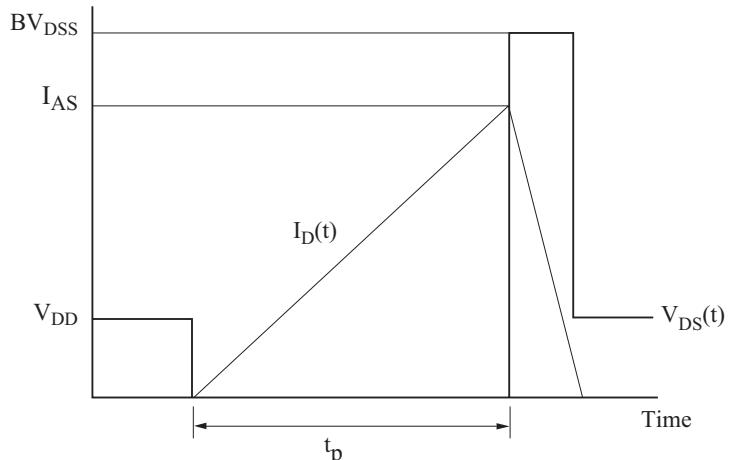
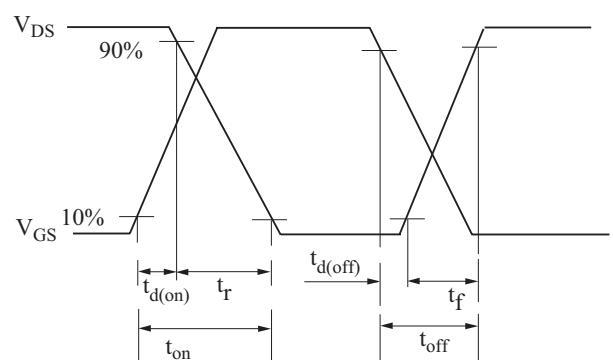
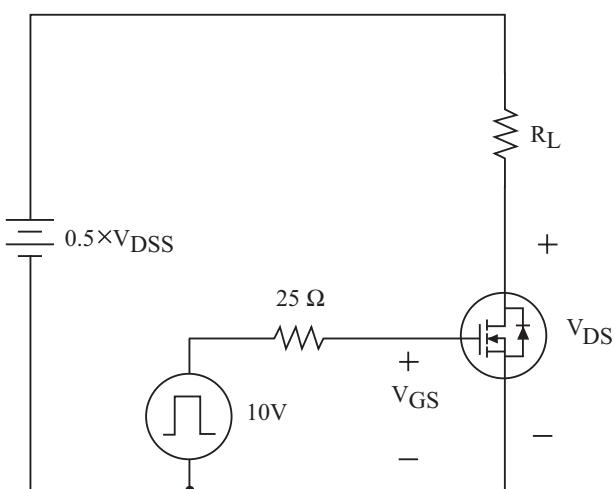


Fig16. Resistive Load Switching



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Fig17. Source - Drain Diode Reverse Recovery and dv /dt

