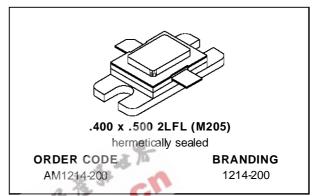


# AM1214-200

# RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

PRELIMINARY DATA

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 200 W MIN. WITH 7.0 dB GAIN



### **DESCRIPTION**

The AM1214-200 device is a high power Class C transistor specifically designed for L-Band Radar pulsed output and driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles and temperatures, and wiil tolerate severe mismatch and over-drive conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

AM1214-200 is supplied in the BIGPAC™ hermetic metal/ceramic package with internal input/output matching structures.

# PIN CONNECTION 1. Collector 3. Emitter 2. Base 4. Base

### **ABSOLUTE MAXIMUM RATINGS** (T<sub>case</sub> = 25°C)

Symbol	Parameter	Value	Unit
P <sub>DISS</sub>	Power Dissipation* (T <sub>C</sub> ≤ 100°C)	575	W
Ic	Device Current*	16	А
V <sub>CC</sub>	Collector-Supply Voltage*	40	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T <sub>STG</sub>	Storage Temperature	- 65 to +200	°C

### THERMAL DATA

R <sub>TH(j-c)</sub>	Junction-Case Thermal Resistance*	0.26	°C/W

<sup>\*</sup>Applies only to rated RF amplifier operation

September 1992

### AM1214-200

### **ELECTRICAL SPECIFICATIONS** (T<sub>case</sub> = 25°C)

### **STATIC**

Symbol	Test Conditions	Value			Unit		
Symbol	rest Conditions		Min.	Тур.	Max.	Oiiit	
ВУсво	I <sub>C</sub> = 50mA	$I_E = 0mA$		70	_		V
BV <sub>EBO</sub>	I <sub>E</sub> = 30mA	$I_C = 0mA$		3.0	_		V
BVces	IC = 50mA	$V_{BE} = 0V$		70	_	ı	V
I <sub>CES</sub>	$V_{BE} = 0V$	$V_{CE} = 40V$		_	_	30	mA
h <sub>FE</sub>	V <sub>CE</sub> = 5V	$I_C = 500 \text{mA}$		10	_	_	_

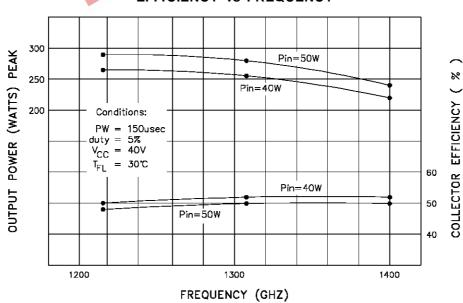
### **DYNAMIC**

Cumbal	Sumbal Tot Canditions		Value		
Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Роит	$f = 1215 - 1400MHz$ $P_{IN} = 40W$ $V_{CC} = 40V$	200	_	_	W
ης	$f = 1215 - 1400MHz$ $P_{IN} = 40W$ $V_{CC} = 40V$	45	_	_	%
G <sub>P</sub>	f = 1215 — 1400MHz P <sub>IN</sub> = 40W V <sub>CC</sub> = 40V	7.0	_	_	dB

Note: Pulse Width =  $150\mu$ Sec Duty Cycle = 5%

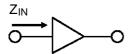
### TYPICAL PERFORMANCE

# POWER OUTPUT & COLLECTOR EFFICIENCY vs FREQUENCY

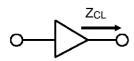


### **IMPEDANCE DATA**

# TYPICAL INPUT IMPEDANCE



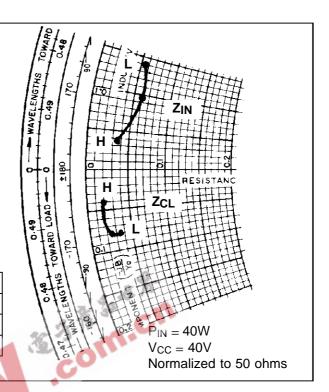
### TYPICAL COLLECTOR LOAD IMPEDANCE



FREQ.	Z <sub>IN</sub> (Ω)	Z <sub>CL</sub> (Ω)		
L = 1215 MHz	2.7 + j 7.0	1.7 – j 4.0		
M = 1300 MHz	3.0 + j 4.8	1.4 – j 4.0		
H = 1400 MHz	1.8 + j 1.7	1.0 – j 2.0		

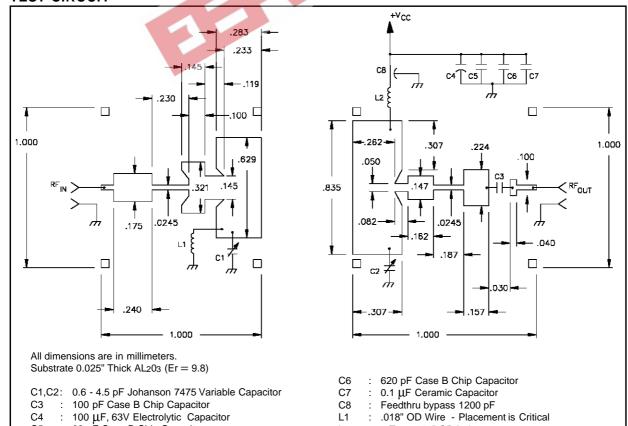
100  $\mu$ F, 63V Electrolytic Capacitor

68 pF Case B Chip Capacitor



### **TEST CIRCUIT**

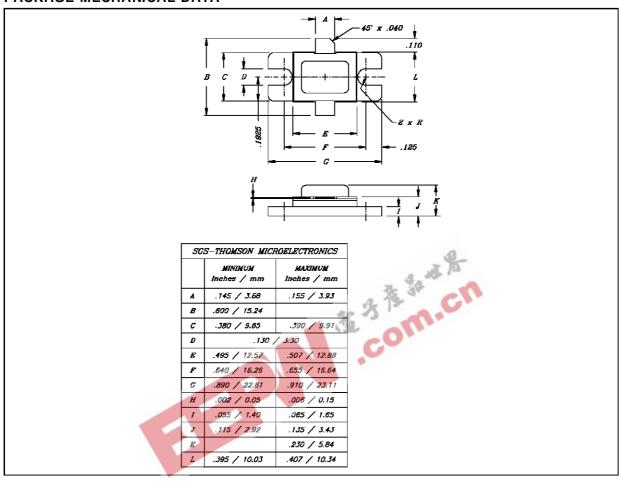
C5



L1

4 Turn .018" OD Inductor

### PACKAGE MECHANICAL DATA



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