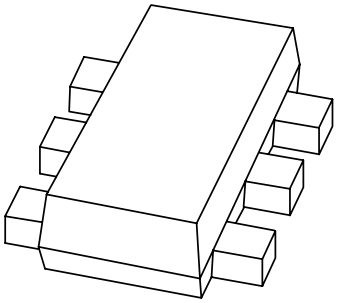
DATA SHEET



BAT74V
Schottky barrier double diode

Product specification

2002 Sep 02

Philips
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Schottky barrier double diode BAT74V

FEATURES

- Low forward voltage
- Low capacitance
- Ultra small SMD plastic package
- Flat leads: excellent coplanarity and improved thermal behaviour.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Inverse polarity protection.

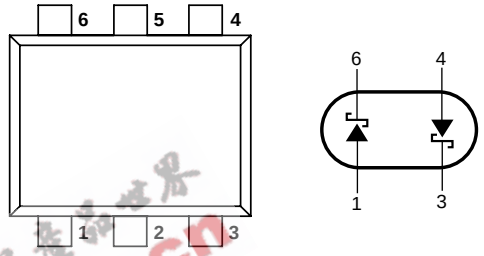
DESCRIPTION

Planar Schottky barrier double diode with an integrated guard ring for stress protection.

Two separate dies encapsulated in a SOT666 ultra small SMD plastic package.

PINNING

PIN	DESCRIPTION
1	anode 1
2	not connected
3	cathode 2
4	anode 2
5	not connected
6	cathode 1



Marking code: 74.

Fig.1 Simplified outline (SOT666) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		–	30	V
I_F	continuous forward current		–	200	mA
I_{FRM}	repetitive peak forward current	$t_p \leq 1$ s; $\delta \leq 0.5$	–	300	mA
I_{FSM}	non-repetitive peak forward current	$t_p < 10$ ms		600	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	–	230	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	125	°C
T_{amb}	operating ambient temperature		–65	+125	°C

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V_F	continuous forward voltage	I_F = 0.1 mA	240	mV
		I_F = 1 mA	320	mV
		I_F = 10 mA	400	mV
		I_F = 30 mA	500	mV
		I_F = 100 mA; note 1; see Fig.2	800	mV
I_R	reverse current	V_R = 25 V; note 1; see Fig.3	2	µA
C_d	diode capacitance	V_R = 1 V; f = 1 MHz; see Fig.4	10	pF

Note

1. Pulse test: t_p = 300 µs; δ = 0.02.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	416	K/W

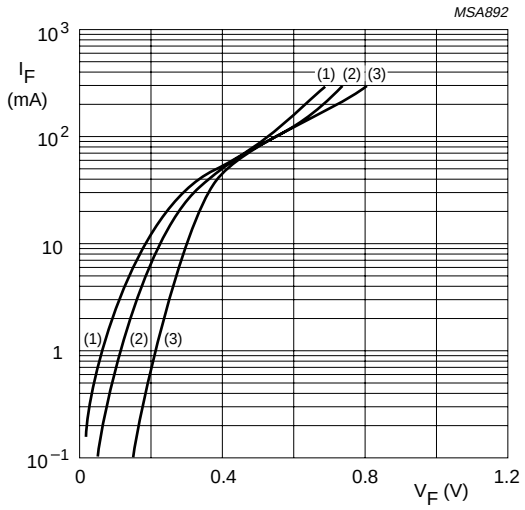
Note

1. Refer to SOT666 standard mounting conditions.

Soldering

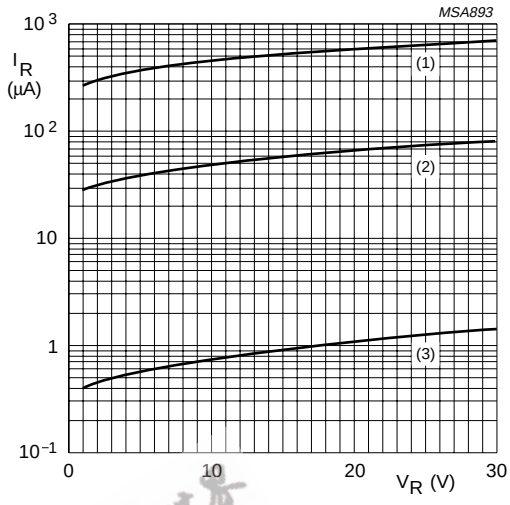
The only recommended soldering method is reflow soldering.

GRAPHICAL DATA



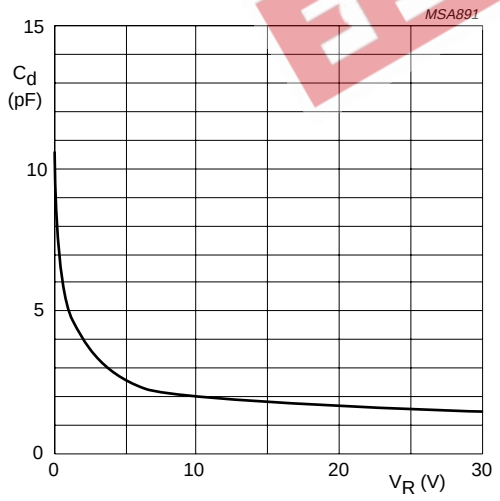
(1) T_{amb} = 125 °C.
(2) T_{amb} = 85 °C.
(3) T_{amb} = 25 °C.

Fig.2 Forward current as a function of forward voltage; typical values.



(1) T_{amb} = 125 °C.
(2) T_{amb} = 85 °C.
(3) T_{amb} = 25 °C.

Fig.3 Reverse current as a function of reverse voltage; typical values.

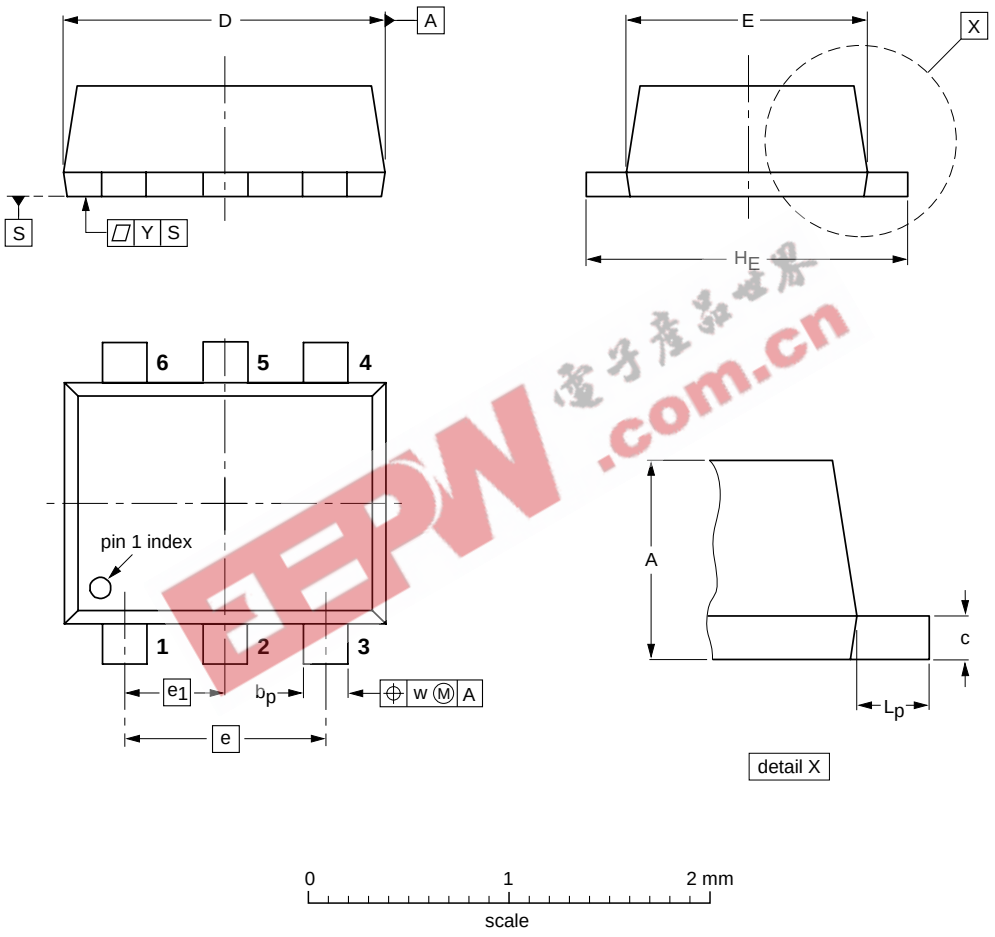


f = 1 MHz; T_{amb} = 25 °C.

Fig.4 Diode capacitance as a function of reverse voltage; typical values.

PACKAGE OUTLINE

Plastic surface mounted package; 6 leads **SOT666**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_p	c	D	E	e	e_1	H_E	L_p	w	y
mm	0.6 0.5	0.27 0.17	0.18 0.08	1.7 1.5	1.3 1.1	1.0	0.5	1.7 1.5	0.3 0.1	0.1	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT666						~~01-01-04~~ 01-08-27

DATA SHEET STATUS

DATA SHEET STATUS[1]	PRODUCT STATUS[2]	DEFINITIONS
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