BOURNS®

- Designed for Complementary Use with BD645, BD647, BD649 and BD651
- 62.5 W at 25°C Case Temperature
- 8 A Continuous Collector Current
- Minimum h_{FF} of 750 at 3V, 3 A

Pin 2 is in electrical contact with the mounting base.

MDTRACA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT	
	BD646		-80	1	
Collector-base voltage (I _E = 0)	BD648	V	-100	V	
	BD650	V _{CBO}	-120		
	BD652	5.0	-140		
Collector-emitter voltage (I _B = 0)	BD646	-10	-60		
	BD648	V	-80	V	
	BD650	V _{CEO}	-100		
	BD652		-120		
Emitter-base voltage		V _{EBO}	-5	V	
Continuous collector current		I _C	-8	Α	
Peak collector current (see Note 1)		I _{CM}	-12	Α	
Continuous base current			-0.3	Α	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			62.5	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			2	W	
Unclamped inductive load energy (see Note 4)			50	mJ	
Operating junction temperature range		Tj	-65 to +150	°C	
Storage temperature range			-65 to +150	°C	
Lead temperature 3.2 mm from case for 10 seconds		TL	260	°C	

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$.

- 2. Derate linearly to 150°C case temperature at the rate of 0.4 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = -5 mA, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = -20 V.



electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER TEST CONDITIONS		MIN	TYP	MAX	UNIT				
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C = -30 mA	I _B = 0	(see Note 5)	BD646 BD648 BD650 BD652	-60 -80 -100 -120			٧
I _{CEO}	Collector-emitter cut-off current	$V_{CE} = -30 \text{ V}$ $V_{CE} = -40 \text{ V}$ $V_{CE} = -50 \text{ V}$ $V_{CE} = -60 \text{ V}$	$I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$		BD646 BD648 BD650 BD652			-0.5 -0.5 -0.5 -0.5	mA
Ісво	Collector cut-off current	$V_{CB} = -100 \text{ V}$ $V_{CB} = -120 \text{ V}$ $V_{CB} = -40 \text{ V}$ $V_{CB} = -50 \text{ V}$ $V_{CB} = -60 \text{ V}$	$I_{E} = 0$	$T_{C} = 150^{\circ}C$ $T_{C} = 150^{\circ}C$ $T_{C} = 150^{\circ}C$ $T_{C} = 150^{\circ}C$	BD646 BD648 BD650 BD652 BD646 BD648 BD650 BD652			-0.2 -0.2 -0.2 -0.2 -2.0 -2.0 -2.0 -2.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} = -5 V	I _C = 0	(see Notes 5 and	16)			-5	mA
h _{FE}	Forward current transfer ratio	V _{CE} = -3 V		(see Notes 5 and	l 6)	750			
V _{CE(sat)}	Collector-emitter saturation voltage	$I_{B} = -12 \text{ mA}$ $I_{B} = -50 \text{ mA}$	$I_C = -3 A$ $I_C = -5 A$	(see Notes 5 and	(6)			-2 -2.5	V
V _{BE(sat)}	Base-emitter saturation voltage	I _B = -50 mA	I _C = -5 A	(see Notes 5 and 6)				-3	V
V _{BE(on)}	Base-emitter voltage	V _{CE} = -3 V	I _C = -3 A	(see Notes 5 and	16)			-2.5	V

NOTES: 5. These parameters must be measured using pulse techniques, $t_p = 300 \mu s$, duty cycle $\leq 2\%$.

thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	R _{0JC} Junction to case thermal resistance			2.0	°C/W
$R_{\theta JA}$	R _{0JA} Junction to free air thermal resistance			62.5	°C/W

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN COLLECTOR CURRENT TCS135AD 50000 -40°C $T_c =$ 25°C $T_c = 100$ °C h_{FE} - Typical DC Current Gain 10000 1000 -3 V = 300 μs, duty cycle < 2% 100 -1.0 -10 -0.5 I_c - Collector Current - A

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

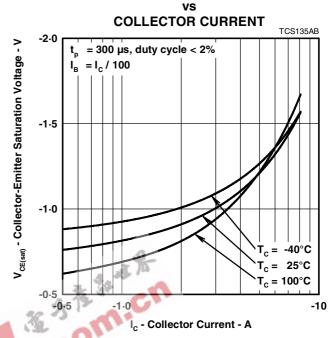
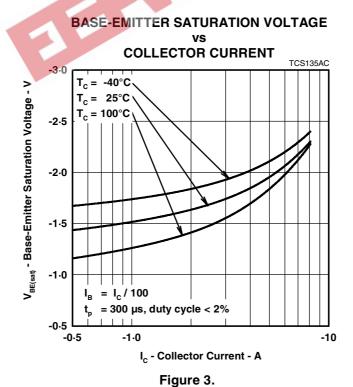
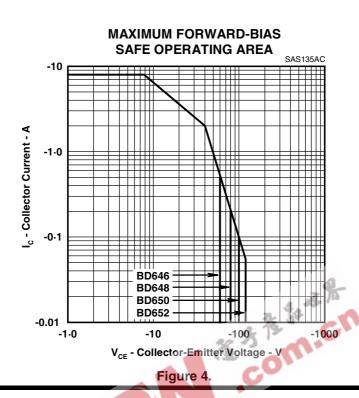


Figure 2.



PRODUCT INFORMATION

MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION

CASE TEMPERATURE TIS130AC 70 60 40 40 20 10

Figure 5.

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 $\rm T_{\rm C}$ - Case Temperature - $^{\circ}\rm C$

100

125

150

0 L

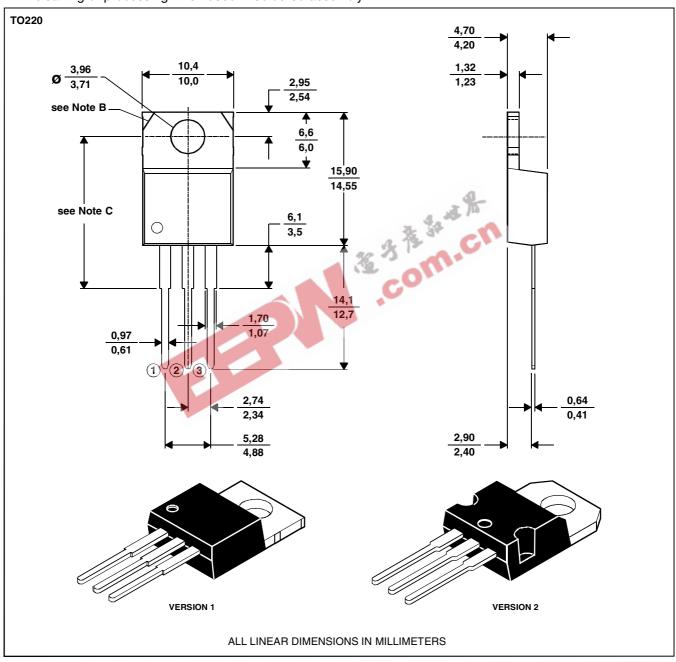
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MECHANICAL DATA

TO-220

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.

C. Typical fixing hole centre stand off height according to package version.

Version 1, 18.0 mm. Version 2, 17.6 mm.

MDXXBE