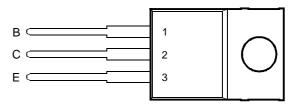
- Designed for Complementary Use with BDW24, BDW24A, BDW24B and BDW24C
- 50 W at 25°C Case Temperature
- 6 A Continuous Collector Current
- Minimum h_{FE} of 750 at 3 V, 2 A

TO-220 PACKAGE (TOP VIEW)



Pin 2 is in electrical contact with the mounting base.

MDTRACA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT		
	BDW23		45		
Collector base voltage (I = 0)	BDW23A	\/	60	V	
Collector-base voltage (I _E = 0)	BDW23B	V _{CBO}	80	v	
	BDW23C		100		
	BDW23	5	45		
Collector-emitter voltage (I _B = 0)	BDW23A	1/	60	V	
	BDW23B	VCEO	80	V	
40 9	BDW23C		100		
Emitter-base voltage	Visit.	V _{EBO}	5	V	
Continuous collector current		I _C	6	Α	
Continuous base current		I _B	0.2	Α	
Continuous device dissipation at (or below) 25°C case temperature (see Note 1)	P _{tot}	50	W		
Continuous device dissipation at (or below) 25°C free air temperature (see Note 2)			2	W	
Operating junction temperature range	T _j	-65 to +150	°C		
Storage temperature range	T _{stg}	-65 to +150	°C		
Operating free-air temperature range	T _A	-65 to +150	°C		

NOTES: 1. Derate linearly to 150°C case temperature at the rate of 0.4 W/°C.

2. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.

BDW23, BDW23A, BDW23B, BDW23C NPN SILICON POWER DARLINGTONS

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electrical characteristics at 25°C case temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS				MIN	TYP	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C = 100 mA	I _B = 0	(see Note 3)	BDW23 BDW23A BDW23B BDW23C	45 60 80 100			V
I _{CEO}	Collector-emitter cut-off current	$V_{CE} = 30 \text{ V}$ $V_{CE} = 30 \text{ V}$ $V_{CE} = 40 \text{ V}$ $V_{CE} = 50 \text{ V}$	$I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$		BDW23 BDW23A BDW23B BDW23C			0.5 0.5 0.5 0.5	mA
I _{CBO}	Collector cut-off current	$V_{CB} = 45 \text{ V}$ $V_{CB} = 60 \text{ V}$ $V_{CB} = 80 \text{ V}$ $V_{CB} = 100 \text{ V}$	I _E = 0 I _E = 0 I _E = 0 I _E = 0		BDW23 BDW23A BDW23B BDW23C			0.2 0.2 0.2 0.2	mA
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	I _C = 0					2	mA
h _{FE}	Forward current transfer ratio	$V_{CE} = 3 V$ $V_{CE} = 3 V$ $V_{CE} = 3 V$	$I_{C} = 1 A$ $I_{C} = 2 A$ $I_{C} = 6 A$	(see Notes 3 and	4)	1000 750 100		20000	
V _{CE(sat)}	Collector-emitter saturation voltage	$I_B = 8 \text{ mA}$ $I_B = 60 \text{ mA}$	$I_C = 2 A$ $I_C = 6 A$	(see Notes 3 and	4)			2	V
V _{BE(sat)}	Base-emitter saturation voltage	I _B = 8 mA	I _C = 2 A	(see Notes 3 and	4)			2.5	
V _{BE(on)}	Base-emitter voltage	$V_{CE} = 3 V$ $V_{CE} = 3 V$	$I_{C} = 1 A$ $I_{C} = 6 A$	(see Notes 3 and	4)			2.5 3	V
V _{EC}	Parallel diode forward voltage	I _E = 2 A	I _B = 0	J.C				1.8	V

NOTES: 3. These parameters must be measured using pulse techniques, $t_p = 300 \,\mu s$, duty cycle $\leq 2\%$.

thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			2.5	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 3 A	$I_{B(on)} = 12 \text{ mA}$	$I_{B(off)} = -12 \text{ mA}$		1		μs
t _{off}	Turn-off time	$V_{BE(off)} = -4.5 \text{ V}$	$R_L = 10 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		5		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

PRODUCT INFORMATION

^{4.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN COLLECTOR CURRENT TCS120AD 40000 $T_c = -40^{\circ}C$ 25°C $T_c = 100$ °C h_{FE} - Typical DC Current Gain 10000 1000 3 V $V_{CE} =$ = 300 µs, duty cycle < 2% 100 0.5 1.0 10 I_c - Collector Current - A

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

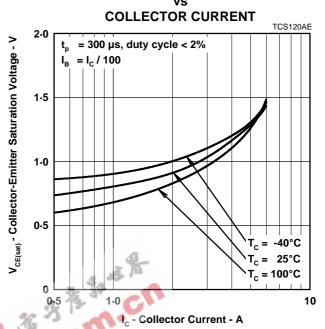
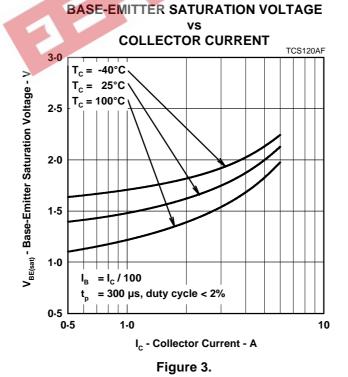
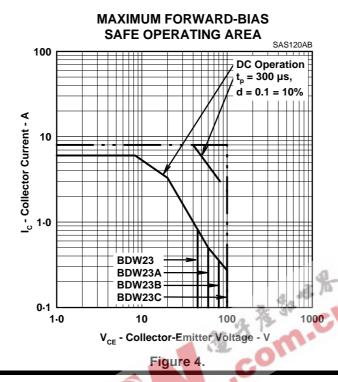


Figure 2.

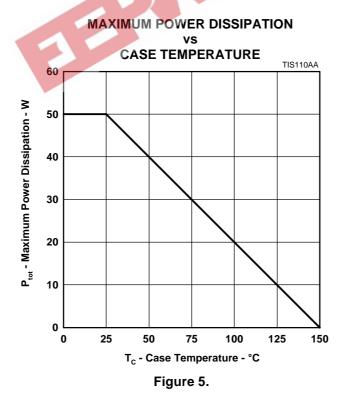


Power 1)

MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION



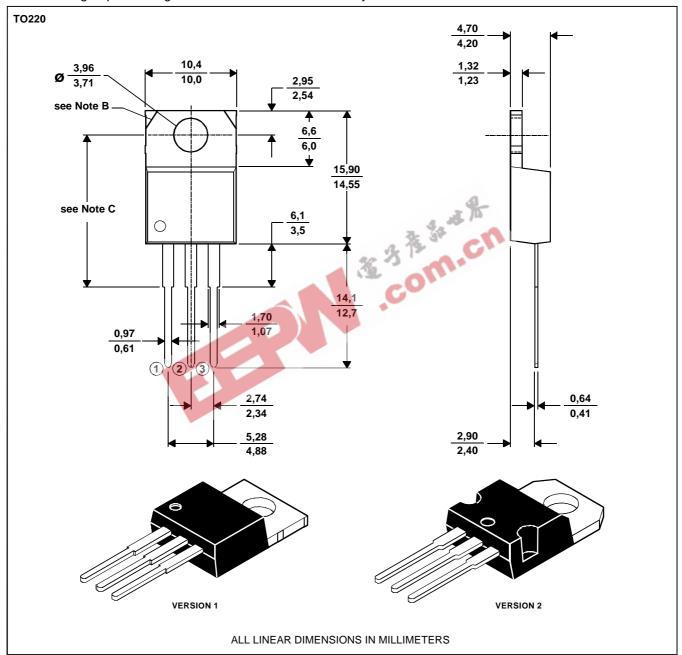
PRODUCT INFORMATION

MECHANICAL DATA

TO-220

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

B. Mounting tab corner profile according to package version.

C. Typical fixing hole centre stand off height according to package version. Version 1, 18.0 mm. Version 2, 17.6 mm. MDXXBE



BDW23, BDW23A, BDW23B, BDW23C NPN SILICON POWER DARLINGTONS

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