

# CD54AC04/3A CD54ACT04/3A

COMPLETE DATA SHEET **COMING SOON!** 

**Hex Inverters, Active Outputs** 

June 1997

## Description

The CD54AC04/3A and CD54ACT04/3A are hex inverters that utilize the Harris Advanced CMOS Logic technology. The CD54AC04/3A and CD54ACT04/3A have active outputs.

The CD54AC04/3A and CD54ACT04/3A are supplied in 14 lead dual-in-line ceramic packages (F suffix).

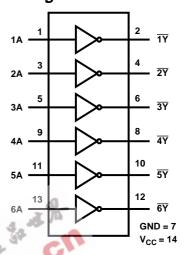
#### **ACT INPUT LOAD TABLE**

INPUT	UNIT LOAD (NOTE 1)
nA	0.18

#### NOTE:

1. Unit load is  $\Delta I_{\mbox{\footnotesize CC}}$  limit specified in DC Electrical Specifications Table, e.g., 2.4mA Max at +25°C.

## Functional Diagram



## **Absolute Maximum Ratings**

DC Supply Voltage, V <sub>CC</sub>	-0.5V to +6V
DC Input Diode Current, I <sub>IK</sub>	
For $V_1 < -0.5V$ or $V_1 > V_{CC} + 0.5V$	±20mA
DC Output Diode Current, I <sub>OK</sub>	
For $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$	±50mA
DC Output Source or Sink Current, Per Output Pin, IO	
For $V_O > -0.5V$ or $V_O < V_{CC} + 0.5V$	±50mA
DC V <sub>CC</sub> or GND Current, I <sub>CC</sub> or I <sub>GND</sub>	
For Up to 4 Outputs Per Device, Add ±25mA For Each	h
Additional Output	±100mA

Power Dissipation Per Package, P <sub>D</sub>
T <sub>A</sub> = -55°C to +100°C (Package F) 500mW
$T_A = +100^{\circ}$ C to $+125^{\circ}$ C (Package F) Derate Linearly at
8mW/°C to 300mW
Operating Temperature Range, T <sub>A</sub>
Package Type F55°C to +125°C
Storage Temperature, T <sub>STG</sub> 65°C to +150°C
Lead Temperature (During Soldering)
At Distance 1/16in. ± 1/32in. (1.59mm ± 0.79mm)
From Case For 10s Max
Unit Inserted Into a PC Board (Min Thickness 1/16in., 1.59mm)
With Solder Contacting Lead Tips Only+300°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

### **Recommended Operating Conditions**

Supply Voltage Range, V <sub>CC</sub>
Unless Otherwise Specified, All Voltages Referenced to GND
T <sub>A</sub> = Full Package Temperature Range
CD54AC Types
CD54ACT Types
DC Input or Output Voltage, $V_I,V_O,\ldots$ 0V to $V_{CC}$

Operating Temperature, T <sub>A</sub>	-55°C to +125°C
Input Rise and Fall Slew Rate, dt/dv	
at 1.5V to 3V (AC Types)	0ns/V to 50ns/V
at 3.6V to 5.5V (AC Types)	0ns/V to 20ns/V
at 4.5V to 5.5V (AC Types)	0ns/V to 10ns/V