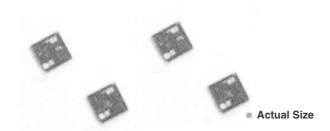
Vishay Sfernice



Single Value Chip Resistor



Chromium silicon thin film is very well suited to produce high density and high ohmic value resistor chips. These high ohmic value chip resistors are available with improved performances and size when compared to thick film counterparts.

FEATURES

- Small size 20 mil x 20 mil
- Very high ohmic value up to 10 $M\Omega$
- Good stability 0.1 % (2000 h, rated power at + 70 °C)
- Wirebondable





TYPICAL PERFORMANCE

	ABS
TCR	100 ppm/°C
TOL.	0.5 %



STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
MATERIAL	PASSIVATED CHROMIUM SILICON	
Resistance range	10 kΩ to 10 MΩ	
Absolute TCR	± 100 ppm/°C (± 50 ppm/°C on request)	- 55 °C to + 155 °C
Absolute tolerance	± 0.5 %, ± 1 %, ± 2 %	
Power dissipation	100 mW at + 25 °C, 50 mW at + 70 °C, 25 mW at + 125 °C	
Stability	± 0.1 % typical, ± 0.2 maximum	2000 h at + 70 °C at Pn
Working voltage	100 V _{DC}	Higher on Al ₂ O ₃
Operating temperature range	- 55 °C to + 155 °C	
Storage temperature range	- 55 °C to + 155 °C	
Noise	< - 20 dB typical	MIL-STD-202 Method 308
Thermal EMF	< 0.01 μV/°C	
Shelf life stability	200 ppm	1 year at + 25 °C

^{*} Please see document "Vishay Green and Halogen-Free Definitions (5-2008)" http://www.vishay.com/doc?99902

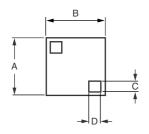




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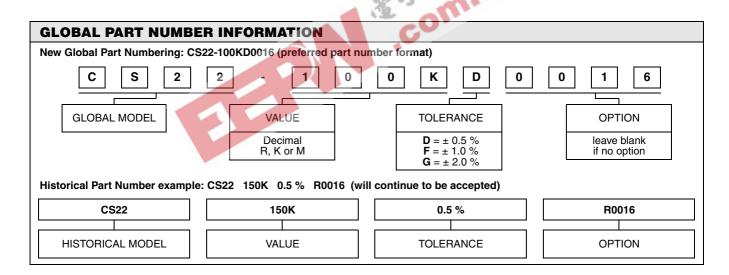
DIMENSIONS





DIMENSION	INCHES	MILLIMETERS
Α	0.021 ± 0.002	0.55 ± 0.10
В	0.021 ± 0.002	0.55 ± 0.10
С	0.004	0.10
D	0.004	0.10
Е	0.015	0.40 maximum

MECHANICAL SPECIFICATIONS		
Resistive element	Chromium Silicon	
Passivation	Silicon Nitride	
Substrate material	Silicon (consult Vishay for Al ₂ O ₃)	
Bonding pads	Aluminum	







Vishay

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