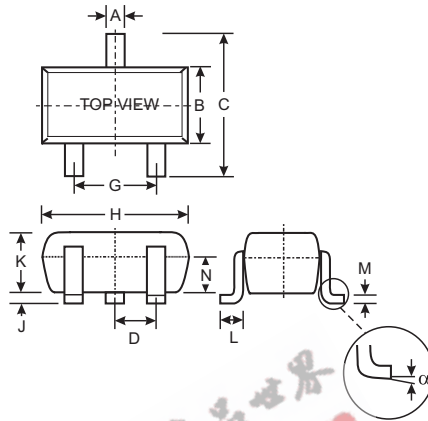


## Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors
- Lead Free/RoHS Compliant (Note 2)

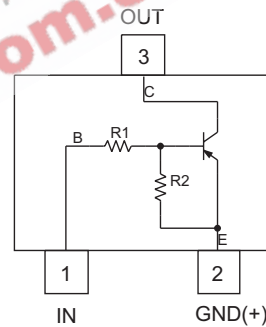
## Mechanical Data

- Case: SOT-523
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminals: Finish — Matte Tin Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Terminal Connections: See Diagram
- Marking: Date Code and Type Code, See Page 2
- Weight: 0.002 grams (approximate)
- Ordering Information (See Page 2)



SOT-523			
Dim	Min	Max	Typ
A	0.15	0.30	0.22
B	0.75	0.85	0.80
C	1.45	1.75	1.60
D	—	—	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
J	0.00	0.10	0.05
K	0.60	0.80	0.75
L	0.10	0.30	0.22
M	0.10	0.20	0.12
N	0.45	0.65	0.50
α	0°	8°	—
All Dimensions in mm			

P/N	R1 (NOM)	R2 (NOM)	Type Code
DDTA122LE	0.22KΩ	10KΩ	P81
DDTA142JE	0.47KΩ	10KΩ	P82
DDTA122TE	0.22KΩ	OPEN	P83
DDTA142TE	0.47KΩ	OPEN	P84



Schematic and Pin Configuration

## Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (2) to (3)	V <sub>CC</sub>	-50	V
Input Voltage, (1) to (2)	V <sub>IN</sub>	+5 to -6 +5 to -6	V
Input Voltage, (2) to (1)	V <sub>EBO</sub> (MAX)	-5	V
Output Current	I <sub>C</sub>	-100	mA
Power Dissipation (Note 1)	P <sub>d</sub>	150	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R <sub>θJA</sub>	625	°C/W
Operating and Storage and Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.  
2. No purposefully added lead.

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

**R1, R2 Types**

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	DDTA122LE DDTA142JE	V <sub>I(off)</sub>	-0.3 -0.3	—	—	V	V <sub>CC</sub> = -5V, I <sub>O</sub> = -100μA
	DDTA122LE DDTA142JE	V <sub>I(on)</sub>	—	—	-2.0 -2.0	V	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA
Output Voltage		V <sub>O(on)</sub>	—	—	-0.3V	V	I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA
Input Current	DDTA122LE DDTA142JE	I <sub>I</sub>	—	—	-28 -13	mA	V <sub>I</sub> = -5V
Output Current		I <sub>O(off)</sub>	—	—	-0.5	μA	V <sub>CC</sub> = -50V, V <sub>I</sub> = 0V
DC Current Gain	DDTA122LE DDTA142JE	G <sub>I</sub>	56 56	—	—	—	V <sub>O</sub> = -5V, I <sub>O</sub> = -10mA
Gain-Bandwidth Product*		f <sub>T</sub>	—	200	—	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = -5mA, f = 100MHz

\* Transistor - For Reference Only

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

**R1-Only Types**

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-50	—	—	V	I <sub>C</sub> = -50μA	
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-40	—	—	V	I <sub>C</sub> = -1mA	
Emitter-Base Breakdown Voltage	DDTA122TE DDTA142TE	BV <sub>EBO</sub>	-5	—	V	I <sub>E</sub> = -50μA I <sub>E</sub> = -50μA	
Collector Cutoff Current		I <sub>CBO</sub>	—	—	μA	V <sub>CB</sub> = -50V	
Emitter Cutoff Current	DDTA122TE DDTA142TE	I <sub>EBO</sub>	—	—	μA	V <sub>EB</sub> = -4V	
Collector-Emitter Saturation Voltage		V <sub>CE(sat)</sub>	—	-0.3	V	I <sub>C</sub> = -5mA, I <sub>B</sub> = -0.25mA	
DC Current Transfer Ratio	DDTA122TE DDTA142TE	h <sub>FE</sub>	100 100	250 250	600 600	I <sub>C</sub> = -1mA, V <sub>CE</sub> = -5V	
Gain-Bandwidth Product*		f <sub>T</sub>	—	200	—	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz

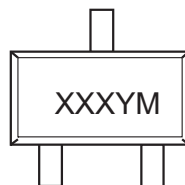
\* Transistor - For Reference Only

**Ordering Information** (Note 3)

Device	Packaging	Shipping
DDTA122LE-7-F	SOT-523	3000/Tape & Reel
DDTA142JE-7-F	SOT-523	3000/Tape & Reel
DDTA122TE-7-F	SOT-523	3000/Tape & Reel
DDTA142TE-7-F	SOT-523	3000/Tape & Reel

Notes: 3. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**



XXX = Product Type Marking Code (See Page 1)  
YM = Date Code Marking  
Y = Year ex: T = 2006  
M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012
Code	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

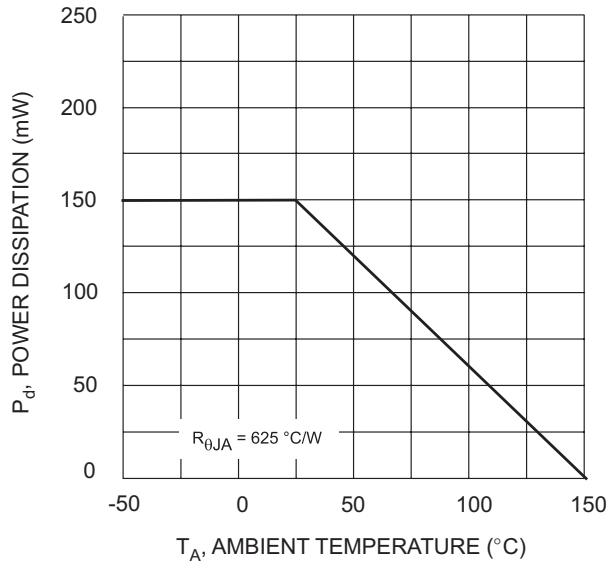


Fig. 1 Power Derating Curve

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