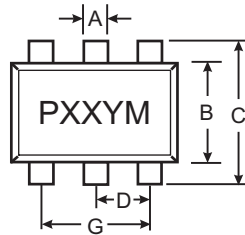


Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDC)
- Built-In Biasing Resistors
- **Lead Free By Design/RoHS Compliant (Note 3)**

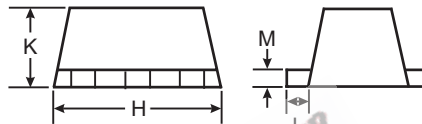
Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminals: Finish - Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.005 grams (approx.)



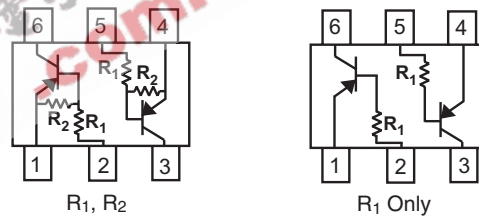
SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.25
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	0.50		
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.56	0.60	0.60
L	0.15	0.25	0.20
M	0.10	0.18	0.11

All Dimensions in mm



SEE NOTE 1

P/N	R1	R2	MARKING
DDA124EH	22KΩ	22KΩ	P17
DDA144EH	47KΩ	47KΩ	P20
DDA143EH	4.7KΩ	4.7KΩ	P08
DDA114YH	10KΩ	47KΩ	P14
DDA123JH	2.2KΩ	47KΩ	P06
DDA114EH	10KΩ	10KΩ	P13
DDA143TH	4.7KΩ	-	P07
DDA114TH	10KΩ	-	P12



SCHEMATIC DIAGRAM, TOP VIEW

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage (6) to (1) and (3) to (4)	V _{CC}	-50	V
Input Voltage (2) to (1) and (5) to (4)	V _{IN}	+10 to -40 +10 to -40 +10 to -30 +6 to -40 +5 to -12 +10 to -40 +5 V _{max} +5 V _{max}	V
Output Current	I _O	-30 -30 -100 -70 -100 -50 -100 -100	mA
Output Current	I _C (Max)	-100	mA
Power Dissipation	P _d	150	mW
Thermal Resistance, Junction to Ambient Air (Note 2)	R _{θJA}	833	°C/W
Operating and Storage and Temperature Range	T _j , T _{STG}	-55 to +150	°C

- Note:
1. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).
 2. Mounted on FR4 Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.
 3. No purposefully added lead.

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic (DDA143TH & DDA114TH only)	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-50	—	—	V	I _C = -50μA
Collector-Emitter Breakdown Voltage	BV _{CEO}	-50	—	—	V	I _C = -1mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	—	—	V	I _E = -50μA
Collector Cutoff Current	I _{CBO}	—	—	-0.5	μA	V _{CB} = -50V
Emitter Cutoff Current	I _{EBO}	—	—	-0.5	μA	V _{EB} = -4V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	—	-0.3	V	I _C /I _B = -2.5mA / -0.25mA I _C /I _B = -1mA / -0.1mA
DC Current Transfer Ratio	h _{FE}	100	250	600	—	I _C = -1mA, V _{CE} = -5V
Gain-Bandwidth Product*	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = 5mA, f = 100MHz

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage	V _{I(off)}	-0.5 -0.5 -0.5 -0.3 -0.5 -0.5	-1.1 -1.1 -1.1 — — -1.1	—	V	V _{CC} = -5V, I _O = -100μA
Output Voltage	V _{O(on)}	—	-0.1	-0.3	V	I _O /I _I = -10mA / -0.5mA I _O /I _I = -10mA / -0.5mA I _O /I _I = -10mA / -0.5mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -5mA / -0.25mA I _O /I _I = -10mA / -0.5mA
Output Current	I _{O(off)}	—	—	-0.5	μA	V _{CC} = -50V, V _I = -0V
Gain-Bandwidth Product*	f _T	—	250	—	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz

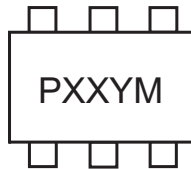
* Transistor - For Reference Only

Ordering Information (Note 4)

Device	Packaging	Shipping
DDA124EH-7	SOT-563	3000/Tape & Reel
DDA144EH-7	SOT-563	3000/Tape & Reel
DDA143EH-7	SOT-563	3000/Tape & Reel
DDA114YH-7	SOT-563	3000/Tape & Reel
DDA123JH-7	SOT-563	3000/Tape & Reel
DDA114EH-7	SOT-563	3000/Tape & Reel
DDA143TH-7	SOT-563	3000/Tape & Reel
DDA114TH-7	SOT-563	3000/Tape & Reel

Notes: 4. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



PXX = Product Type Marking Code (See Page 1)
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

TYPICAL CURVES - DDA143EH

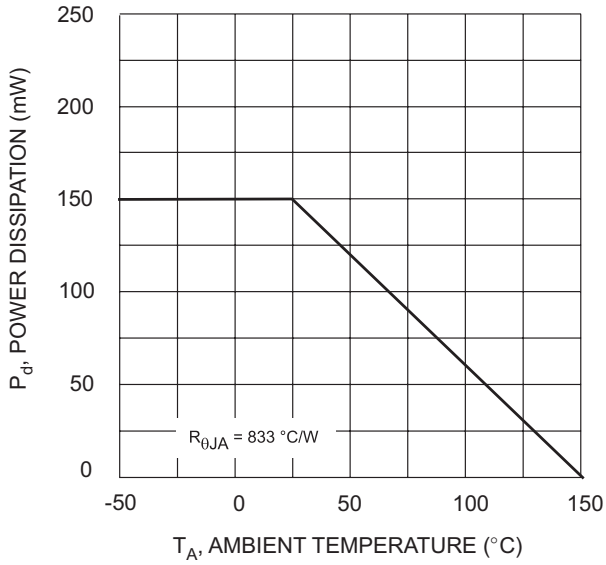


Fig. 1 Derating Curve

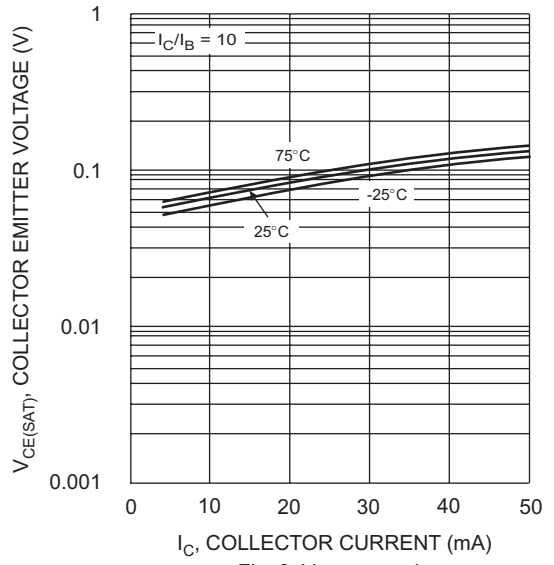


Fig. 2 $V_{CE(SAT)}$ vs. I_C

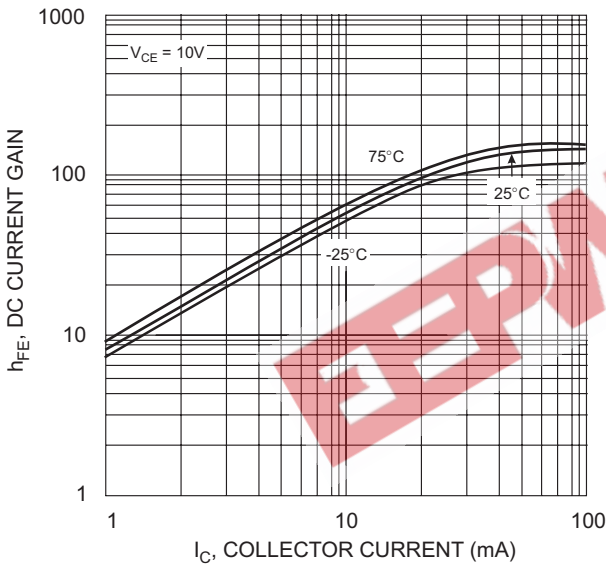


Fig. 3 DC Current Gain

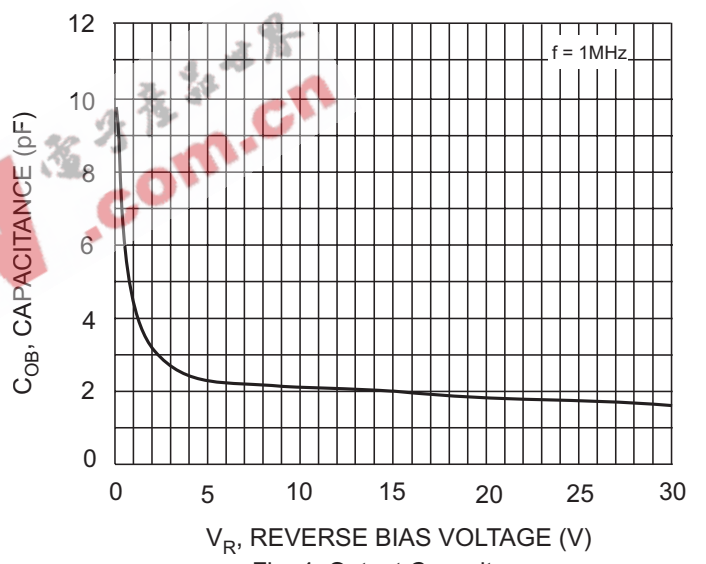


Fig. 4 Output Capacitance

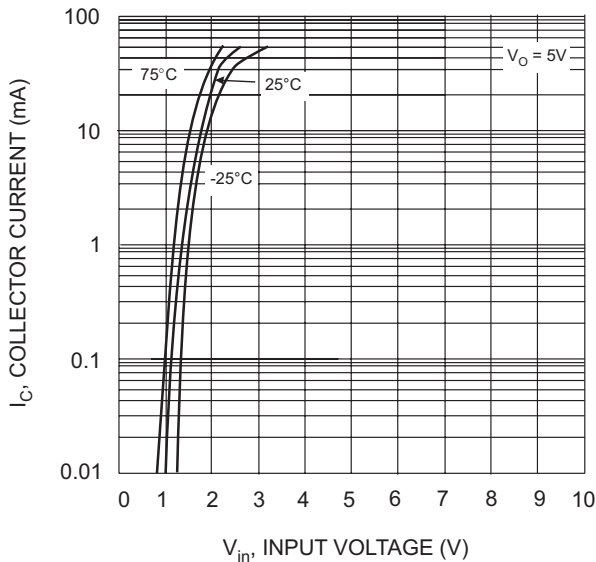


Fig. 5 Collector Current Vs. Input Voltage

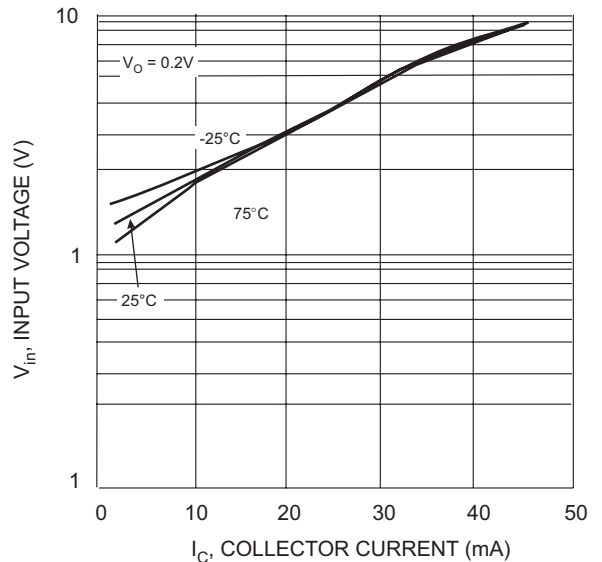


Fig. 6 Input Voltage vs. Collector Current

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