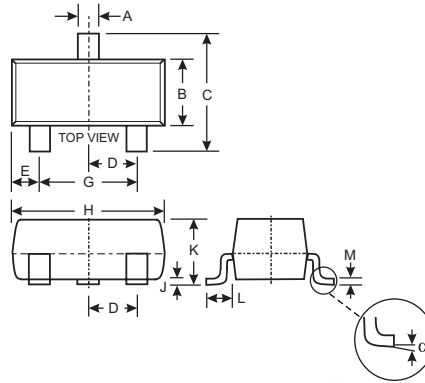


### Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1≠R2
- Lead Free/RoHS Compliant (Note 2)

### Mechanical Data

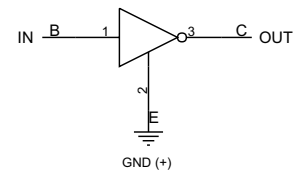
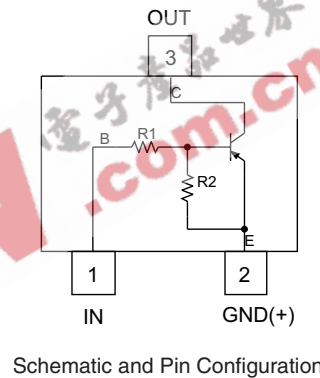
- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Marking: Date Code and Marking Code (See Table Below & Page 3)
- Ordering Information (See Page 2)
- Weight: 0.008 grams (approximate)



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°

All Dimensions in mm

P/N	R1 (NOM)	R2 (NOM)	MARKING
DDTA113ZCA	1KΩ	10KΩ	P02
DDTA123YCA	2.2KΩ	10KΩ	P05
DDTA123JCA	2.2KΩ	47KΩ	P06
DDTA143XCA	4.7KΩ	10KΩ	P09
DDTA143FCA	4.7KΩ	22KΩ	P10
DDTA143ZCA	4.7KΩ	47KΩ	P11
DDTA114YCA	10KΩ	47KΩ	P14
DDTA114WCA	10KΩ	4.7KΩ	P15
DDTA124XCA	22KΩ	47KΩ	P18
DDTA144VCA	47KΩ	10KΩ	P21
DDTA144WCA	47KΩ	22KΩ	P22



### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (2)	V <sub>CC</sub>	-50	V
Input Voltage, (1) to (2)	V <sub>IN</sub>	DDTA113ZCA: +5 to -10 DDTA123YCA: +5 to -12 DDTA123JCA: +5 to -12 DDTA143XCA: +7 to -20 DDTA143FCA: +6 to -30 DDTA143ZCA: +5 to -30 DDTA114YCA: +6 to -40 DDTA114WCA: +10 to -30 DDTA124XCA: +10 to -40 DDTA144VCA: +15 to -40 DDTA144WCA: +10 to -40	V
Output Current	I <sub>O</sub>	DDTA113ZCA: -100 DDTA123YCA: -100 DDTA123JCA: -100 DDTA143XCA: -100 DDTA143FCA: -100 DDTA143ZCA: -100 DDTA114YCA: -70 DDTA114WCA: -100 DDTA124XCA: -50 DDTA144VCA: -30 DDTA144WCA: -30	mA
Output Current	I <sub>C</sub> (Max)	-100	mA
Power Dissipation	P <sub>d</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R <sub>θJA</sub>	625	°C/W
Operating and Storage and Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.  
2. No purposefully added lead.

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	DDTA113ZCA DDTA123YCA DDTA123JCA DDTA143XCA DDTA143FCA DDTA143ZCA DDTA114YCA DDTA114WCA DDTA124XCA DDTA144VCA DDTA144WCA	V <sub>I(off)</sub>	-0.3 -0.3 -0.5 -0.3 -0.3 -0.5 -0.3 -0.8 -0.4 -1.0 -0.8	—	—	—	V	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA
	DDTA113ZCA DDTA123YCA DDTA123JCA DDTA143XCA DDTA143FCA DDTA143ZCA DDTA114YCA DDTA114WCA DDTA124XCA DDTA144VCA DDTA144WCA	V <sub>I(on)</sub>	—	—	-3.0 -3.0 -1.1 -2.5 -1.3 -1.3 -1.4 -3.0 -2.5 -5.0 -4.0	—	V	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -5mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -20mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -3mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -5mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -1mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA V <sub>O</sub> = -0.3V, I <sub>O</sub> = -2mA
Output Voltage		V <sub>O(on)</sub>	—	-0.1	-0.3	V	I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA123JCA I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA143ZCA I <sub>O</sub> /I <sub>I</sub> = -5mA/-0.25mA DDTA114YCA I <sub>O</sub> /I <sub>I</sub> = -10mA/-0.5mA All Others	
Input Current	DDTA113ZCA DDTA123YCA DDTA123JCA DDTA143XCA DDTA143FCA DDTA143ZCA DDTA114YCA DDTA114WCA DDTA124XCA DDTA144VCA DDTA144WCA	I <sub>I</sub>	—	—	-7.2 -3.8 -3.6 -1.8 -1.8 -1.8 -0.88 -0.88 -0.36 -0.16 -0.16	mA	V <sub>I</sub> = -5V	
Output Current		I <sub>O(off)</sub>	—	—	-0.5	μA	V <sub>CC</sub> = -50V, V <sub>I</sub> = 0V	
DC Current Gain	DDTA113ZCA DDTA123YCA DDTA123JCA DDTA143XCA DDTA143FCA DDTA143ZCA DDTA114YCA DDTA114WCA DDTA124XCA DDTA144VCA DDTA144WCA	G <sub>I</sub>	-33 -33 -80 -30 -68 -80 -68 -24 -68 -33 -56	—	—	—	V <sub>O</sub> = -5V, I <sub>O</sub> = -10mA	
Input Resistor Tolerance		ΔR <sub>1</sub>	-30	—	+30	%	—	
Resistance Ratio Tolerance		ΔR <sub>2</sub> /R <sub>1</sub>	-20	—	+20	%	—	
Gain-Bandwidth Product*		f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	

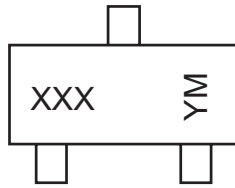
\* Transistor - For Reference Only

**Ordering Information** (Note 3)

Device	Packaging	Shipping
DDTA113ZCA-7-F	SOT-23	3000/Tape & Reel
DDTA123YCA-7-F	SOT-23	3000/Tape & Reel
DDTA123JCA-7-F	SOT-23	3000/Tape & Reel
DDTA143XCA-7-F	SOT-23	3000/Tape & Reel
DDTA143FCA-7-F	SOT-23	3000/Tape & Reel
DDTA143ZCA-7-F	SOT-23	3000/Tape & Reel
DDTA114YCA-7-F	SOT-23	3000/Tape & Reel
DDTA114WCA-7-F	SOT-23	3000/Tape & Reel
DDTA124XCA-7-F	SOT-23	3000/Tape & Reel
DDTA144VCA-7-F	SOT-23	3000/Tape & Reel
DDTA144WCA-7-F	SOT-23	3000/Tape & Reel

Notes: 3. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**



XXX = Product Type Marking Code, See Table on Page 1  
 YM = Date Code Marking  
 Y = Year ex: T = 2006  
 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009
Code	N	P	R	S	T	U	V	W

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D



**TYPICAL CURVES - DDTA123JCA**

NEW PRODUCT

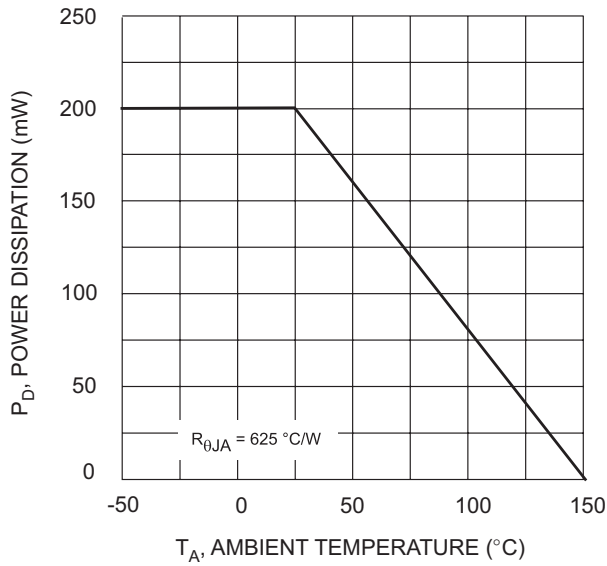


Fig. 1 Derating Curve

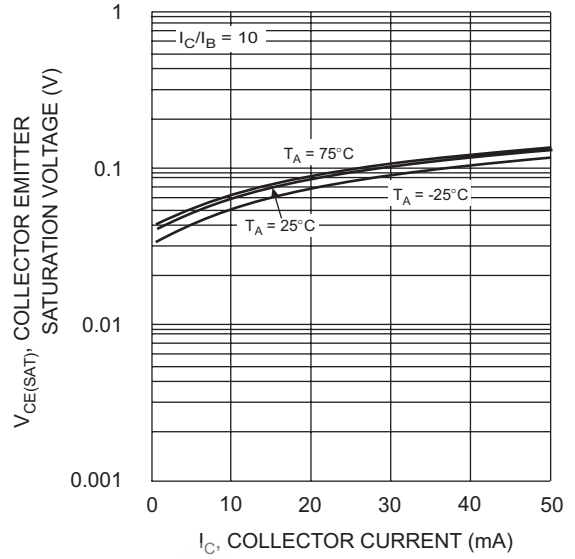


Fig. 2  $V_{CE(SAT)}$  vs.  $I_C$

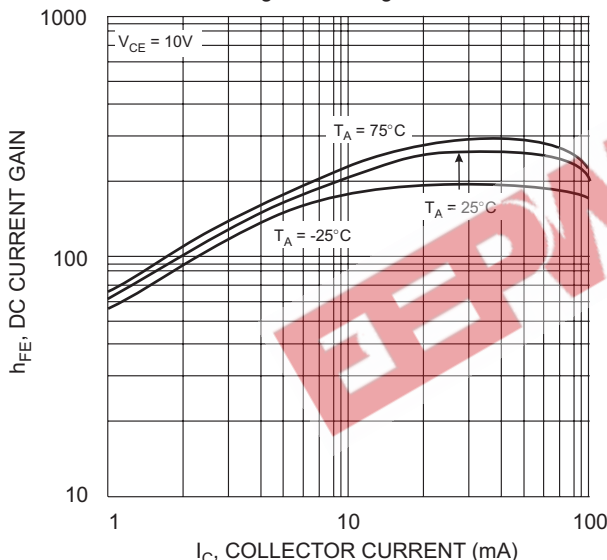


Fig. 3 DC CURRENT GAIN

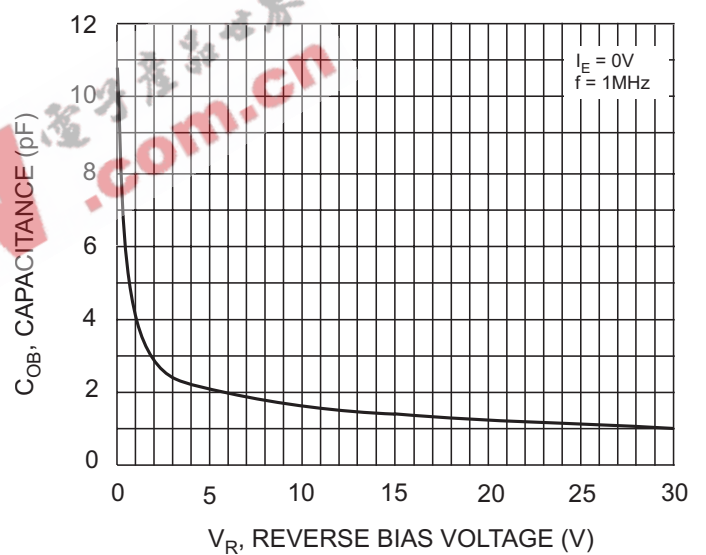


Fig. 4 Output Capacitance

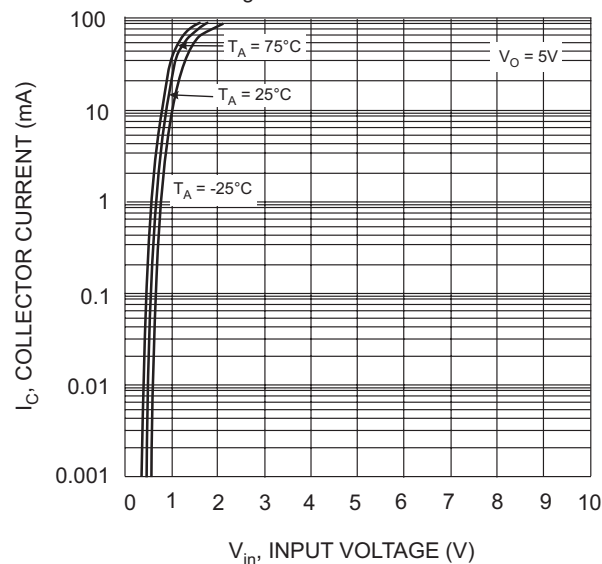


Fig. 5 Collector Current Vs. Input Voltage

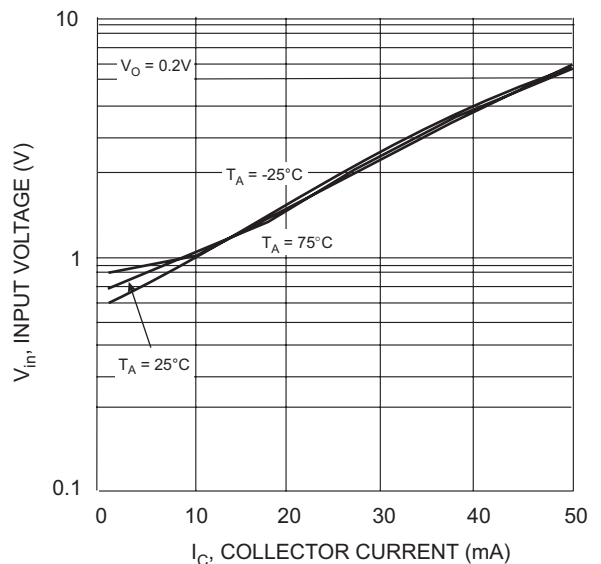


Fig. 6 Input Voltage vs. Collector Current

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