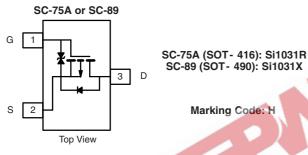




# P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (mA)				
- 20	8 at V <sub>GS</sub> = - 4.5 V	- 150				
	12 at V <sub>GS</sub> = - 2.5 V	- 125				
	15 at V <sub>GS</sub> = - 1.8 V	- 100				
	20 at V <sub>GS</sub> = - 1.5 V	- 30				



#### **Ordering Information:**

Si1031R-T1-E3 (SC-75A, Lead (Pb)-free) Si1031R-T1-GE3 (SC-75A, Lead (Pb)-free and Halogen-free) Si1031X-T1-E3 (SC-89, Lead (Pb)-free) Si1031X-T1-GE3 (SC-89, Lead (Pb)-free and Halogen-free)

#### **FEATURES**

- · Halogen-free Option Available
- High-Side Switching
- Low On-Resistance: 8 Ω
- Low Threshold: 0.9 V (typ.)
- Fast Switching Speed: 45 ns
- TrenchFET<sup>®</sup> Power MOSFETs: 1.5-V Rated
- ESD Protected: 2000 V

#### **APPLICATIONS**

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

### BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- · High-Speed Circuits
- · Low Battery Voltage Operation

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted										
		Symbol	Si1031R		Si1031X					
Parameter	5 s		Steady State	5 s	Steady State	Unit				
Drain-Source Voltage		$V_{DS}$	- 20				V			
Gate-Source Voltage		$V_{GS}$	± 6							
Continuous Drain Current /T 150 °C\a	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 150	- 140	- 165	- 155	A			
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		- 110	- 100	- 150	- 125				
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 500		- 600		mA			
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 250	- 200	- 340	- 240				
M	T <sub>A</sub> = 25 °C	P <sub>D</sub>	280	250	340	300	mW			
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		145	130	170	150				
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150				°C			
Gate-Source ESD Rating (HBM, Method 3015)		ESD	2000				V			

#### Notes:

a. Surface Mounted on FR4 board.

### Si1031R/X

# Vishay Siliconix



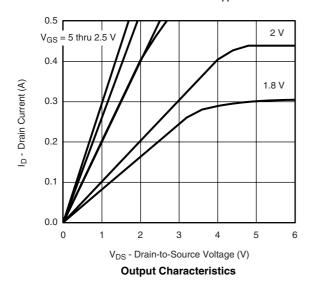
<b>SPECIFICATIONS</b> $T_A = 25  ^{\circ}C$ , unless otherwise noted										
Parameter	Symbol	Test Conditions	Typ. <sup>a</sup>	Max.	Unit					
Static	•			•						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.40		- 1.2	V				
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 2.8 \text{ V}$		± 0.5	± 1.0	μΑ				
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$		± 1.0	± 2.0					
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V		- 1	- 500	nA				
		V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C			- 10	μΑ				
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 200			mA				
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 150 mA			8					
D : 0		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 125 mA			12	Ω				
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 100 mA			15					
		V <sub>GS</sub> = - 1.5 V, I <sub>D</sub> = - 30 mA			20					
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = 150 mA		0.4		S				
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 150 mA, V <sub>GS</sub> = 0 V			- 1.2	V				
Dynamic <sup>b</sup>			75							
Total Gate Charge	$Q_g$		-	1500						
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -150 \text{ mA}$	$S_{II}$	150		рC				
Gate-Drain Charge	$Q_{gd}$	36 3		450						
Turn-On Delay Time	t <sub>d(on)</sub>	-01			55					
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 65 $\Omega$			30	no				
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 150 mA, $V_{GEN} =$ - 4.5 V, $R_G =$ 10 $\Omega$			60	ns				
Fall Time	t <sub>f</sub>				30					

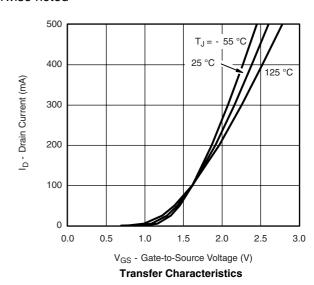
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **TYPICAL CHARACTERISTICS** $T_A = 25$ °C, unless otherwise noted



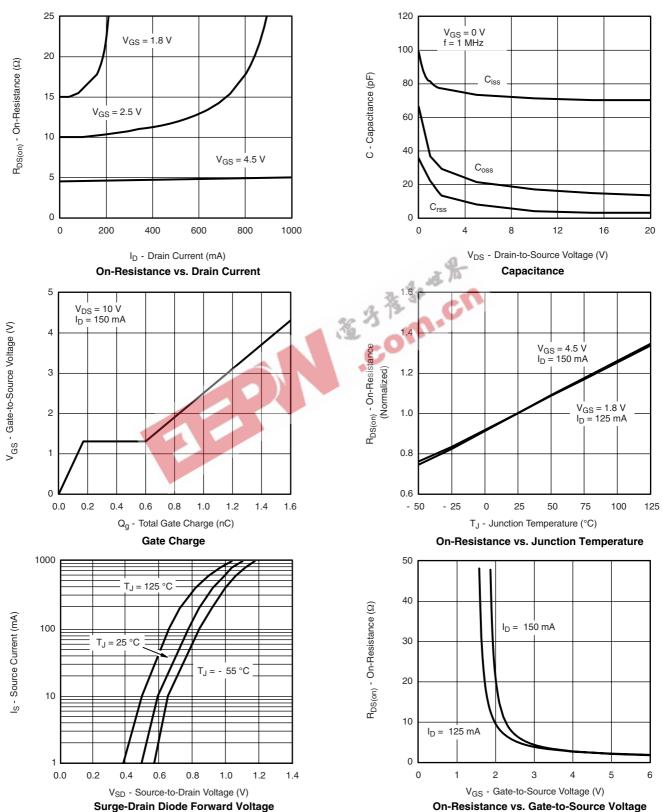








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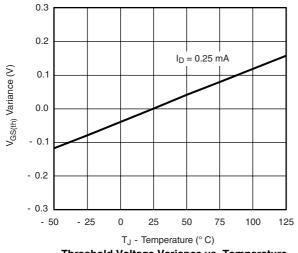


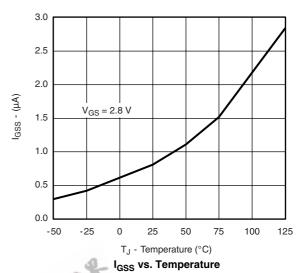
## Si1031R/X

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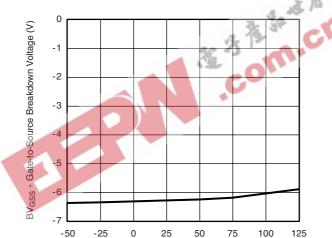


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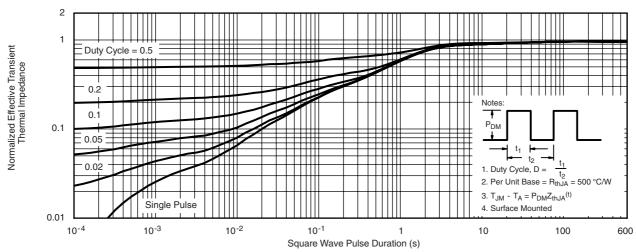




Threshold Voltage Variance vs. Temperature



 $T_J$  - Temperature (°C) BV<sub>GSS</sub> vs. Temperature



Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A, Si1031R Only)

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