



New Product

Si1972DH

Vishay Siliconix

Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ)
30	0.190 at $V_{GS} = 10$ V	1.3	0.91 nC
	0.344 at $V_{GS} = 4.5$ V	1.3	

FEATURES

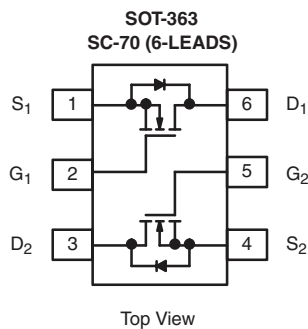
- TrenchFET[®] Power MOSFET

APPLICATIONS

- Load switch for portable applications



RoHS
COMPLIANT

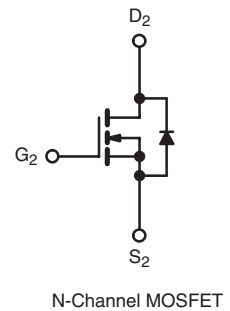


Marking Code



Lot Traceability
and Date Code

Part # Code



Ordering Information: Si1972DH-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	I_D	$T_C = 25^\circ\text{C}$	1.3 ^a
		$T_C = 70^\circ\text{C}$	1.3 ^a
		$T_A = 25^\circ\text{C}$	1.3 ^a
		$T_A = 70^\circ\text{C}$	1.2
Pulsed Drain Current	I_{DM}	4	
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$	1.0
		$T_A = 25^\circ\text{C}$	0.61 ^c
Maximum Power Dissipation	P_D	$T_C = 25^\circ\text{C}$	1.25
		$T_C = 70^\circ\text{C}$	0.8
		$T_A = 25^\circ\text{C}$	0.74 ^{b, c}
		$T_A = 70^\circ\text{C}$	0.47 ^{b, c}
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$
Soldering Recommendations (Peak Temperature) ^{d, e}		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, f}	R_{thJA}	130	170	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	R_{thJF}	80	100	

Notes:

a. Package limited.

b. Surface Mounted on 1" x 1" FR4 Board.

c. $t = 5$ sec.

d. Maximum under Steady State conditions is 220°C/W .

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SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = 250\text{ }\mu\text{A}$	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		23.5		mV/ $^{\circ}\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 4.6		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	1.5		2.8	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 20\text{ V}$			± 100	ns
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^{\circ}\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq 5\text{ V}$, $V_{GS} = 10\text{ V}$	4			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10\text{ V}$, $I_D = 1.3\text{ A}$		0.155	0.225	Ω
		$V_{GS} = 4.5\text{ V}$, $I_D = 0.29\text{ A}$		0.278	0.340	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}$, $I_D = 1.3\text{ A}$		1.4		S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{DS} = 15\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$		75		pF
Output Capacitance	C_{oss}			18		
Reverse Transfer Capacitance	C_{rss}			6		
Total Gate Charge	Q_g	$V_{DS} = 15\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 1.3\text{ A}$		1.85	2.8	nC
		$V_{DS} = 15\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 1.3\text{ A}$		0.91	1.4	
Gate-Source Charge	Q_{gs}			0.51		
Gate-Drain Charge	Q_{gd}			0.3		
Gate Resistance	R_g	$f = 1\text{ MHz}$		4.5	Ω	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}$, $R_L = 12.5\text{ }\Omega$ $I_D \cong 1.2\text{ A}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 1\text{ }\Omega$		15	25	ns
Rise Time	t_r			50	75	
Turn-Off Delay Time	$t_{d(off)}$			7	15	
Fall Time	t_f			15	25	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}$, $R_L = 12.5\text{ }\Omega$ $I_D \cong 1.2\text{ A}$, $V_{GEN} = 10\text{ V}$, $R_g = 1\text{ }\Omega$		5	10	
Rise Time	t_r			10	15	
Turn-Off Delay Time	$t_{d(off)}$			10	15	
Fall Time	t_f			6	12	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$			1	A
Pulse Diode Forward Current	I_{SM}				4	
Body Diode Voltage	V_{SD}	$I_S = 1.2\text{ A}$, $V_{GS} = 0\text{ V}$		0.85	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 1.2\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^{\circ}\text{C}$		20	40	ns
Body Diode Reverse Recovery Charge	Q_{rr}			18	36	nC
Reverse Recovery Fall Time	t_a			16		ns
Reverse Recovery Rise Time	t_b			4		

Notes:

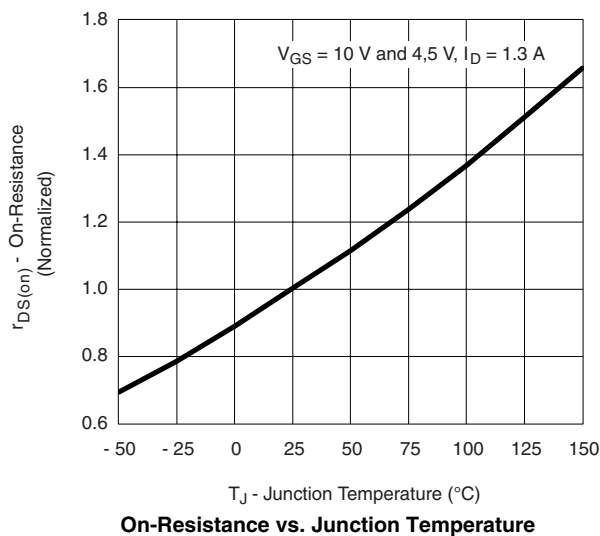
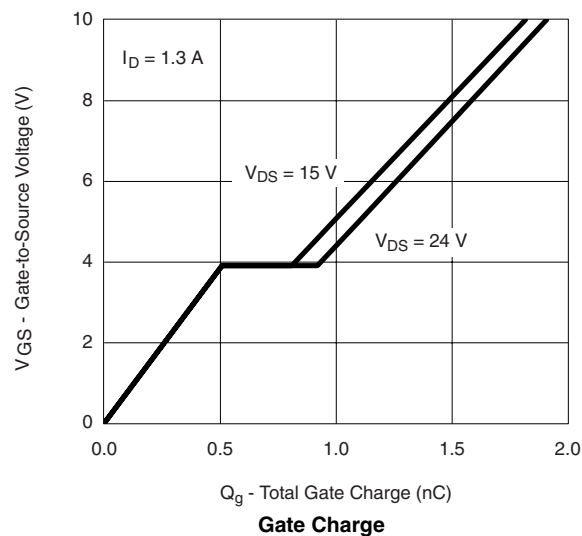
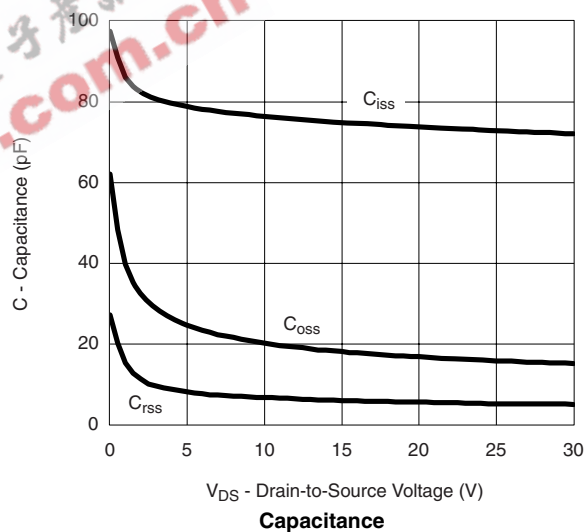
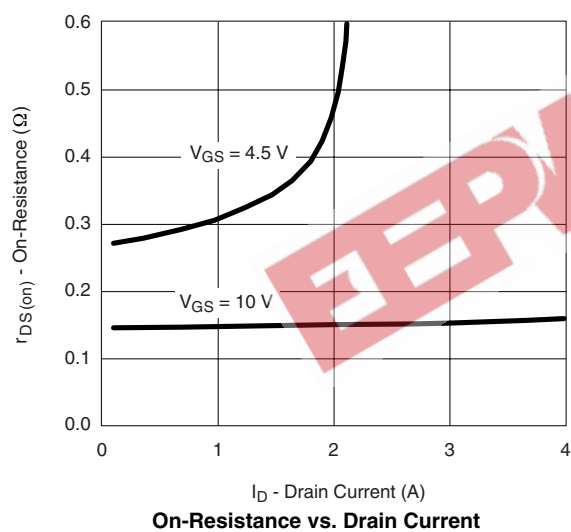
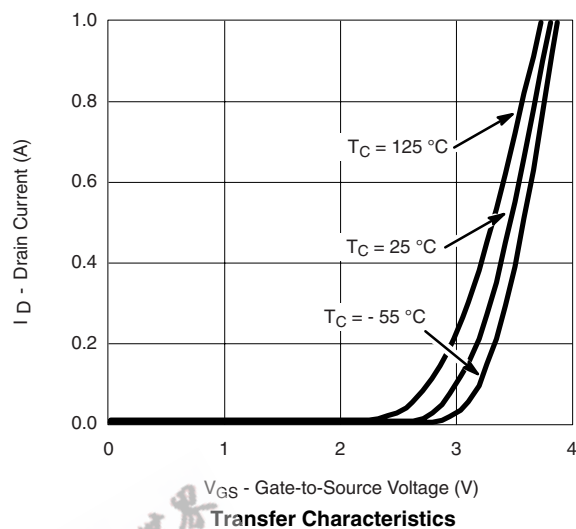
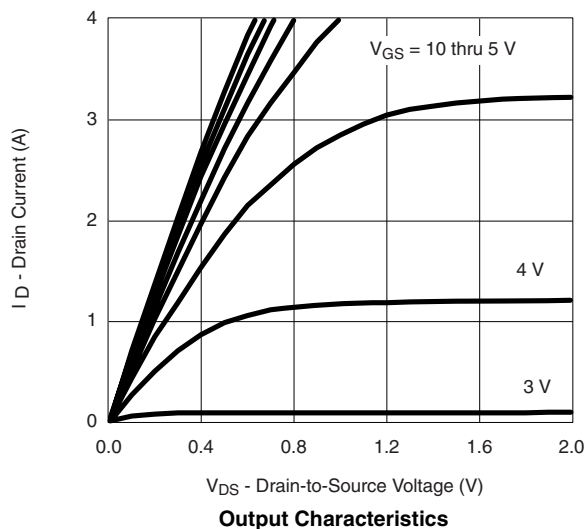
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

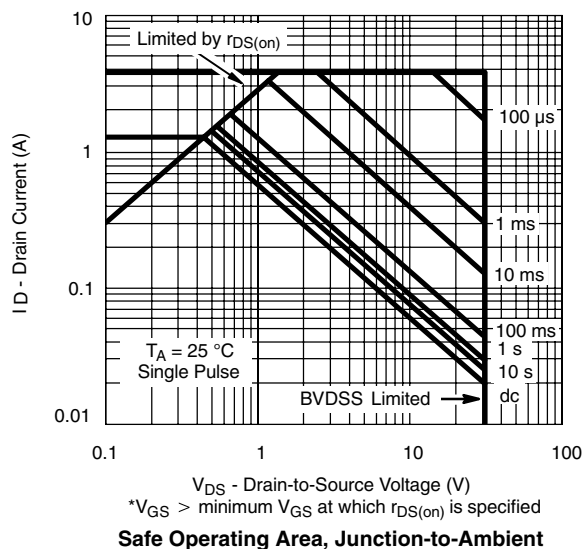
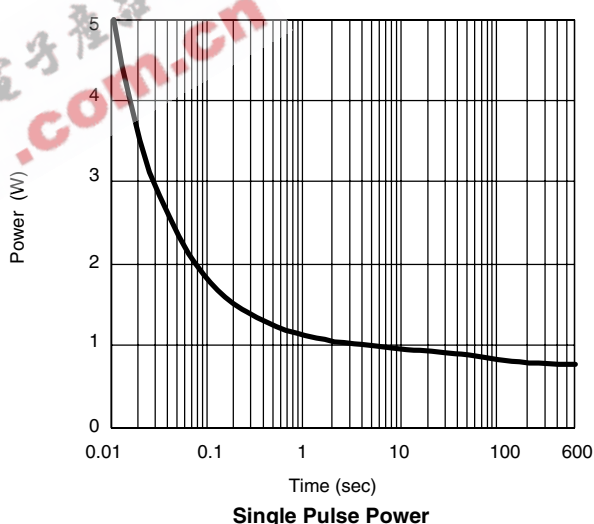
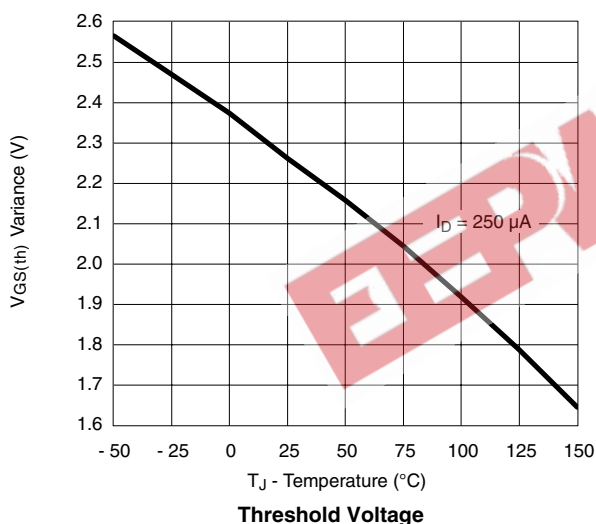
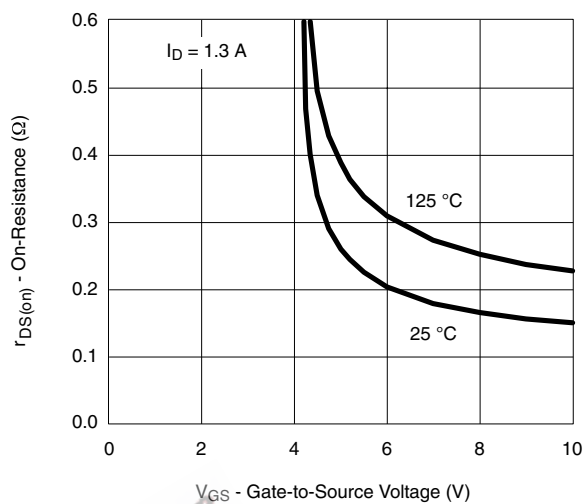
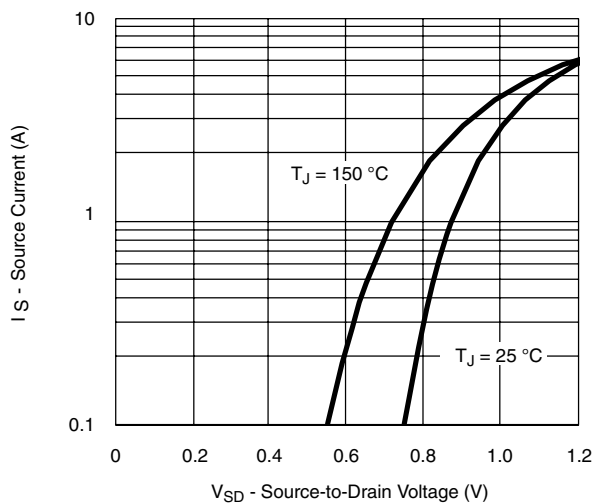


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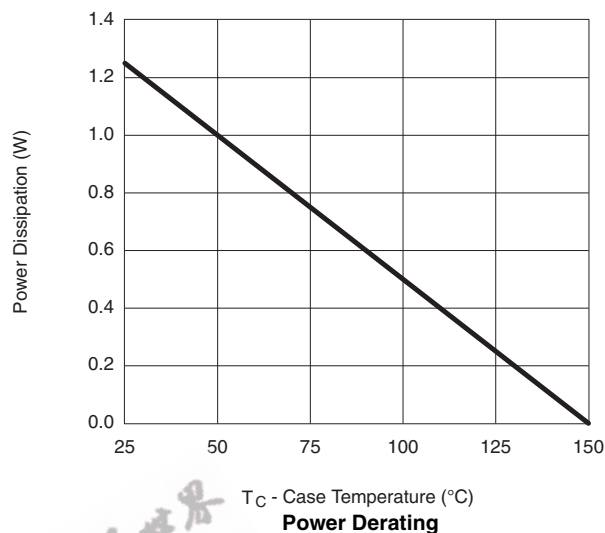
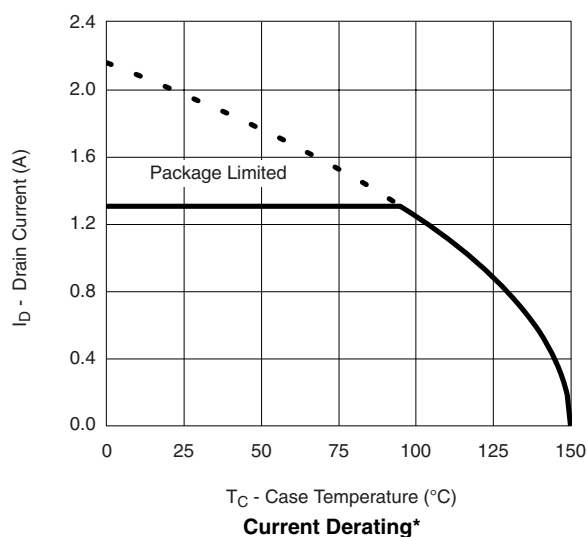


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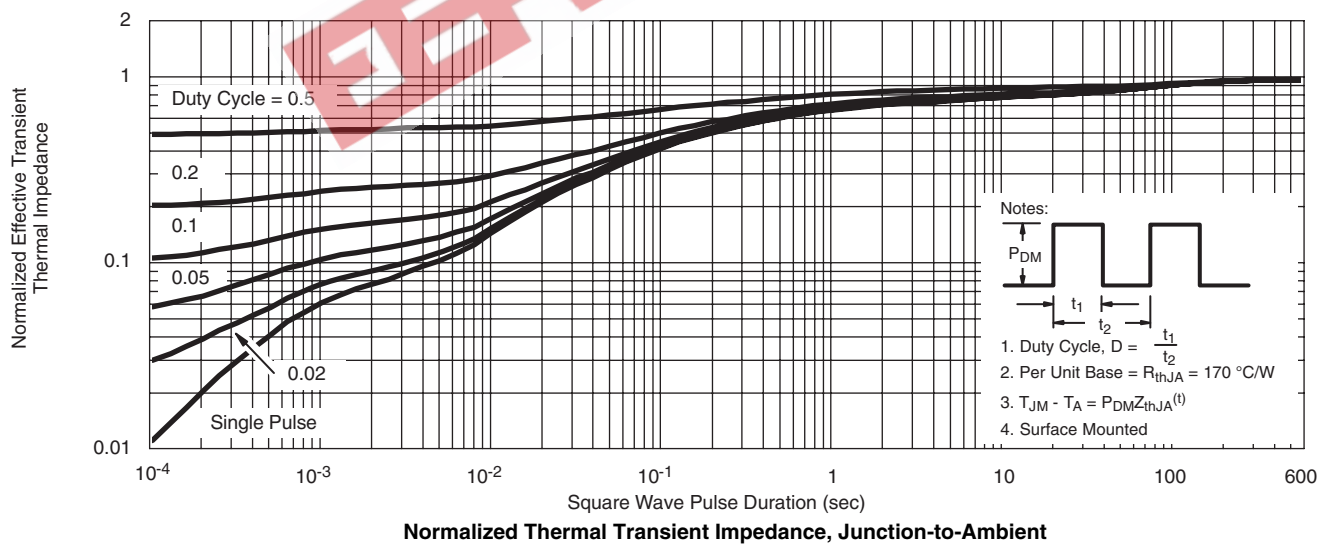




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*The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

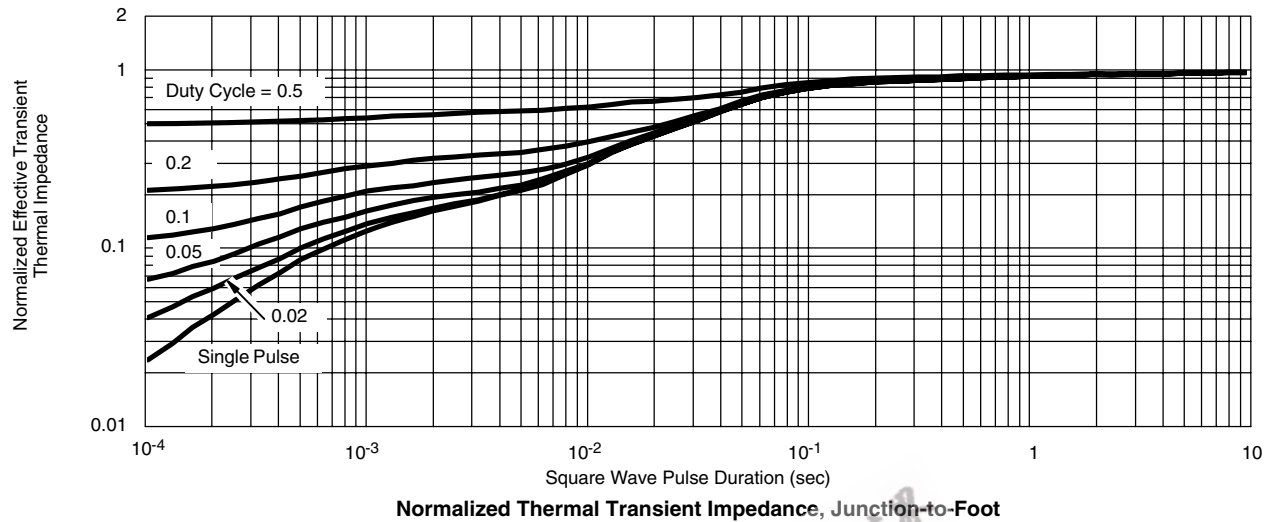


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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?74398>.



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