



New Product

Si1472DH

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
30	0.057 at $V_{GS} = 10$ V	5.6 ^a	5.5
	0.082 at $V_{GS} = 4.5$ V	4.7	

FEATURES

- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested

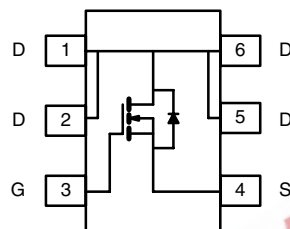
APPLICATIONS

- Load Switch for Portable Devices



RoHS
COMPLIANT

SOT-363
SC-70 (6-LEADS)



Top View

Marking Code



Lot Traceability
and Date Code

Part # Code

Ordering Information: Si1472DH-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	5.6	A
		4.5	
		4.2 ^{b, c}	
		3.4 ^{b, c}	
Pulsed Drain Current	I_{DM}	15	mJ
Avalanche Current	I_{AS}	10	
Repetitive Avalanche Energy	E_{AS}	5	
Continuous Source-Drain Diode Current	I_S	2.3	A
		1.3 ^{b, c}	
Maximum Power Dissipation ^a	P_D	2.8	W
		1.8	
		1.5 ^{b, c}	
		1.0 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	60	80	$^\circ\text{C/W}$
Maximum Junction-to-Foot (Drain)	R_{thJF}	34	45	

Notes:

- Based on $T_C = 25^\circ\text{C}$.
- Surface Mounted on 1" x 1" FR4 board.
- $t = 5$ sec.
- Maximum under Steady State conditions is 125°C/W .

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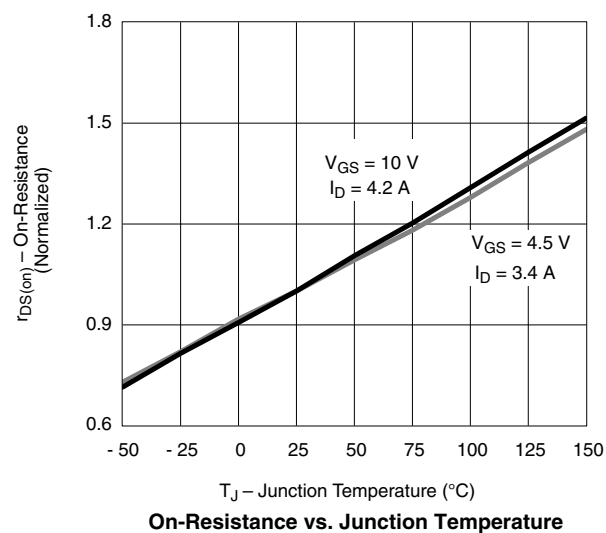
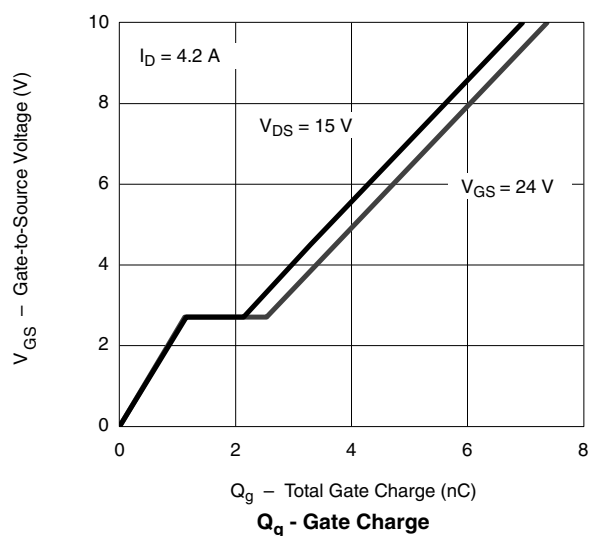
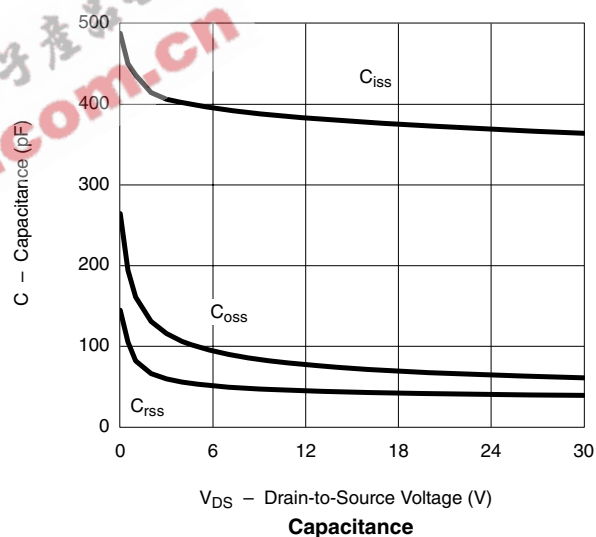
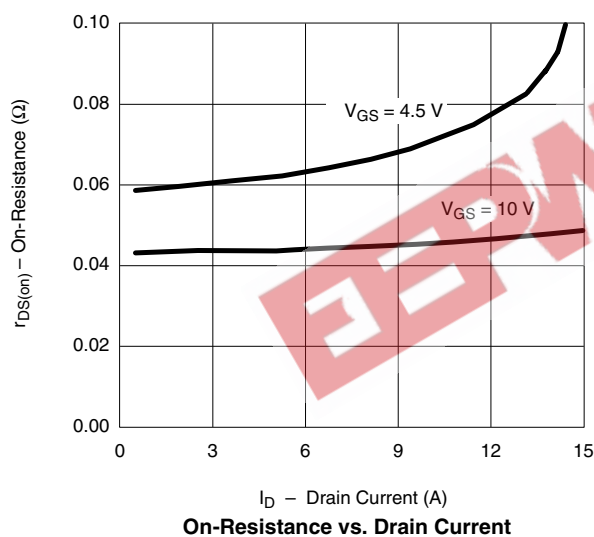
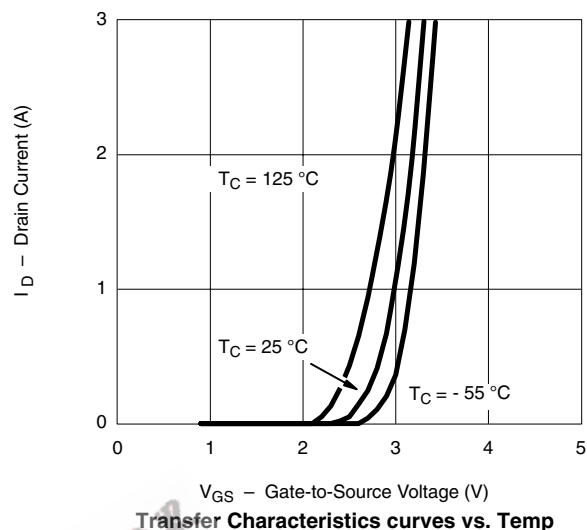
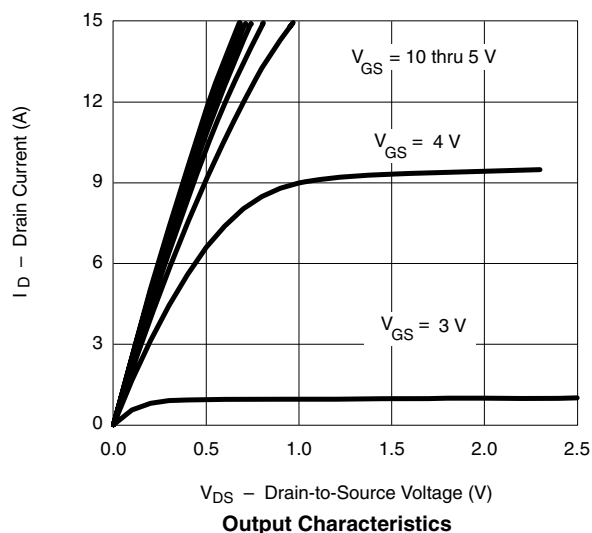
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = 250\text{ }\mu\text{A}$	30			V	
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		25.15		mV/ $^{\circ}\text{C}$	
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			5.6			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	1		3	V	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 20\text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$			1	nA	
		$V_{DS} = 30\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 85\text{ }^{\circ}\text{C}$			10	μA	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}$, $V_{GS} = 10\text{ V}$	15			A	
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10\text{ V}$, $I_D = 4.2\text{ A}$		0.046	0.057	Ω	
		$V_{GS} = 4.5\text{ V}$, $I_D = 3.5\text{ A}$		0.065	0.082		
Forward Transconductance	g_{fs}	$V_{DS} = 15\text{ V}$, $I_D = 4.2\text{ A}$		8.5		S	
Dynamic ^b							
Input Capacitance	C_{iss}	$V_{DS} = 15\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$		380		pF	
Output Capacitance	C_{oss}			75			
Reverse Transfer Capacitance	C_{rss}			45			
Total Gate Charge	Q_g	$V_{DS} = 15\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 4.2\text{ A}$		7	11	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 24\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 4.2\text{ A}$		3.3	5		
Gate-Drain Charge	Q_{gd}			1.2			
Gate Resistance	R_g	$f = 1\text{ MHz}$		1.0			
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}$, $R_L = 4.4\text{ }\Omega$ $I_D \cong 3.4\text{ A}$, $V_{GEN} = 10\text{ V}$, $R_g = 1\text{ }\Omega$		7.1	10.6	Ω	
Rise Time	t_r			7.0	11		ns
Turn-Off DelayTime	$t_{d(off)}$			56	84		
Fall Time	t_f			18	27		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}$, $R_L = 5.4\text{ }\Omega$ $I_D \cong 2.8\text{ A}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 1\text{ }\Omega$		5.5	9	ns	
Rise Time	t_r			15	23		
Turn-Off DelayTime	$t_{d(off)}$			95	143		
Fall Time	t_f			12	18		
Drain-Source Body Diode Characteristics							
Continous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$			2.3	A	
Pulse Diode Forward Current ^a	I_{SM}				15		
Body Diode Voltage	V_{SD}	$I_S = 1.8\text{ A}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 2.3\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$		12.3	19	nC	
Body Diode Reverse Recovery Charge	Q_{rr}			5	7.5		ns
Reverse Recovery Fall Time	t_a			7.6			
Reverse Recovery Rise Time	t_b			4.7			

Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

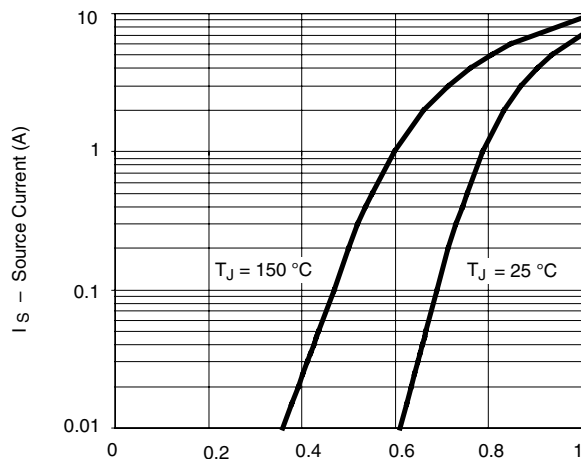
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

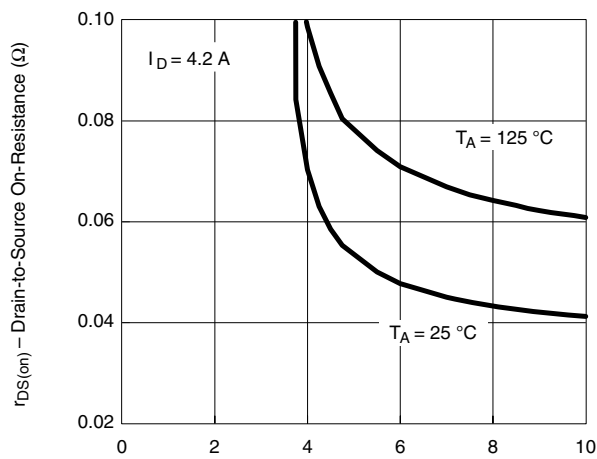
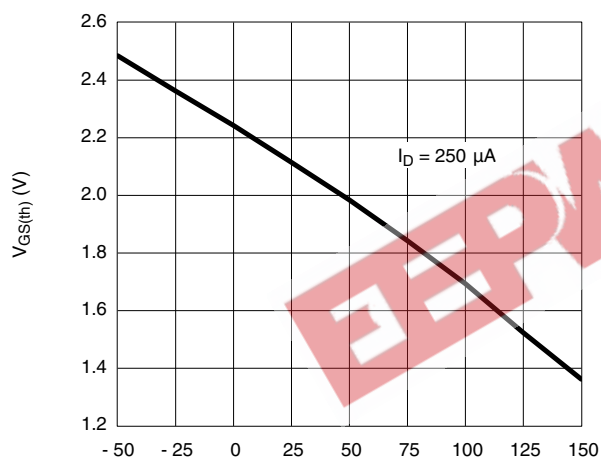
**TYPICAL CHARACTERISTICS** $T_A = 25^\circ\text{C}$, unless otherwise noted

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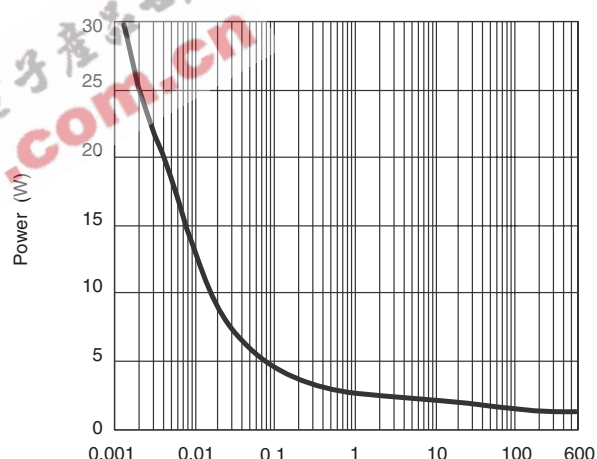
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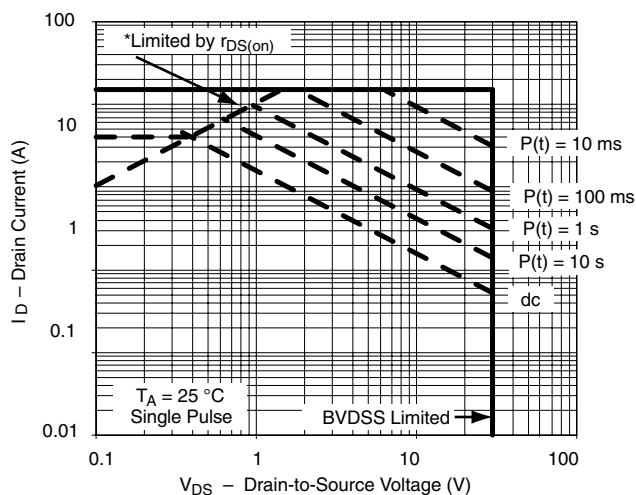
Source-Drain Diode Forward Voltage

 $r_{DS(on)}$ vs V_{GS} vs Temperature

Threshold Voltage



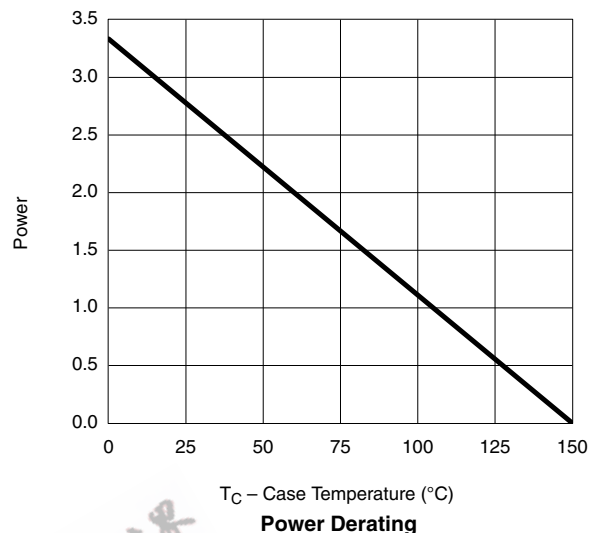
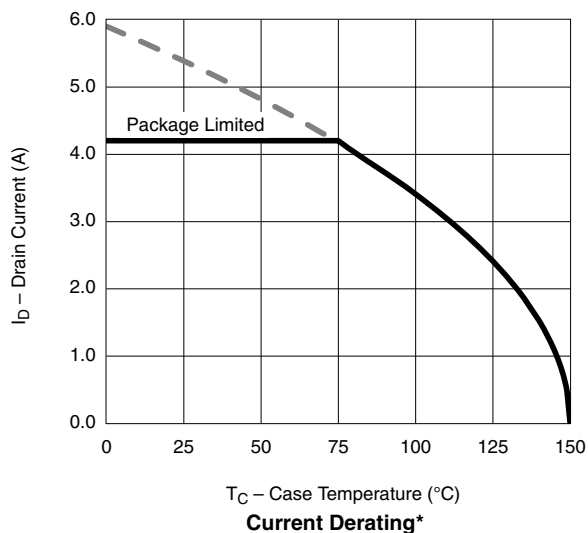
Single Pulse Power



Safe Operating Area, Junction-to-Ambient



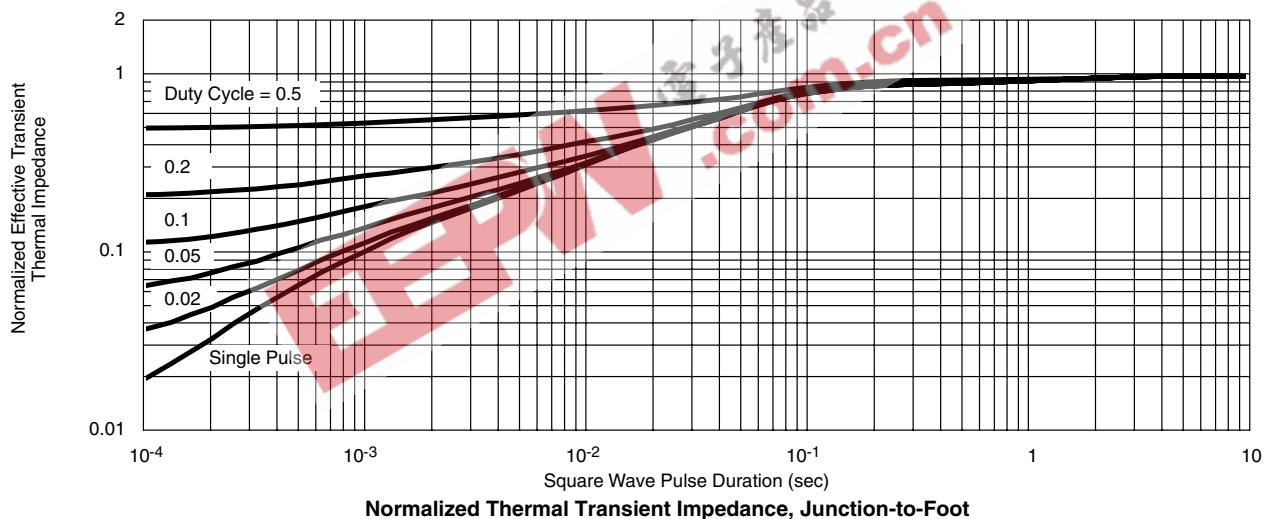
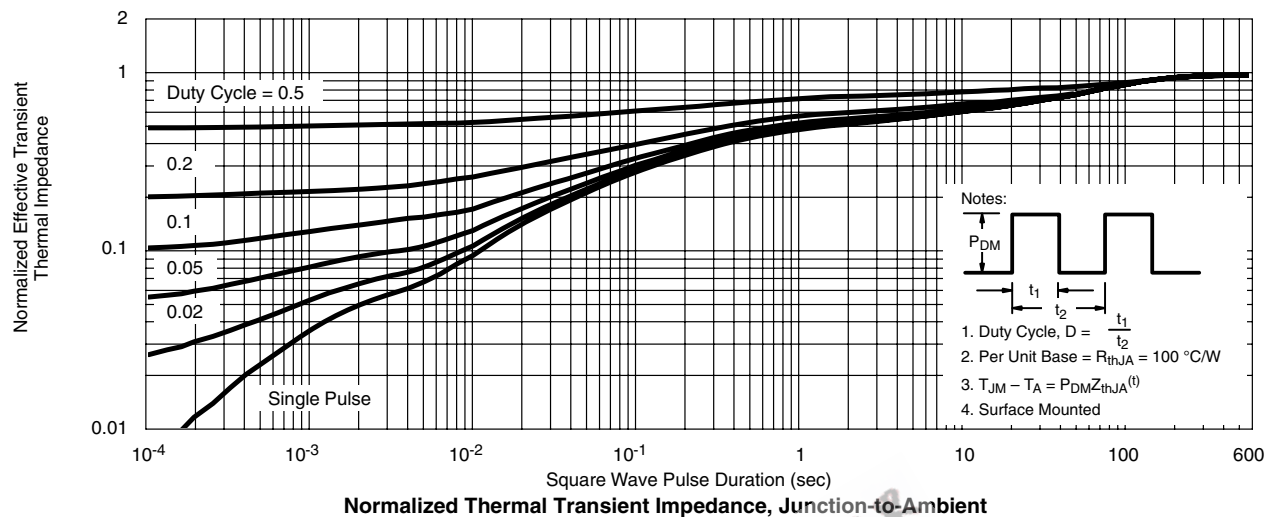
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* The power dissipation P_D is based on $T_{J(max)} = 150\text{ }^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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