

FC143 NPN Epitaxial Planar Silicon Composite Transistor Switching Applications (with Bias Resistance)

Applications

• Switching circuits, inverter circuits, interface circuits, driver circuits.

Features

- · On-chip bias resistance (R1=4.7k Ω , R2=10k Ω).
- Composite type with 2 transistors contained in the CP package currently in use, improving the mounting efficiency greatly.
- The FC143 is formed with two chips, being equivalent to the 2SC4360, placed in one package.
- · Excellent in thermal equilibrium and pair capability.

Electrical Connection

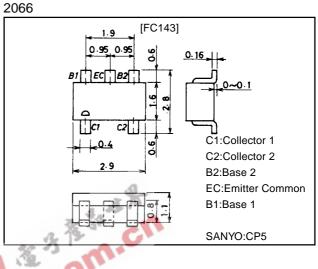


Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm



Parameter		Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage		VCBO		50	V
Collector-to-Emitter Voltage		VCEO		50	V
Emitter-to-Base Voltage		VEBO		6	V
Collector Current		IC		100	mA
Peak Collector Current		ICP		200	mA
Collector Dissipation		PC	1 unit	200	mW
Total Dissipation		PΤ		300	mW
Junction Temperature		Tj		150	°C
Storage Temperature		Tstg		-55 to +150	°C

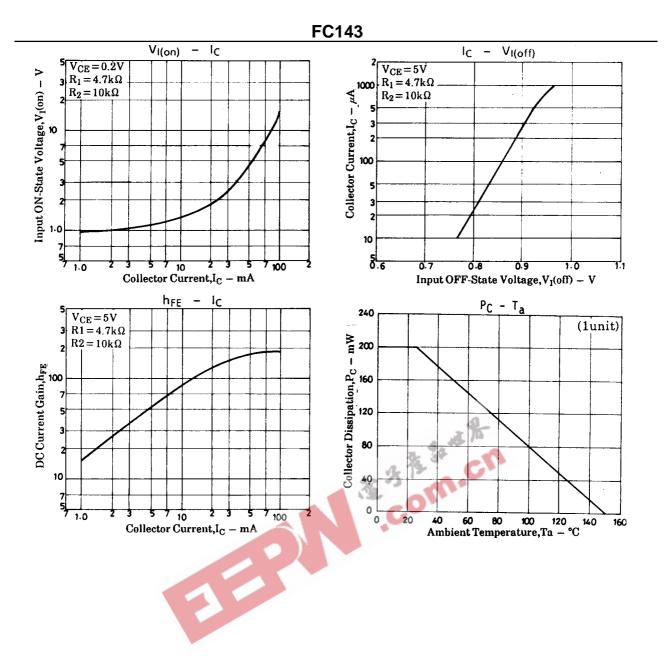
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditons	Ratings			Unit
Falameter			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0			0.1	μΑ
Collector Cutoff Current	ICEO	V _{CE} =40V, I _E =0			0.5	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =5V, I _C =0	262	340	485	μΑ
DC Current Gain	hFE	V _{CE} =5V, I _C =10mA	50			
Gain-Bandwidth Product	fT	V _{CE} =10V, I _C =5mA		250		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		3.3		pF
C-E Saturation Voltage	V _{CE(sat)}	I _C =10mA, I _B =0.5mA		0.1	0.3	V
C-B Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0	50			V
C-E Breakdown Voltage	V(BR)CEO	I _C =100µA, R _{BE} =∞	50			V
Input OFF-State Voltage	V _{I(off)}	V _{CE} =5V, I _C =100µA	0.7	0.85	0.95	V
Input ON-State Voltage	V _{I(on)}	V _{CE} =0.2V, I _C =10mA	0.95	1.3	2.0	V
Input Resistance	R1		3.3	4.7	6.1	kΩ
Resistance Ratio	R1/R2			0.47		

Note: The specifications shown above are for each individual transistor.

Marking:143

SANYO Electric Co., Ltd. Semiconductor Bussiness Headquaters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN



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