

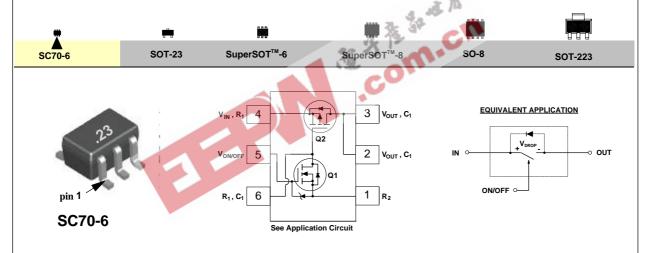
FDG6323L Integrated Load Switch

General Description

This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 0.6A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SC70-6 package.

Features

- $V_{DROP}=0.2V @ V_{IN}=5V$, $I_L=0.36A$. $R_{(ON)}=0.55\Omega$ $V_{DROP}=0.2V @ V_{IN}=2.5V$, $I_L=0.27A$. $R_{(ON)}=0.75\Omega$.
- Very small package outline SC70-6.
- Control MOSFET (Q1) includes Zener protection for ESD ruggedness (>6KV Human Body Model).
- High density cell design for extremely low on-resistance.
- Compact industry standard SC70-6 surface mount package.



Symbol	Parameter	FDG6323L	Units
V _{IN}	Input Voltage Range	2.5 - 8	V
V _{ON/OFF}	On/Off Voltage Range	1.5 - 8	V
I _L	Load Current - Continuous (Note 1)	0.6	A
	- Pulsed (Note 1 & 3)	1.8	
)	Maximum Power Dissipation (Note 2)	0.3	W
T_,T _{stg}	Operating and Storage Temperature Range	-55 to 150	°C
ESD	Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm)	6	kV
THERMA	L CHARACTERISTICS		•
R _{eja}	Thermal Resistance, Junction-to-Ambient (Note 2)	415	°C/W

Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHA	RACTERISTICS	·				•
I _{FL}	Forward Leakage Current	$V_{IN} = 8 V, V_{ONOFF} = 0 V$			1	μA
ON CHAR	ACTERISTICS (Note 3)					
VDROP	Conduction Voltage Drop	$V_{IN} = 5 \text{ V}, \ V_{ONOFF} = 3.3 \text{ V}, \ I_L = 0.36 \text{ A}$		0.14	0.2	V
		$V_{IN} = 2.5 \text{ V}, V_{ONOFF} = 3.3 \text{ V}, I_{L} = 0.27 \text{ A}$		0.15	0.2	
R _(ON)	Q2 - Static On-Resistance	$V_{GS} = -5 V, I_{D} = -0.6 A$		0.41	0.55	Ω
		$V_{GS} = -2.5 \text{ V}, \ I_{D} = -0.5 \text{ A}$		0.58	0.75	
I _L	Load Current	$V_{DROP} = 0.2 \text{ V}, V_{IN} = 5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}$	0.36			Α
		$V_{DROP} = 0.2 \text{ V}, V_{IN} = 2.5 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}$	0.27]

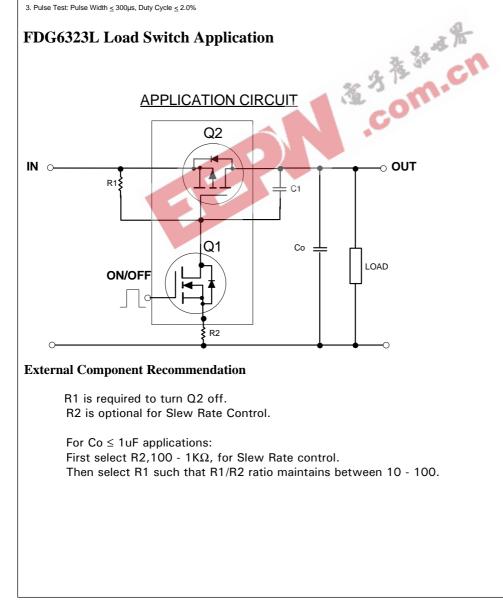
Notes

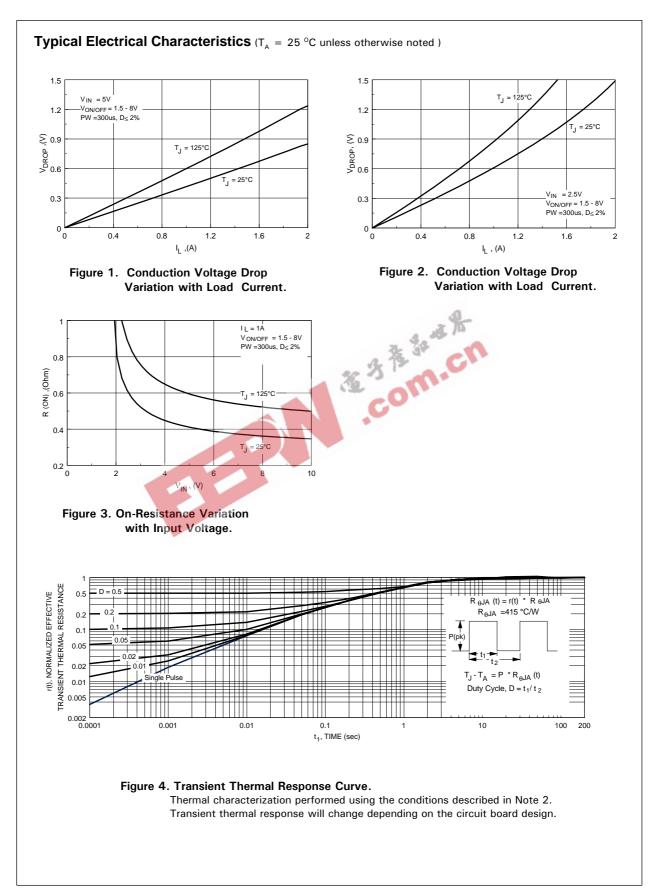
1. Range of V_{in} can be up to 8V, but R_1 and R_2 must be scaled such that V_{GS} of Q2 does not exceed -8V.

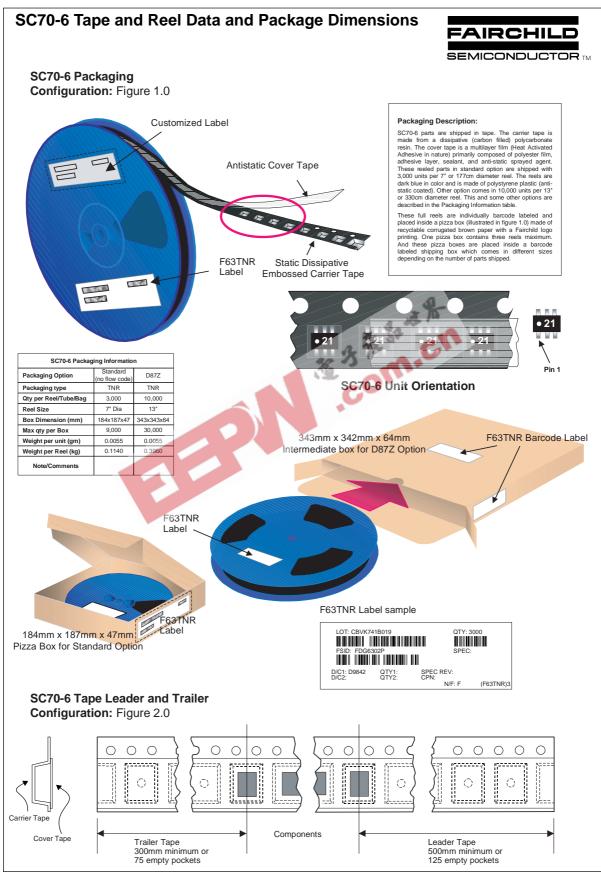
2. Rain is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.

 $R_{a_{NC}}$ is guaranteed by design while $R_{a_{CA}}$ is determined by the user's board design.

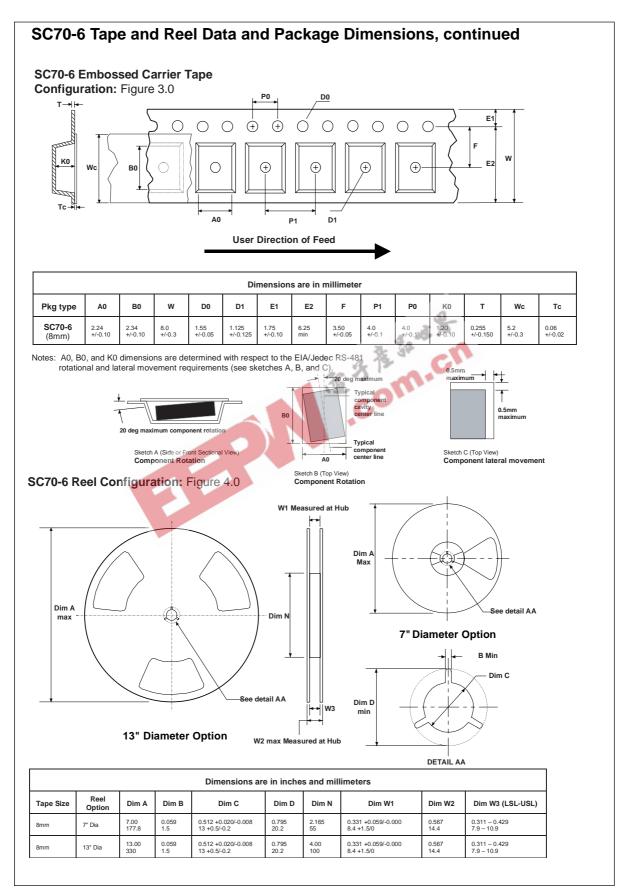
3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2.0%

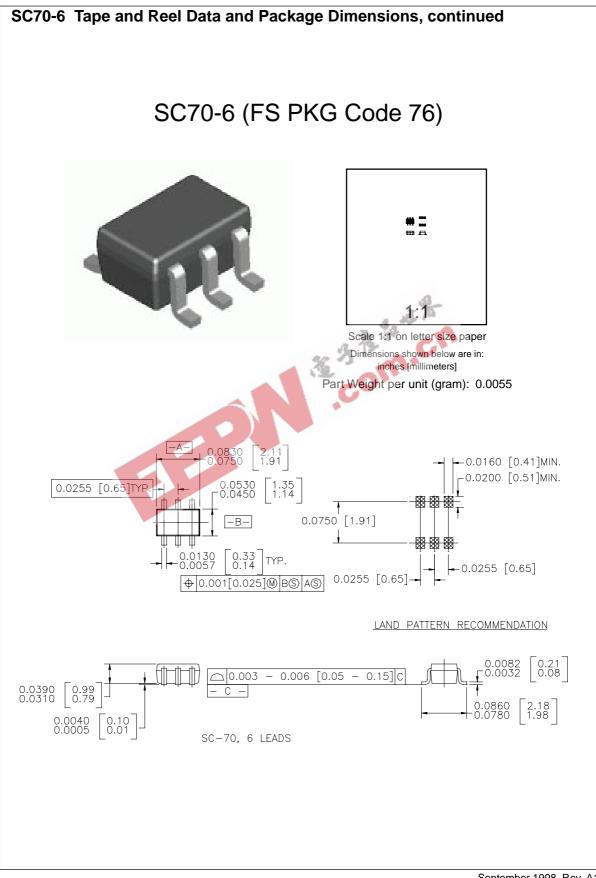






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