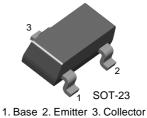
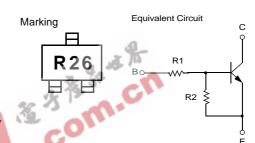


FJV3106R

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R_1=10K\Omega$, $R_2=47K\Omega$)
- Complement to FJV4106R





NPN Epitaxial Silicon Transistor

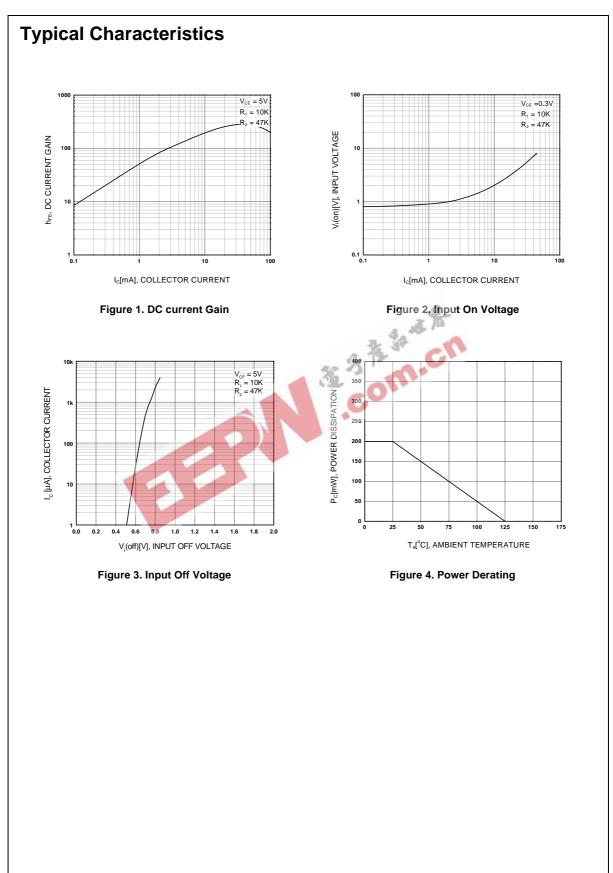
Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	10	V
lc	Collector Current	100	mA
c c	Collector Power Dissipation	200	mW
Γ _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C}=10\mu A, I_{E}=0$	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50			V
I _{CBO}	Collector Cut-off Current	V _{CB} =40V, I _E =0			0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} =5V, I_{C} =5mA	68			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
C _{ob}	Output Capacitance	V _{CE} =10mA, I _E =0 f=1.0MHz		3.7		pF
f _T	Current Gain Bandwidth Product	V _{CB} =10V, I _C =5mA		250		MHz
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.3			V
V _I (on)	Input On Voltage	V_{CE} =0.3V, I_{C} =1mA			1.4	V
R ₁	Input Resistor		7	10	13	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.19	0.21	0.24	

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Package Dimensions SOT-23 0.20 MIN 0.45~0.60 0.40 ± 0.03 1.30 ±0.10 0.03~0.10 0.38 REF $0.12^{\,+0.05}_{\,-0.023}$ 0.40 ±0.03 0.96~1.14 2.90 ±0.10 0.97REF 0.95 ±0.03 0.95 ±0.03 1.90 ±0.03 0.508REF Dimensions in Millimeters

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CoolFET™	FASTr™	MicroFET™	PowerTrench®	SuperSOT™-6
CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
DOME™	GlobalOptoisolator™	MICROWIRE™	QS™	SyncFET™
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E ² CMOS TM	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	I ² C TM	OCX™	RapidConfigure™	UHC™
Across the board	. Around the world.™	OCXPro™	RapidConnect™	UltraFET [®]
The Power Franc	hise™	OPTOLOGIC®	SILENT SWITCHER®	VCX™
Programmable A	ctive Droop™	OPTOPLANAR™	SMART START™	

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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