Silicon Schottky Barrier Diode for System Protection

HITACHI

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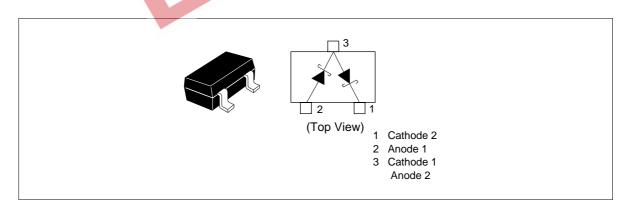
Features

- Low V_F and high efficiency.
- HSM107S which is interconnected in series configuration is designed for protection from not only
 external excessive voltage but also miss-operation on electric systems.
- MPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSM107S	C5	MPAK

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Value	Unit
Reverse voltage	V_R	8	V
Peak forward current	I _{FM}	0.1	A
Non-Repetitive Peak forward surge current	I _{FSM} *1	0.5	A
Average forward current	Io	50	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

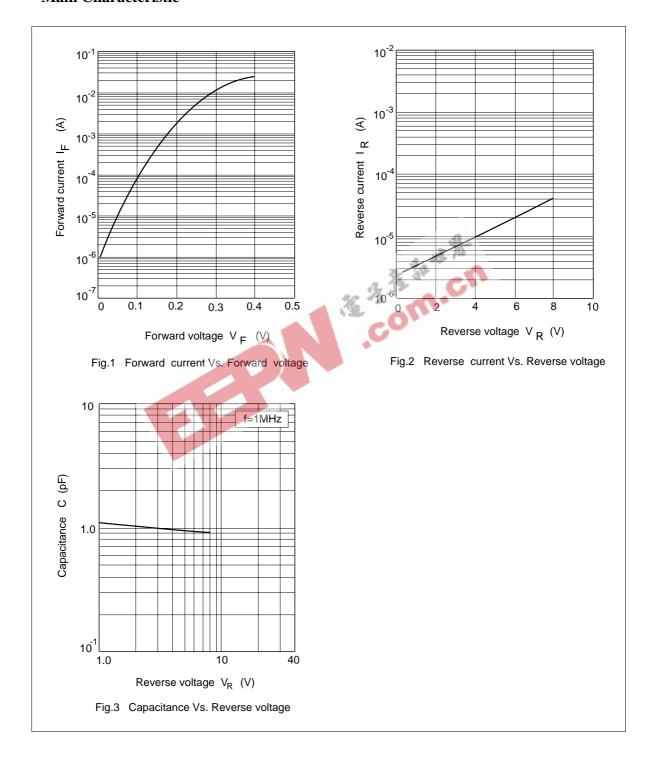
Notes: 1. Square wave, 10ms

Electrical Characteristics (Ta = 25^{\circ}C)

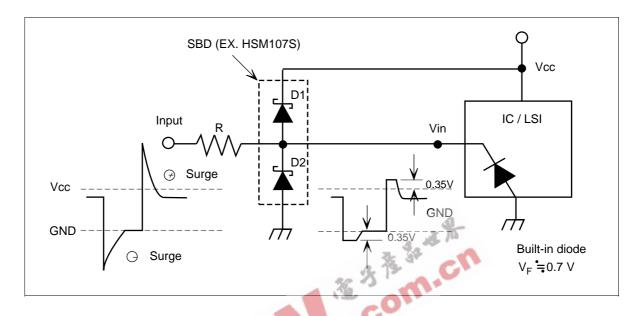
Electrical Characteristics (Ta = 25°C)						
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse voltage	V_R	8	_	1		I _R = 1.0 mA
Reverse current	I _R	_		30	μА	V _R = 5 V
Forward voltage	V_{F}	-	7/1	0.3	V	I _F = 10 mA
ESD Capability *1	-	100	7		V	C=200pF, Both forward and reverse direction 1 pulse

Notes: 1. Failure Criterion ; I_R ≥60μA at V_R=5V

Main Characteristic

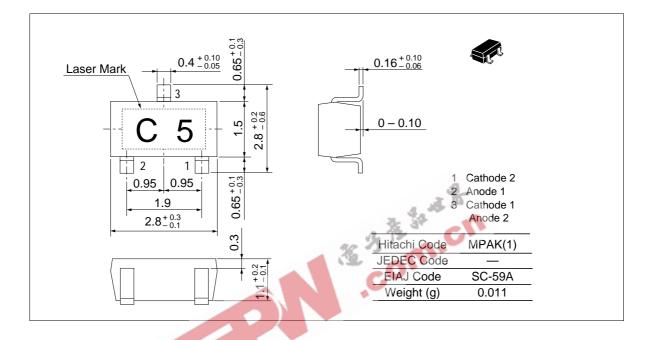


Example of application circuite



Package Dimensions

Unit: mm



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