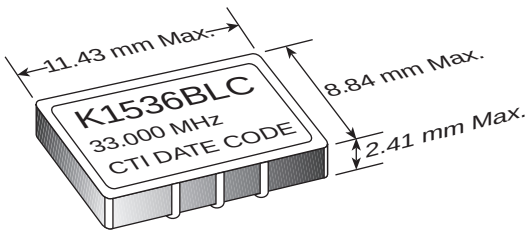
K1536BLC

LOW COST SURFACE MOUNT VCXOS

- 2.0 to 55 MHz Frequency Range
- 0.3V to 3.0V Control Voltage Range
- Tri-state Function Option for Automatic Testing
- –40°C to 85°C Operating Temperature
- Tape and Reel Packaging
- General PLL Applications
- Small Package Size; Industry-Standard Pad Footprint
- A Cost-Effective, SMT Alternative
- 3.3V operation



MECHANICAL DIMENSIONS

ELECTRICAL SPECIFICATIONS

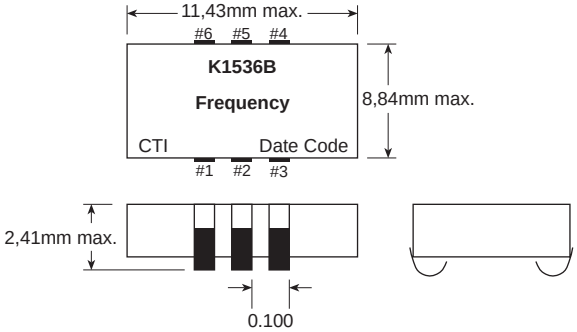
MODEL	K1536BLC	
Parameters		
Frequency Range (MHz)	2 to 55	
Temperature Range	0°C to 70°C	–40°C to 85°C
Storage Temp.	–55°C to +125°C	
Frequency Stability (ppm)	25ppm(typical)	50ppm(typical)
Total Pull Range (Typical)	±120 ppm	
Minimum Tuning Limit (MTL)*	± 60ppm	± 50ppm
Linearity	<10%	
Modulation Bandwidth (±3dB)	> 20 KHz	
Nominal Control Voltage	+1.65V	
Voltage Control Range	0.3V to 3.0V	
Transfer Function	Positive	
Input Impedance	>50 ohms @ 10 KHz	
Supply Voltage	+3.3 ± 10%	
Input Current	< 30 ma @ 55mhz	
Output	CMOS	

PIN	Function
1	Control Voltage
2	Tri-state
3	Ground
4	Output
5	No Connection
6	Vcc

SAMPLE ORDERING INFORMATION

Quantity:	One
Model No:	K1536BLC “M” 40.000MHz
	“Blank” — 0° to 70°C (M). “M” –40° to +85°C

* MTL > (Total Pull Range) – (Frequency variations due to calibration, temperature, power supply, and aging)



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