M1MA141WKT1, M1MA142WKT1

Preferred Device

Common Cathode Silicon Dual Switching Diode

This Common Cathode Silicon Epitaxial Planar Dual Diode is designed for use in ultra high speed switching applications. This device is housed in the SC-70 package which is designed for low power surface mount applications.

Features

- Fast t_{rr} , < 3.0 ns
- Low C_D, < 2.0 pF
- Pb–Free Packages are Available

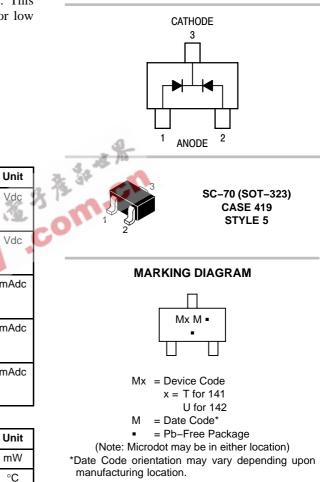
MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating



ON Semiconductor®

http://onsemi.com



ORDERING INFORMATION

Device	Package	Shipping [†]
M1MA141WKT1	SC-70	3000/Tape & Reel
M1MA141WKT1G	SC-70 (Pb-Free)	3000/Tape & Reel
M1MA142WKT1	SC-70	3000/Tape & Reel
M1MA142WKT1G	SC-70 (Pb-Free)	3000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

Reverse Voltage M1MA141WKT1 M1MA142WKT1	V _R	40 80	Vdc
Peak Reverse Voltage M1MA141WKT1 M1MA142WKT1	V _{RM}	40 80	Vdc
Forward Current Single Dual	I _F	100 150	mAdc
Peak Forward Current Single Dual	I _{FM}	225 340	mAdc
Peak Forward Surge Current M1MA141WKT1 M1MA142WKT1	I _{FSM} (Note 1)	500 750	mAdc

Symbol

Value

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	PD	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. t = 1 SEC

M1MA141WKT1, M1MA142WKT1

ELECTRICAL CHARACTERISTICS (T_A = 25° C)

Characteristic		Condition	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current	M1MA141WKT1 M1MA142WKT1	V _R = 35 V V _R = 75 V	I _R	-	0.1	μAdc
Forward Voltage		I _F = 100 mA	V _F	-	1.2	Vdc
Reverse Breakdown Voltage	M1MA141WKT1 M1MA142WKT1	I _R = 100 μA	V _R	40 80	-	Vdc
Diode Capacitance		V _R = 0, f = 1.0 MHz	CD	-	2.0	pF
Reverse Recovery Time (Figure 1)		I_F = 10 mA, V_R = 6.0 V, R _L = 100 Ω, I_{rr} = 0.1 I_R	t _{rr} (Note 2)	-	3.0	ns

2. t_{rr} Test Circuit



M1MA141WKT1, M1MA142WKT1

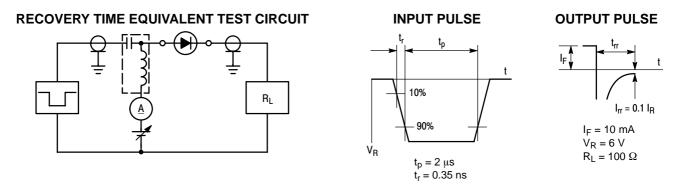


Figure 1. Recovery Time Equivalent Test Circuit

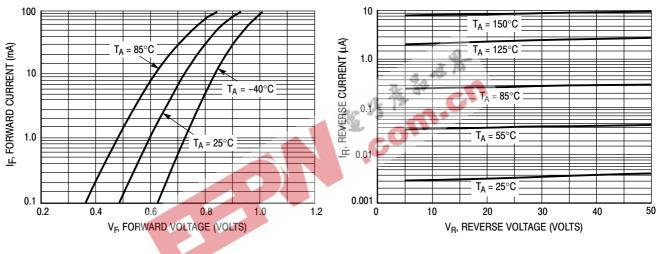


Figure 2. Forward Voltage

Figure 3. Reverse Current

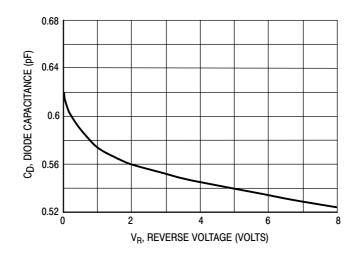
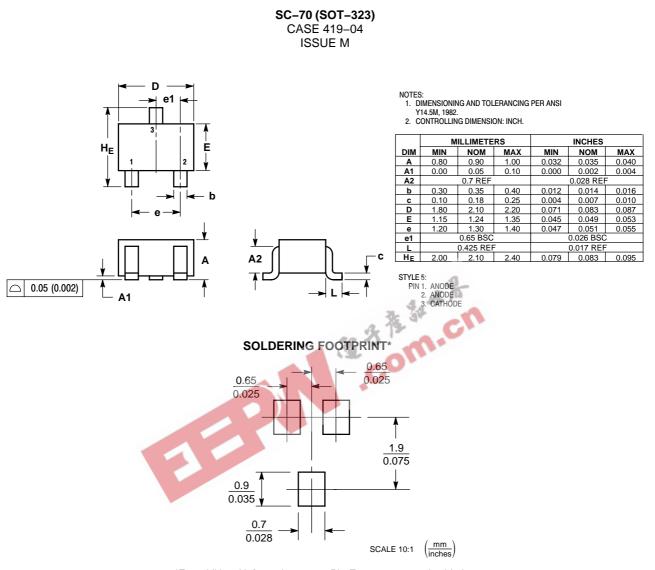


Figure 4. Diode Capacitance

M1MA141WKT1, M1MA142WKT1

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and with a registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product create a stuation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent free design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850 ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.