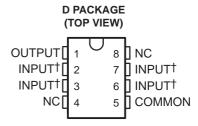
MC79L00 SERIES NEGATIVE-VOLTAGE REGULATORS

SLVS011D - OCTOBER 1982 - REVISED AUGUST 2003

- 3-Terminal Regulators
- Output Current Up To 100 mA
- No External Components Required
- Internal Thermal-Overload Protection
- Internal Short-Circuit Current Limiting
- Direct Replacement for Industry-Standard MC79L00 Series
- Available in 5% or 10% Selections

description/ordering information

This series of fixed negative-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These include on-card regulation for elimination of noise and distribution problems associated with single-point



† Internally connected NC – No internal connection

LP PACKAGE (TOP VIEW)



regulation. In addition, they can be used to control series pass elements to make high-current voltage-regulator circuits. One of these regulators can deliver up to 100 mA of output current. The internal current-limiting and thermal-shutdown features essentially make the regulators immune to overload. When used as a replacement for a Zener-diode and resistor combination, these devices can provide an effective improvement in output impedance of two orders of magnitude, with lower bias current.

ORDERING INFORMATION

TJ	OUTPUT VOLTAGE TOLERANCE	NOMINAL OUTPUT VOLTAGE (V)	PACKAG	Ε [†]	ORDERABLE PART NUMBER	TOP-SIDE MARKING	
	1		SOIC (D)	Tube of 75	MC79L05ACD	79L05A	
		5	1 301C (D)	Reel of 2500	MC79L05ACDR	792057	
		_5	TO 2000 / TO 200 // P)		MC79L05ACLP	701.0540	
			TO-226 / TO-92 (LP)	Reel of 2000	MC79L05ACLPR	79L05AC	
		-12	COIC (D)	Tube of 75	MC79L12ACD	701.404	
	5%		SOIC (D)	Reel of 2500	MC79L12ACDR	79L12A	
0°C to 125°C		-12			MC79L12ACLP	79L12AC	
			TO-226 / TO-92 (LP)	Reel of 2000	MC79L12ACLPR		
				Bulk of 1000	MC79L15ACLP		
		-15	TO-226 / TO-92 (LP)	Ammo of 2000	MC79L15ACLPM	79L15AC	
				Reel of 2000	MC79L15ACLPR		
	400/	-12	TO-226 / TO-92 (LP)	Bulk of 1000	MC79L12CLP	79L12C	
	10%	-15	SOIC (D)	Tube of 75	MC79L15CD	79L15C	

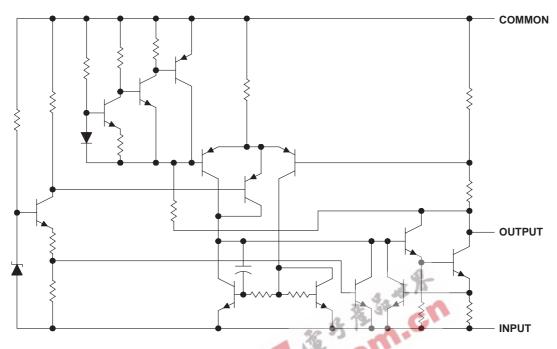
[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



equivalent schematic



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Input voltage: MC79L05		
MC79L12, MC79L15		
Package thermal impedance, θ _{JA} (see Notes 1 an	nd 2): D package	97°C/W
	LP package	140°C/W
Operating free-air, case, or virtual junction temperation	ature	150°C
Lead temperature 1.6 mm (1/16 inch) from case for	or 10 seconds	260°C
Storage temperature range, T _{stq}		–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

			MIN	MAX	UNIT
	Input voltage		-7	-20	
VI			-14.5	-27	V
		MC79L15	-17.5	-30	
lo	Output current			100	mA
TJ	Operating virtual junction temperature		0	125	°C



 $NOTES: \quad 1. \quad Maximum power dissipation is a function of T_J(max), \\ \theta_{JA}, \\ and T_{A}. \\ The maximum allowable power dissipation at any allowable ambient$ temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.

^{2.} The package thermal impedance is calculated in accordance with JESD 51-7.

MC79L00 SERIES NEGATIVE-VOLTAGE REGULATORS

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electrical characteristics at specified virtual junction temperature, $V_I = -10 \text{ V}$, $I_O = 40 \text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	т.	MC79L05C			MC79L05AC			UNIT	
PARAIVIETER	TEST CONDITIONS!	TJ	MIN	TYP	MAX	MIN	TYP	MAX		
		25°C	-4.6	- 5	-5.4	-4.8	-5	-5.2		
Output voltage‡	$V_I = -7 \text{ V to } -20 \text{ V},$ $I_O = 1 \text{ mA to } 40 \text{ mA}$	0°C to 125°C	-4.5		-5.5	-4.75		-5.25	V	
	$V_I = -10 \text{ V}, I_O = 1 \text{ mA to } 70 \text{ mA}$	0°C to 125°C	-4.5		-5.5	-4.75		-5.25		
Innut regulation	V _I = −7 V to −20 V	25°C			200			150	mV	
Input regulation	V _I = −8 V to −20 V	7 25 [150			100		
Ripple rejection	$V_I = -8 \text{ V to } -18 \text{ V, f} = 120 \text{ Hz}$	25°C	40	49		41	49		dB	
Outrout regulation	I _O = 1 mA to 100 mA	25°C			60			60	mV	
Output regulation	I _O = 1 mA to 40 mA	25°C	3.0		30			30	IIIV	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		40			40		μV	
Dropout voltage	I _O = 40 mA	25°C		1.7			1.7		V	
Dina aurrant		25°C			6			6	A	
Bias current		125°C		- %-	5.5			5.5	mA	
Bias current change	V _I = −8 V to −20 V	0°C to 135°C	4.4	G- /**	1.5			1.5	mA	
	I _O = 1 mA to 40 mA	0°C to 125°C			0.2			0.1	IIIA	

[†] All characteristics are measured with a 0.33-μF capacitor across the input and a 0.1-μF capacitor across the output. Pulse-testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. ‡ This specification applies only for dc power dissipation permitted by absolute maximum ratings.

electrical characteristics at specified virtual junction temperature, $V_I = -19 \text{ V}$, $I_O = 40 \text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	Τ.	MC79L12C			MC79L12AC			UNIT	
PARAMETER	TEST CONDITIONS!	TJ	MIN	TYP	MAX	MIN	TYP	MAX	ONII	
		25°C	-11.1	-12	-12.9	-11.5	-12	-12.5		
Output voltage‡	$V_I = -14.5 \text{ V to } -27 \text{ V},$ $I_O = 1 \text{ mA to } 40 \text{ mA}$	0°C to 125°C	-10.8		-13.2	-11.4		-12.6	V	
	$V_I = -19 \text{ V}, I_O = 1 \text{ mA to } 70 \text{ mA}$	0°C to 125°C	-10.8		-13.2	-11.4		-12.6		
Input regulation	$V_{\parallel} = -14.5 \text{ V to } -27 \text{ V}$	25°C			250			250	mV	
Input regulation	$V_{I} = -16 \text{ V to } -27 \text{ V}$	25 0		200				200	IIIV	
Ripple rejection	$V_{I} = -15 \text{ V to } -25 \text{ V, f} = 120 \text{ Hz}$	25°C	36	42		37	42		dB	
Output regulation	I _O = 1 mA to 100 mA	25°C			100			100	mV	
Output regulation	I _O = 1 mA to 40 mA	25 C			50			50	IIIV	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		80			80		μV	
Dropout voltage	I _O = 40 mA	25°C		1.7			1.7		V	
Dina aurrant		25°C			6.5			6.5	A	
Bias current		125°C			6			6	mA	
Dies surrent change	V _I = -16 V to -27 V	0°C to 125°C			1.5			1.5	A	
Bias current change	I _O = 1 mA to 40 mA	0°C to 125°C			0.2			0.1	mA	

[†] All characteristics are measured with a 0.33-µF capacitor across the input and a 0.1-µF capacitor across the output. Pulse-testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. ‡ This specification applies only for dc power dissipation permitted by absolute maximum ratings.



MC79L00 SERIES **NEGATIVE-VOLTAGE REGULATORS**

SLVS011D - OCTOBER 1982 - REVISED AUGUST 2003

electrical characteristics at specified virtual junction temperature, $V_I = -23 \text{ V}$, $I_O = 40 \text{ mA}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	т.	MC79L15C			MC	UNIT			
PARAMETER	TEST CONDITIONST	TJ	MIN	TYP	MAX	MIN	TYP	MAX	ONII	
		25°C	-13.8	-15	-16.2	-14.4	-15	-15.6		
Output voltage‡	$V_I = -17.5 \text{ V to } -30 \text{ V},$ $I_O = 1 \text{ mA to } 40 \text{ mA}$	0°C to 125°C	-13.5		-16.5	-14.25		-15.75	V	
ı	$V_I = -23 \text{ V}, I_O = 1 \text{ mA to } 70 \text{ mA}$	0°C to 125°C	-13.5		-16.5	-14.25		-15.75		
Input regulation	V _I = −17.5 V to −30 V	25°C			300			300	mV	
Input regulation	V _I = −17.5 V to −30 V	25°C			250			250	1117	
Ripple rejection	$V_I = -18.5 \text{ V to } -28.5 \text{ V, f} = 120 \text{ Hz}$	25°C	33	39		34	39		dB	
Output regulation	I _O = 1 mA to 100 mA	25°C			150			150	mV	
Output regulation	I _O = 1 mA to 40 mA	25°C			75			75	IIIV	
Output noise voltage	f = 10 Hz to 100 kHz	25°C		90			90		μV	
Dropout voltage	I _O = 40 mA	25°C		1.7			1.7		V	
Bias current		25°C			6.5			6.5	mA	
bias current		125°C			6			6	IIIA	
Rice current change	$V_{I} = -20 \text{ V to } -30 \text{ V}$	0°C to 125°C	1.5		1.5					
Bias current change	I _O = 1 mA to 40 mA	0 0 10 120 0	2	>	0.2			0.1	mA	

[†] All characteristics are measured with a 0.33-µF capacitor across the input and a 0.1-µF capacitor across the output. Pulse-testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately. ‡ This specification applies only for dc power dissipation permitted by absolute maximum ratings.





21-Jun-2005



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
MC79L05ACD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L05ACLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
MC79L05ACLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
MC79L05AILP	OBSOLETE	TO-92	LP	3		TBD	Call TI	Call TI
MC79L05CD	OBSOLETE	SOIC	D	8	-Öc	TBD 🔥	Call TI	Call TI
MC79L05CDR	OBSOLETE	SOIC	D	8	久 73	TBD	Call TI	Call TI
MC79L05CLP	OBSOLETE	TO-92	LP	3		TBD	Call TI	Call TI
MC79L12ACD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L12ACDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L12ACDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L12ACDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L12ACLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
MC79L12ACLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
MC79L12CD	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI
MC79L12CLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
MC79L15ACD	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI
MC79L15ACLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
MC79L15ACLPM	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
MC79L15ACLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
MC79L15CD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L15CDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MC79L15CLP	OBSOLETE	TO-92	LP	3		TBD	Call TI	Call TI

(1) The marketing status values are defined as follows: **ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.



PACKAGE OPTION ADDENDUM

21-Jun-2005

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

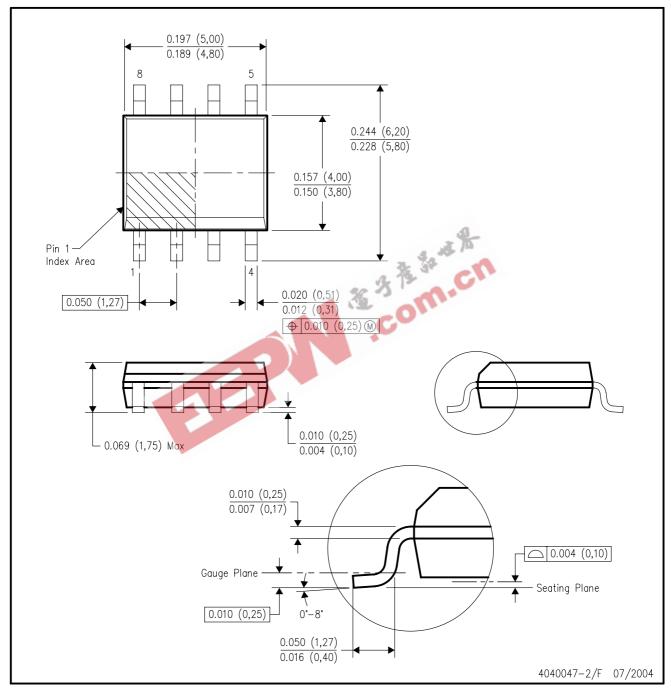
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



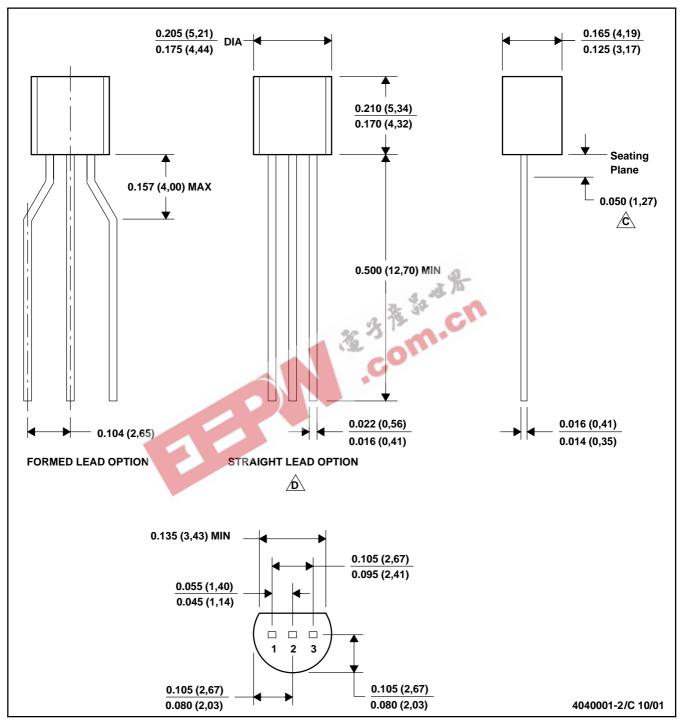
NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MS-012 variation AA.



LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Lead dimensions are not controlled within this area

Alls within JEDEC TO -226 Variation AA (TO-226 replaces TO-92)

E. Shipping Method:

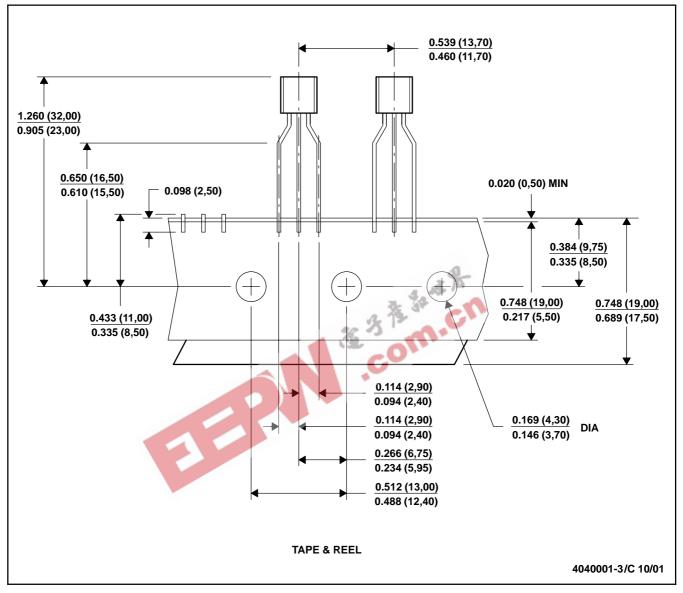
Straight lead option available in bulk pack only.
Formed lead option available in tape & reel or ammo pack.



1

LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Tape and Reel information for the Format Lead Option package.

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