

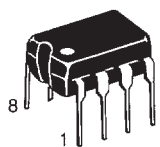
MECL PLL COMPONENTS ÷20/21 DUAL MODULUS
SEMICONDUCTOR TECHNICAL DATA

Legacy Device: Motorola MC12019


The ML12019 is a divide by 20 and 21 dual modulus prescaler. It will divide by 20 when the modulus control input is HIGH and divide by 21 when the modulus control input is LOW.

- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 5.5 V
- Control Input is Compatible with Standard Motorola or Lansdale CMOS Synthesizers
- Emitter Follower Output
- Operating Temperature Range $T_A = -40$ to 85°C

P DIP 8 = PP
PLASTIC PACKAGE
CASE 626-04



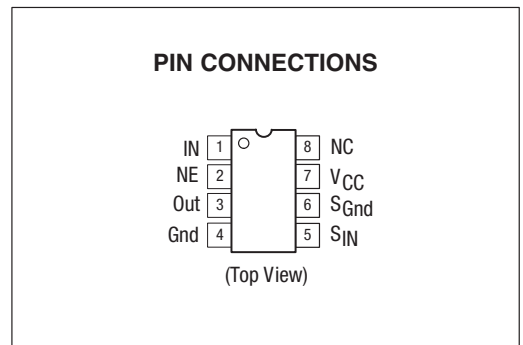
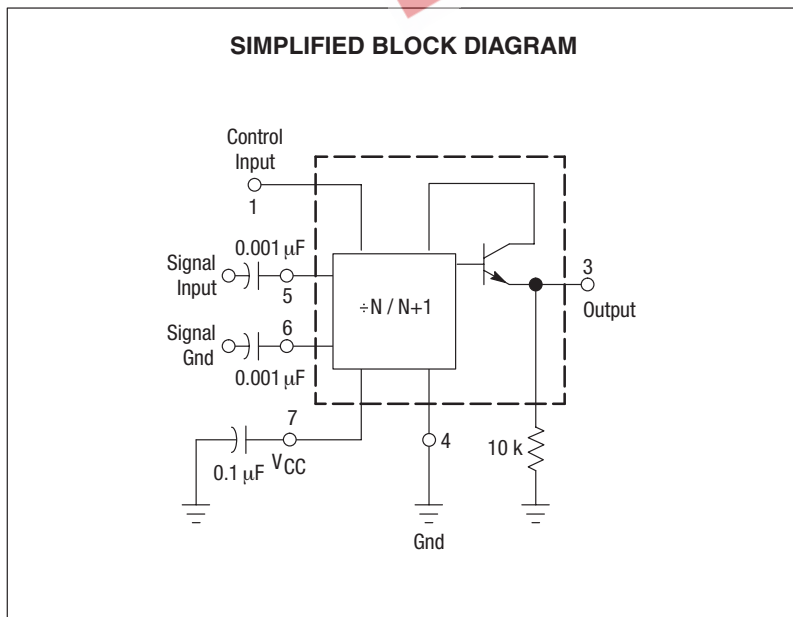
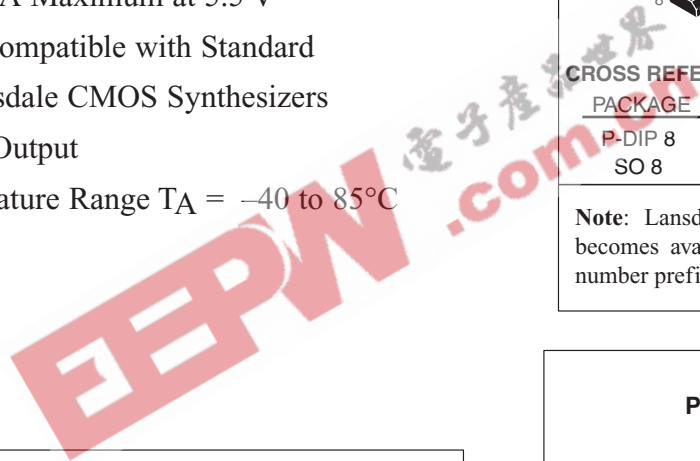
SO 8 = -5P
PLASTIC PACKAGE
CASE 751
(SO-8)



CROSS REFERENCE/ORDERING INFORMATION

PACKAGE	MOTOROLA	LANSDALE
P-DIP 8	MC12019P	ML12019PP
SO 8	MC12019D	ML12019-5P

Note: Lansdale lead free (Pb) product, as it becomes available, will be identified by a part number prefix change from **ML** to **MLE**.



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage, Pin 7	V _{CC}	8.0	Vdc
Operating Temperature Range	T _A	-40 to +85	°C
Storage Temperature Range	T _{stg}	-65 to +175	°C

ELECTRICAL CHARACTERISTICS (V_{CC} = 4.5 to 5.5 V; T_A = -40 to 85°C), unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Toggle Frequency (Sine Wave Input)	f _{max} f _{min}	225 -	- -	- 20	MHz
Supply Current	I _{CC}	-	-	7.5	mA
Control Input HIGH (±20)	V _{IH}	2.0	-	-	V
Control Input LOW (±21)	V _{IL}	-	-	0.8	V
Output Swing Voltage (10 kΩ to ground)	V _{out}	600	-	1200	mVpp
Input Voltage Sensitivity 20 MHz to 225 MHz	V _{in}	200	-	800	mVpp
PLL Response Time (Notes 1 and 2)	t _{PLL}	-	-	t _{out-70}	ns

NOTES: 1. t_{PLL} = the period of time the PLL has from the prescaler rising output transition (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection.
2. t_{out} = period of output waveform.

Figure 1. Generic block diagram showing prescaler connection to PLL device

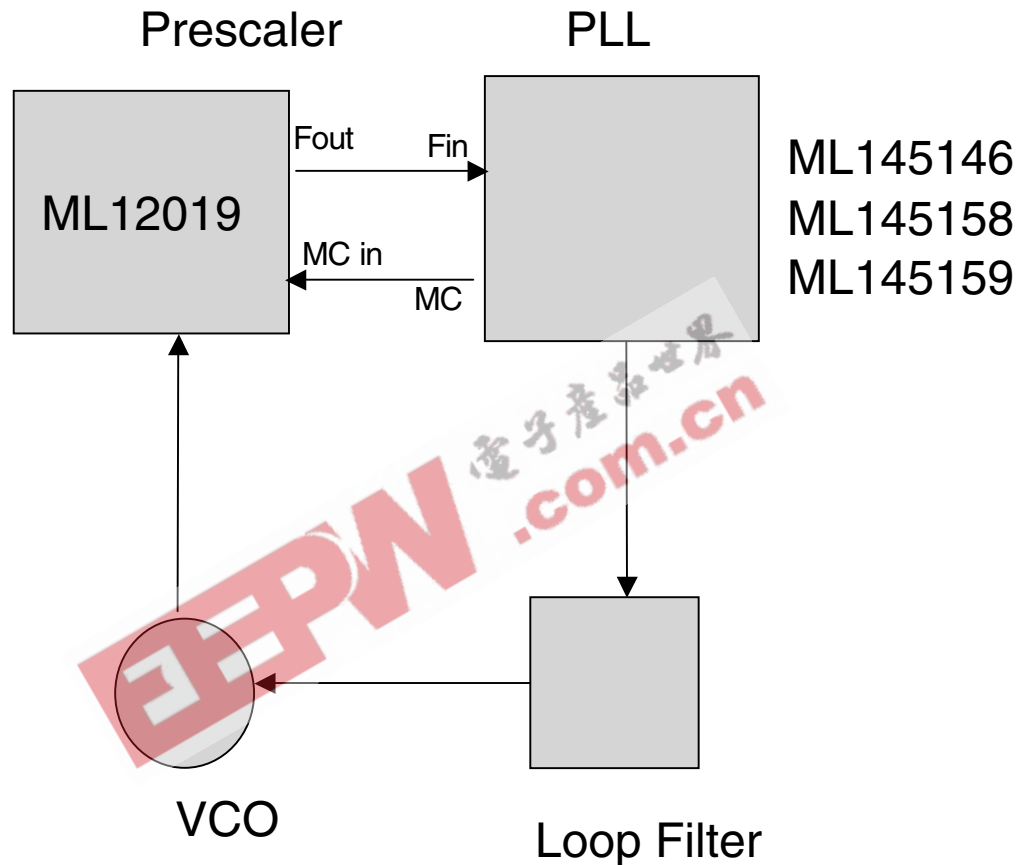
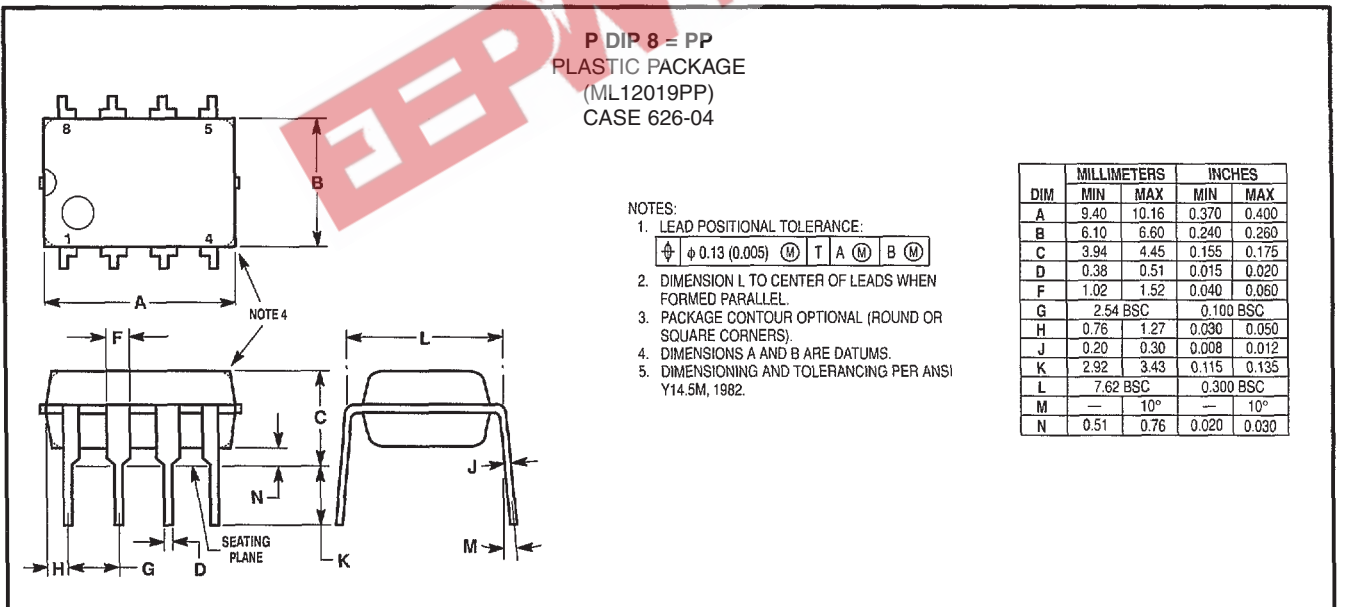
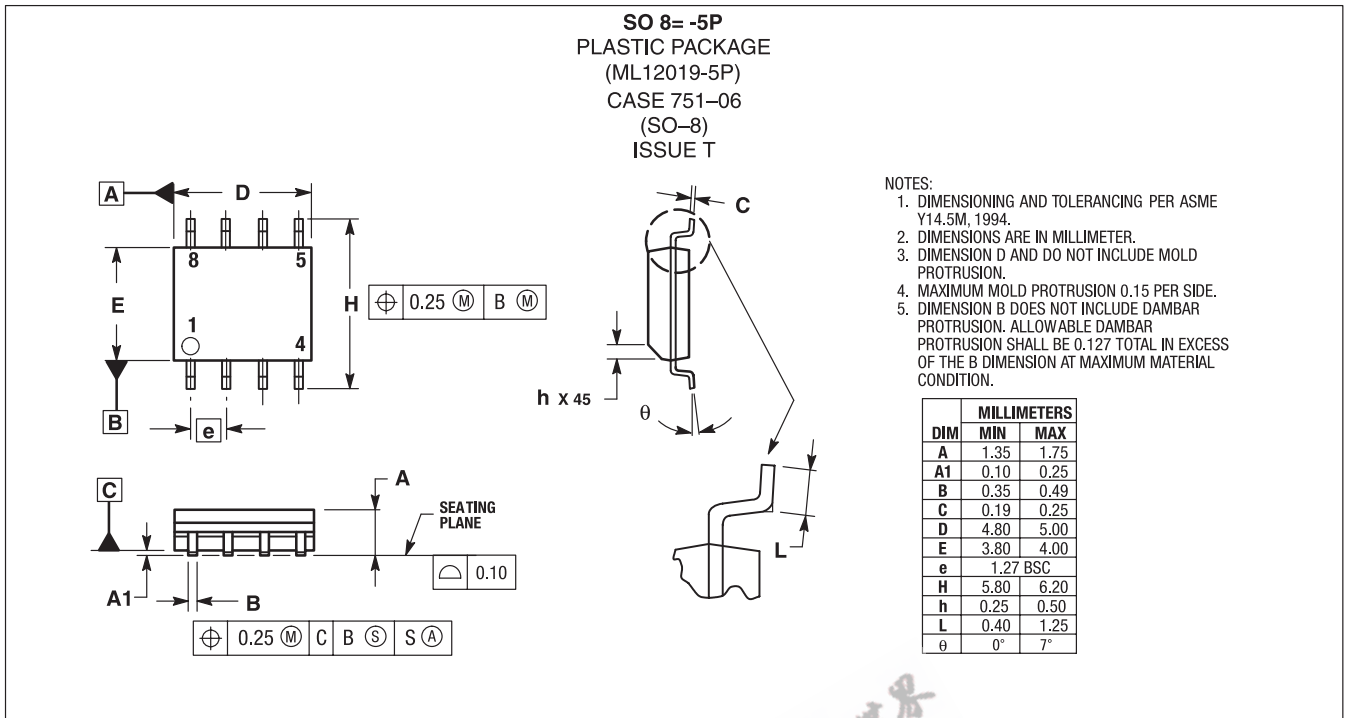


Figure 1 shows a generic block diagram for connecting a prescaler to a PLL device that supports dual modulus control. Application note AN535 describes using a two-modulus prescaler technique. By using a prescaler, higher frequencies can be achieved than by a single CMOS PLL device.

OUTLINE DIMENSIONS



Lansdale Semiconductor reserves the right to make changes without further notice to any products herein to improve reliability, function or design. Lansdale does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights nor the rights of others. "Typical" parameters which may be provided in Lansdale data sheets and/or specifications can vary in different applications, and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by the customer's technical experts. Lansdale Semiconductor is a registered trademark of Lansdale Semiconductor, Inc.