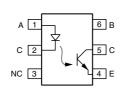


Vishay Semiconductors

Optocoupler, Phototransistor Output, No Base Connection





DESCRIPTION

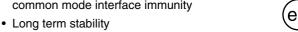
The MOC8101/2/3/4/5 family optocoupler consisting of a gallium arsenide infrared emitting diode optically coupled to a silicon planar phototransistor detector in a plastic plug-in DIP-6 package.

The coupling device is suitable for signal transmission between two electrically separated circuits. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

The base terminal of the MOC8101/2/3/4/5 is not connected, resulting in a substantially improved common mode interference immunity.

FEATURES

- Isolation test voltage, 5300 V_{RMS}
- No base terminal connection for improved common mode interface immunity



- Industry standard dual in line package
- Lead (Pb)-free component
 Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1

ORDER INFORMATION	
PART	REMARKS
MOC8101	CTR 50 to 80 %, DIP-6
MOC8102	CTR 73 to 117 %, DIP-6
MOC8103	CTR 108 to 173 %, DIP-6
MOC8104	CTR 160 to 256 %, DIP-6
MOC8105	CTR 65 to 133 %, DIP-6
MOC8101-X006	CTR 50 to 80 %, DIP-6 400 mil (option 6)
MOC8101-X007	CTR 50 to 80 %, SMD-6 (option 7)
MOC8101-X009	CTR 50 to 80 %, SMD-6 (option 9)
MOC8102-X006	CTR 73 to 117 %, DIP-6 400 mil (option 6)
MOC8102-X007	CTR 73 to 117 %, SMD-6 (option 7)
MOC8102-X009	CTR 73 to 117 %, SMD-6 (option 9)
MOC8104-X006	CTR 160 to 256 %, DIP-6 400 mil (option 6)
MOC8104-X009	CTR 160 to 256 %, SMD-6 (option 9)
MOC8105-X006	CTR 65 to 133 %, DIP-6 400 mil (option 6)
MOC8105-X009	CTR 65 to 133 %, SMD-6 (option 9)

Note

For additional information on the available options refer to option information.

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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V_R	6.0	V	
Forward continuous current		I _F	60	mA	
Surge forward current	t ≤ 10 μs	I _{FSM}	2.5	А	
Power dissipation		P _{diss}	100	mW	
Derate linearly from 25°C			1.33	mW/°C	
OUTPUT					
Collector emitter breakdown voltage		BV _{CEO}	30	V	
Emitter collector breakdown voltage		BV _{ECO}	7.0	V	
Collector current		I _C	50	mA	
Derate linearly from 25°C			2.0	mW/°C	
Power dissipation		P _{diss}	150	mW	
COUPLER					
Isolation test voltage		V _{ISO}	5300	V_{RMS}	
Creepage distance		4 4 1	≥ 7.0	mm	
Clearance distance		32 34	≥ 7.0	mm	
Isolation thickness between emitter and detector		372	≥ 0.4	mm	
Comparative tracking index per DIN IEC 112/VDE 0303, part 1	130	СТІ	175		
Isolation resistance	V _{1O} = 500 V	R _{IO}	10 ¹²	Ω	
Derate linearly from 25 °C			3.33	mW/°C	
Total power dissipation		P _{tot}	250	mW	
Storage temperature		T _{stg}	- 55 to + 150	°C	
Operating temperature		T _{amb}	- 55 to + 100	°C	
Junction temperature		Tj	100	°C	
Soldering temperature (2)	max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C	

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I _F = 10 mA		V_{F}		1.25	1.5	V
Breakdown voltage	I _R = 10 μA		V_{BR}	6.0			V
Reverse current	V _R = 6.0 V		I _R		0.01	10	μΑ
Capacitance	V _R = 0 V, f = 1.0 MHz		Co		25		pF
Thermal resistance			R _{thja}		750		K/W
OUTPUT							
Collector emitter capacitance	V _{CE} = 5.0 V, f = 1.0 MHz		C _{CE}		5.2		pF
Collector emitter dark current	V _{CE} = 10 V, T _{amp} = 25 °C	MOC8101	I _{CEO1}		1.0	50	nA
Collector emiller dark current	V _{CE} = 10 V, T _{amp} = 100 °C	MOC8102	I _{CEO1}		1.0		μΑ
Collector emitter breakdown voltage	I _C = 1.0 mA		BV _{CEO}	30			V
Emitter collector breakdown voltage	I _E = 100 μA		BV _{ECO}	7.0			V
Thermal resistance			R _{thja}		500		K/W

 $^{^{(1)}}$ T_{amb} = 25 $^{\circ}$ C, unless otherwise specified.



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ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER							
Saturation voltage collector emitter	$I_F = 5.0 \text{ mA}$		V _{CEsat}		0.25	0.4	V
Coupling capacitance			C _C		0.6		pF

Note

 T_{amb} = 25 °C, unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio		MOC8101	CTR	50		80	%
		MOC8102	CTR	73		117	%
	$V_{CE} = 10 \text{ V}, I_F = 10 \text{ mA}$	MOC8103	CTR	108		173	%
		MOC8104	CTR	160		256	%
		MOC8105	CTR	6 5		133	%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn-on time	$V_{CC} = 10 \text{ V}, I_{C} = 2.0 \text{ mA}, R_{L} = 100 \Omega$	ton		3.0		μs	
Turn-off time	$V_{CC} = 10 \text{ V}, I_{C} = 2.0 \text{ mA}, R_{L} = 100 \Omega$	t _{off}		2.3		μs	
Rise time	$V_{CC} = 10 \text{ V}, I_C = 2.0 \text{ mA}, R_L = 100 \Omega$	t _r		2.0		μs	
Fall time	$V_{CC} = 10 \text{ V}, I_C = 2.0 \text{ mA}, R_L = 100 \Omega$	t _f		2.0		μs	
Cut off frequency		f _{co}		250		kHz	

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified

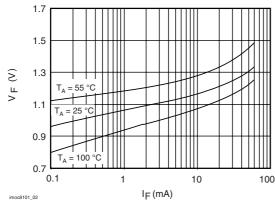


Fig. 1 - Forward Voltage vs. Forward Current

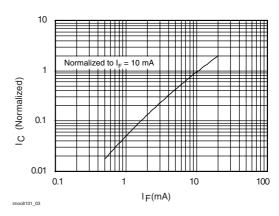
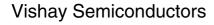
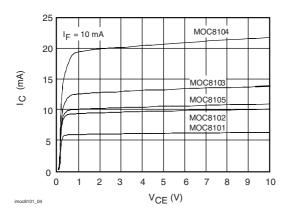


Fig. 2 - Collector Current vs. LED Forward Current



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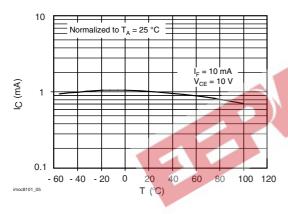




10000 Normalized to V_{CE} = 10 V 1000 (Normalized) 100 10 ICEO V_{CE} = 10 V 0.1 20 60 80 100 40

Fig. 3 - Collector Current vs. Collector Emitter Voltage

Fig. 5 - Collector Emitter Dark Current vs. Ambient Temperature



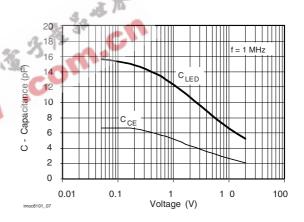


Fig. 4 - Collector Current vs. Ambient Temperature

Fig. 6 - Capacitance vs. Voltage

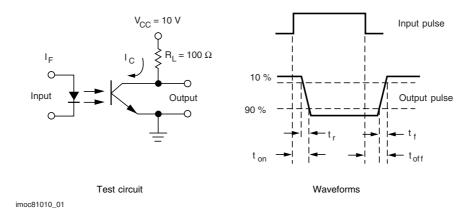
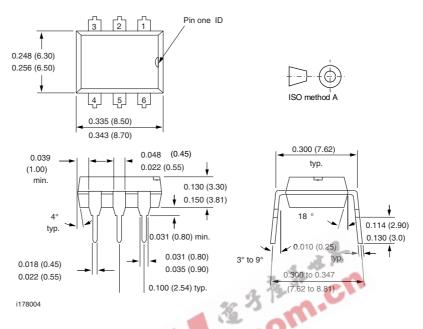


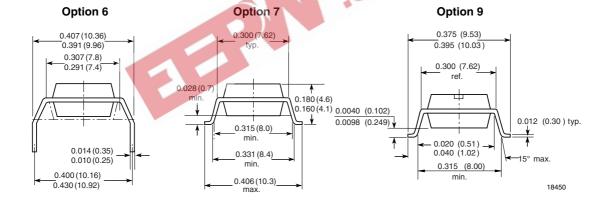
Fig. 7 - Switching Time Test Circuit and Waveforms



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PACKAGE DIMENSIONS in inches (millimeters)





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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

> We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

Document Number: 83660 For technical questions, contact: optocoupler.answers@vishay.com Rev. 1.5, 11-Jan-08





Vishay

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