### INTEGRATED CIRCUITS

# DATA SHEET

74F30240

N.com.cn Octal 300 line driver with enable, inverting (open collector)\*

# 74F30244

Octal  $30\Omega$  line driver with enable, non-inverting (open collector)

\* Discontinued part. Please see the Discontinued Product List.

Product specification

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IC15 Data Handbook





Philips Semiconductors Product specification

### 30 $\Omega$ line drivers

74F30240\*, 74F30244

74F30240 74F30244 Octal 30 $\Omega$  line driver with enable, inverting (open collector) Octal 30 $\Omega$  line driver with enable, non-inverting (open collecor)

### **FEATURES**

- Ideal for driving transmission lines or backplanes. 160mA  $I_{OL}$  ideal for applications with impedance as low as  $30\Omega$
- $\bullet$  Guaranteed threshold voltages on the incident wave while driving line as low as  $30\Omega$
- High impedance NPN base inputs for reduced loading (20μA in High and Low states)
- Ideal for applications which require high output drive and minimal bus loading
- Octal interface
- 74F30240 inverting
- 74F30244 non-inverting
- Open-Collector outputs sink 160mA
- Multiple side pins are used for V<sub>CC</sub> and GND to reduce lead inductance (improves speed and noise immunity)
- Available in 24-pin standard slim DIP (300mil) plastic, SOL or CERDIP packages

### **DESCRIPTION**

The 74F30240 and 74F30244 are high current open collector octal buffers composed of eight inverters. The 74F30240 has inverting data paths and the 74F30244 has non-inverting paths. Each device has eight inverters with two Output Enables ( $\overline{OE}0$ ,  $\overline{OE}1$ ), each controlling four outputs. Both drivers are designed to deal with the low-impedance transmission line effects found on printed circuit boards when fast edge rates are used. The 160mA I<sub>OL</sub> provides ample power to achieve TTL switching voltages on the incident wave.

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F30240	9.5ns	62.5mA
74F30244	10.5ns	69mA

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### ORDERING INFORMATION

DESCRIPTION	COMMERCIAL RANGE $V_{CC} = 5V \pm 10\%$ , $T_{amb} = 0^{\circ}C \text{ to } +70^{\circ}C$	PACKAGE DRAWING NUMBER
24-pin Plastic Slim DIP (300mil)	N74F30244N	SOT222-1
24–pin Plastic SOL <sup>1</sup>	N74F30244D	SOT137-1

### NOTE:

1. Because of the high current sinking capability of these parts, the SOL package should only be used under the following conditions:

a. 50% duty cycle,

AND

b. 3/5 of remaining 50% driving  $\leq$  100mA (leaving the remaining 2/5 of the drive  $\leq$  160mA)

OR

c. use ≤ 450 linear feet per minute forced air or other thermal mounting techniques.

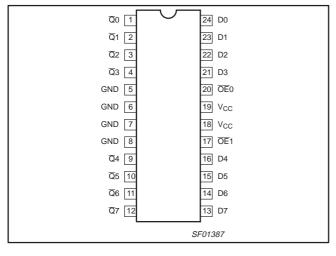
### INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

PINS	DESCRIPTION	74F(U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
D0-D7	Data inputs	1.0/0.033	20μΑ/20μΑ
OE0-OE1	Output Enable inputs (Active Low)	1.0/0.033	20μΑ/20μΑ
\overline{Q}0-\overline{Q}7	Data outputs (OC) for 74F30240	OC/266.7	OC/160mA
Q0-Q7	Data outputs (OC) for 74F30244	OC/266.7	OC/160mA

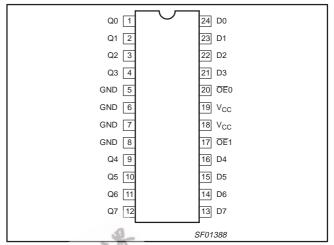
NOTE: One (1.0) FAST unit load is defined as: 20µA in the High state and 0.6mA in the Low state. OC = Open Collector.

### 74F30240\*, 74F30244

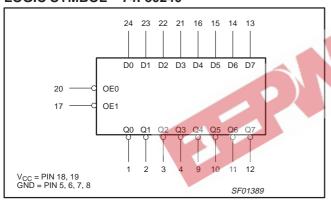
### PIN CONFIGURATION - 74F30240



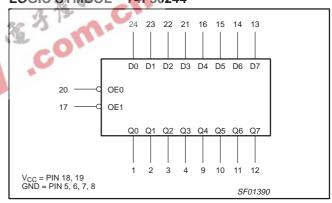
### PIN CONFIGURATION - 74F30244



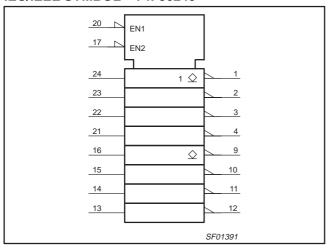
### LOGIC SYMBOL - 74F30240



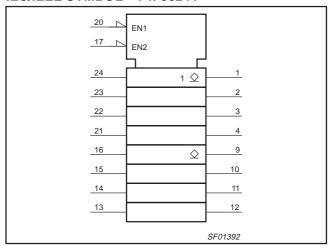
### LOGIC SYMBOL - 74F30244



### IEC/IEEE SYMBOL - 74F30240



### IEC/IEEE SYMBOL - 74F30244



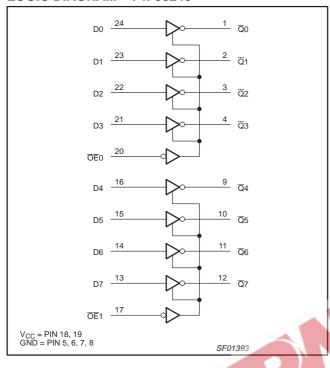
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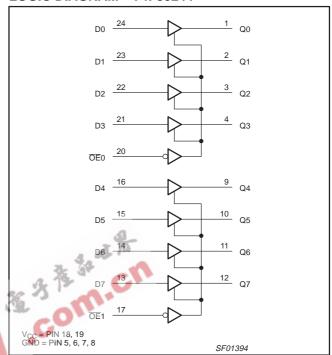
<sup>\*</sup> Discontinued part. Please see the Discontinued Products List.

### 74F30240\*, 74F30244

### LOGIC DIAGRAM - 74F30240



### LOGIC DIAGRAM - 74F30244



### **FUNCTION TABLE**

	NPUTS	OUTPUTS						
	WPU13	74F30240	74F30244					
<del>OE</del> n	Dn	Qn	Qn					
L	L	Н	L					
L	н	L	Н					
Н	X	OFF	OFF					

H = High voltage level = Low voltage level

X = Don't care
OFF = Pulled up t

OFF = Pulled up through resistor (Open Collector)

<sup>\*</sup> Discontinued part. Please see the Discontinued Products List.

74F30240\*, 74F30244

### **ABSOLUTE MAXIMUM RATINGS**

(Operation beyond the limits set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V <sub>CC</sub>	Supply voltage	-0.5 to +7.0	V
V <sub>IN</sub>	Input voltage	-0.5 to +7.0	V
I <sub>IN</sub>	Input current	-30 to +5	mA
V <sub>OUT</sub>	Voltage applied to output in High output state	–0.5 to $V_{CC}$	V
I <sub>OUT</sub>	Current applied to output in Low output state	320	mA
T <sub>amb</sub>	Operating free-air temperature range	0 to +70	°C
T <sub>stg</sub>	Storage temperature range	-65 to +150	°C

### RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	3 %	LIMITS						
STWIBUL	PARAMETER	MIN	NOM	MAX	UNIT				
V <sub>CC</sub>	Supply voltage	4.5	5.0	5.5	V				
V <sub>IH</sub>	High-level input voltage	<b>2</b> .0			V				
V <sub>IL</sub>	Low-level input voltage			0.8	V				
I <sub>IK</sub>	Input clamp current			-18	mA				
V <sub>OH</sub>	High-level output voltage			4.5	V				
I <sub>OL</sub>	Low-level output current			160	mA				
T <sub>amb</sub>	Operating free-air temperature range	0		+70	°C				

### DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

CYMPOL	DADA	METER		T.F.	CT CONDITION	D1		LIMITS		
SYMBOL	PARA	METER		I E	ST CONDITIONS	5.	MIN	TYP <sup>2</sup>	MAX	UNIT
Іон	High-level output	current		V <sub>CC</sub> V <sub>IH</sub>	= MIN, V <sub>IL</sub> = MA = MIN, V <sub>OH</sub> = MA	AX, AX			250	μА
				\/ _ MINI	I <sub>OL</sub> = 100mA	±10% V <sub>CC</sub>		0.42	0.55	V
V <sub>OL</sub>	Low-level output	current		$V_{CC} = MIN,$ $V_{IL} = MAX,$ $V_{IH} = MIN$	I <sub>OL</sub> = 160mA NO TAG	±5% V <sub>CC</sub>			0.80	V
V <sub>IK</sub>	Input clamp volta	ge		$V_{CC} = MIN, I_I = I_{IK}$				-0.73	-1.2	V
I <sub>I</sub>	Input current at minput voltage	naximum		$V_{CC} = 0.0V, V_{I} = 7.0V$					100	μА
I <sub>IH</sub>	High-level input c	urrent		V <sub>CC</sub>	$S = MAX, V_1 = 2.7$	7V			20	μΑ
I <sub>IL</sub>	Low-level input co	urrent		V <sub>CC</sub>	$S = MAX, V_1 = 0.5$	5V			-20	μΑ
		74520240	I <sub>CCH</sub>		\/ MA\			13	23	mA
	Supply current (total) 74F30240 ICCL			$V_{CC} = MAX$				70	95	mA
Icc								19	27	mA
		74F30244	I <sub>CCL</sub>		$V_{CC} = MAX$			70	100	mA

### NOTES

- 1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- 2. All typical values are at  $V_{CC}$  = 5V,  $T_{amb}$  = 25°C.
- 3.  $I_{OL1}$  is the current necessary to guarantee the High-to-Low transition in a  $30\Omega$  transmission line on the incident wave.

<sup>\*</sup> Discontinued part. Please see the Discontinued Products List.

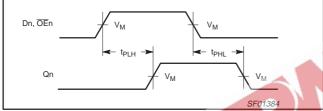
74F30240\*, 74F30244

### **AC ELECTRICAL CHARACTERISTICS**

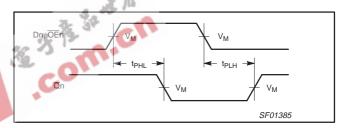
						LIMIT	S		
SYMBOL	PARAMETER		TEST CONDITION	T <sub>2</sub> V C <sub>L</sub> = 5	<sub>nmb</sub> = +25° <sub>CC</sub> = +5.0° 50pF, R <sub>L</sub> =	C V 500Ω	T <sub>amb</sub> = 0°C V <sub>CC</sub> = +5. C <sub>L</sub> = 50pF,	UNIT	
				MIN	TYP	MAX	MIN	MAX	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay Dn to Qn	74f30240	Waveform 2	4.0 1.0	10.0 2.0	14.5 5.0	4.0 1.0	15.0 5.5	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay OEn to Qn	74130240	Waveform 1, 2	4.0 3.5	10.0 6.0	14.0 9.0	4.0 3.5	14.5 10.5	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay Dn to Qn	74f30244	Waveform 1	4.0 3.0	10.5 5.5	14.5 9.0	4.0 3.0	15.0 9.5	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay OEn to Qn	74130244	Waveform 1, 2	4.0 3.5	9.5 6.0	14.0 9.0	4.0 3.5	14.5 10.5	ns

### **AC WAVEFORMS**

For all waveforms,  $V_M = 1.5V$ .

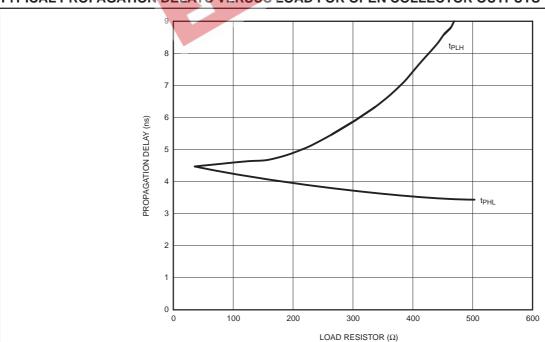


**Propagation Delay for Data to Output** Waveform 1.



Waveform 2. **Propagation Delay for Data to Output** 

### TYPICAL PROPAGATION DELAYS VERSUS LOAD FOR OPEN COLLECTOR OUTPUTS



When using Open-Collector parts, the value of the pull-up resistor greatly affects the value of the  $t_{PLH}$ . For example, changing the specified pull-up resistor value from  $500\Omega$  to  $100\Omega$  will improve the  $t_{PLH}$  up to 50% with only a slight increase in the  $t_{PHL}$ . However, if the value of the pull-up resistor is changed, the user must make certain that the total IOL current through the resistor and the total  $I_{IL}s$  of the receivers does not exceed the  $I_{OL}$  maximum specification.

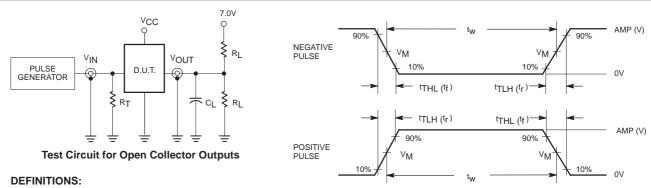
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<sup>\*</sup> Discontinued part. Please see the Discontinued Products List.

### 74F30240\*, 74F30244

### **TEST CIRCUIT AND WAVEFORMS**



R<sub>L</sub> = Load resistor; see AC electrical characteristics for value.

Load capacitance includes jig and probe capacitance; see AC electrical characteristics for value.

Termination resistance should be equal to  $Z_{OUT}$  of pulse generators.

### Input Pulse Definition

family	INPL	JT PU	LSE REQUI	SE REQUIREMENTS						
family	amplitude	V <sub>M</sub>	rep. rate	t <sub>w</sub>	t <sub>TLH</sub>	t <sub>THL</sub>				
74F	3.0V	1.5V	1MHz	500ns	2.5ns	2.5ns				

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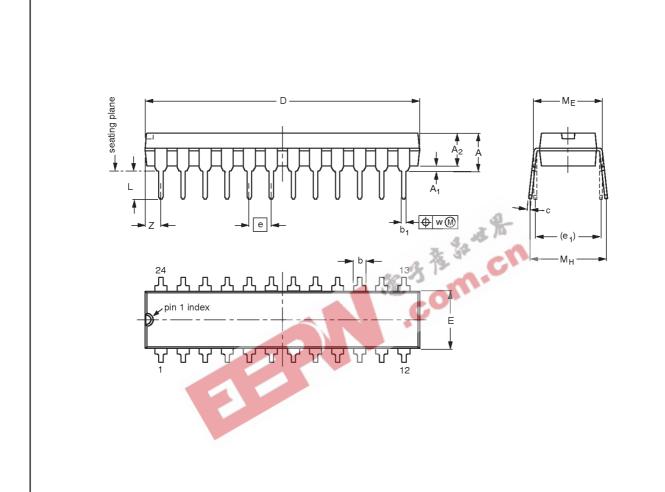
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<sup>\*</sup> Discontinued part. Please see the Discontinued Products List.

## 74F30240\*, 74F30244

### DIP24: plastic dual in-line package; 24 leads (300 mil)

SOT222-1





### DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A max.	A <sub>1</sub> min.	A <sub>2</sub> max.	b	b <sub>1</sub>	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	e <sub>1</sub>	L	ME	Мн	w	Z <sup>(1)</sup> max.
mm	4.70	0.38	3.94	1.63 1.14	0.56 0.43	0.36 0.25	31.9 31.5	6.73 6.48	2.54	7.62	3.51 3.05	8.13 7.62	10.03 7.62	0.25	2.05
inches	0.185	0.015	0.155	0.064 0.045	0.022 0.017	0.014 0.010	1.256 1.240	0.265 0.255	0.100	0.300	0.138 0.120	0.32 0.30	0.395 0.300	0.01	0.081

### Note

1. Plastic or metal protrusions of 0.01 inches maximum per side are not included.

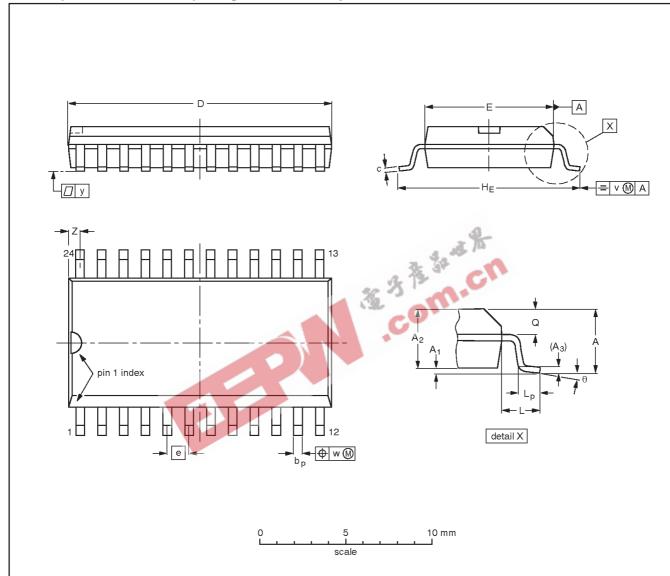
OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE		
SOT222-1		MS-001AF			95-03-11		

<sup>\*</sup> Discontinued part. Please see the Discontinued Product List.

## 74F30240\*, 74F30244

### SO24: plastic small outline package; 24 leads; body width 7.5 mm

SOT137-1



### DIMENSIONS (inch dimensions are derived from the original mm dimensions)

D.IIII E 110																		
UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	HE	L	Lp	Q	v	w	у	z <sup>(1)</sup>	θ
mm	2.65	0.30 0.10	2.45 2.25	0.25	0.49 0.36	0.32 0.23	15.6 15.2	7.6 7.4	1.27	10.65 10.00	1.4	1.1 0.4	1.1 1.0	0.25	0.25	0.1	0.9 0.4	8°
inches	0.10	0.012 0.004	0.096 0.089	0.01	0.019 0.014	0.013 0.009	0.61 0.60	0.30 0.29	0.050	0.419 0.394	0.055	0.043 0.016	0.043 0.039	0.01	0.01	0.004	0.035 0.016	0°

### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT137-1	075E05	MS-013AD				<del>-95-01-24</del> 97-05-22

<sup>\*</sup> Discontinued part. Please see the Discontinued Product List.

Philips Semiconductors Product specification

### $30\Omega$ line drivers

74F30240\*, 74F30244

### Data sheet status

Data sheet status	Product status	Definition [1]	
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.	
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make chages at any time without notice in order to improve design and supply the best possible product.	
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.	

<sup>[1]</sup> Please consult the most recently issued datasheet before initiating or completing a design.

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**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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