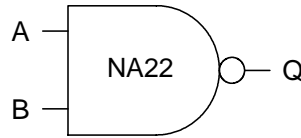


NA22 is a 2-input NAND gate with 2x drive strength.

**Truth Table**

A	B	Q
L	L	H
L	H	H
H	L	H
H	H	L



**Capacitance**

	Ci (pF)
A	0.069
B	0.061

**Area**

0.41 mils<sup>2</sup>

**Power**

3.00 μW/MHz

Delay [ns] = tpd.. = f(SL, L)      with SL = Input Slope [ns] ; L = Output Load [pF]  
 Output Slope [ns] = op\_sl.. = f(L)      with L = Output Load [pF]

AC Characteristics : Tj = 25°C    VDD = 3.3V    Typical Process

**AC Characteristics**

Characteristics	Symbol	SL = 0.1			SL = 2.0		
		L = 0.2	L = 1.4	L = 2.0	L = 0.2	L = 1.4	L = 2.0
Delay A to Q	tpdar	0.30	1.75	2.43	0.64	1.84	2.47
	tpdaf	0.23	1.30	1.78	0.39	1.40	1.90
Delay B to Q	tpdbr	0.33	1.72	2.49	0.68	1.97	2.53
	tpdbf	0.25	1.32	1.80	0.29	1.30	1.80
Output Slope A to Q	op_slar	0.90	5.56	7.88	1.16	5.41	7.73
	op_slaf	0.66	3.83	5.28	1.06	3.90	5.42
Output Slope B to Q	op_slbr	0.95	5.17	7.42	1.23	5.30	7.38
	op_slbf	0.66	3.83	5.30	0.93	3.78	5.38