

**PART NUMBERING GUIDE**

**Environmental/Mechanical Specifications on page F5**

<b>OAH 100 48 A T - 30.000MHz</b>	
<p><b>Package</b></p> <p>OAH = 14 Pin Dip / 5.0Vdc / HCMOS-TTL          OAH3 = 14 Pin Dip / 3.3Vdc / HCMOS-TTL          OBH = 8 Pin Dip / 5.0Vdc / HCMOS-TTL          OBH3 = 8 Pin Dip / 3.3Vdc / HCMOS-TTL</p> <p><b>Inclusive Stability</b></p> <p>100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm,          20= +/-20ppm, 15= +/-15ppm, 10= ±10ppm</p>	<p><b>Pin One Connection</b></p> <p>Blank = No Connect, T = Tri State Enable High</p> <p><b>Output Symmetry</b></p> <p>Blank = 40/60%, A = 45/55%</p> <p><b>Operating Temperature Range</b></p> <p>Blank = 0°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C</p>

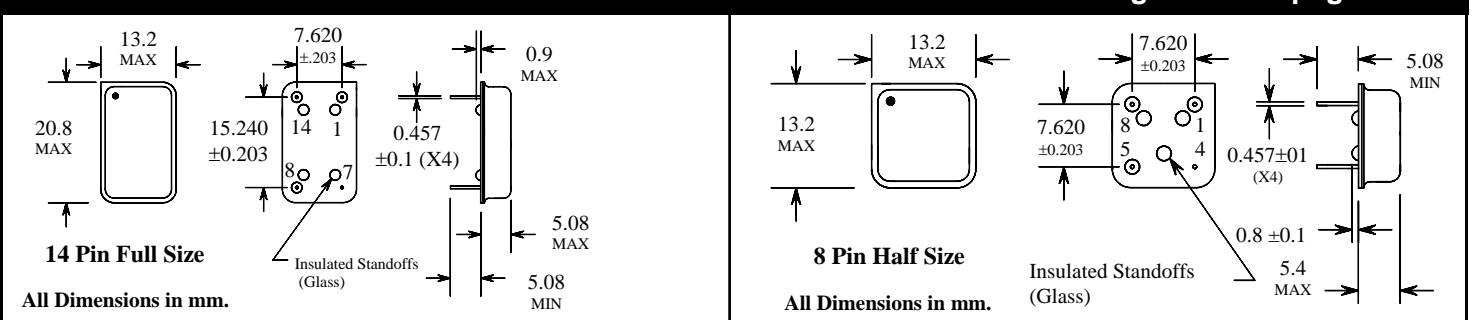
**ELECTRICAL SPECIFICATIONS**

**Revision: 1994-C**

Frequency Range	250kHz to 200.000MHz
Operating Temperature Range	0°C to 70°C / -20°C to 70°C / -40°C to 85°C
Storage Temperature Range	-55°C to 125°C
Supply Voltage	5.0Vdc ±10%, 3.3Vdc ±10%
Input Current	250.000kHz to 24.000MHz: 25mA Maximum 24.001MHz to 50.000MHz: 30mA Maximum 50.001MHz to 66.667MHz: 60mA Maximum 66.668MHz to 200.000MHz: 80mA Maximum
Frequency Tolerance / Stability	Inclusive of Operating Temperature Range, Supply Voltage and Load: ±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (20, 15, 10 = 0°C to 70°C Only)
Output Voltage Logic High (Voh)	w/TTL Load: 2.4Vdc Minimum w/HCMOS Load: Vdd -0.5Vdc Minimum
Output Voltage Logic Low (Vol)	w/TTL Load: 0.4Vdc Maximum w/HCMOS Load: 0.5Vdc Maximum
Rise Time	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load <=66.667MHz. 6nSeconds Maximum
Fall Time	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load >66.667MHz. 3nSeconds Maximum
Duty Cycle	@1.4Vdc w/TTL Load; @50% w/HCMOS Load @1.4Vdc w/TTL Load or w/HCMOS Load @50% of Waveform w/LSTTL or HCMOS Load >66.667MHz 50 ±10% (Standard) 50±5% (Optional) 50±5% (Optional)
Load Drive Capability	250.000kHz to 24.000MHz: 10TTL or 50pF HCMOS Load 24.001MHz to 66.667MHz: 10TTL or 15pF HCMOS Load 66.668MHz to 150.000MHz: 10LSTTL or 15pF HCMOS Load
Pin 1 Tristate Input Voltage	No Connection VIH: Enables Output VIL: +2.2Vdc Minimum to Enable Output +0.8Vdc Maximum to Disable Output
Aging (@ 25°C)	±5ppm / year Maximum
Start Up Time	10mSeconds Maximum
Absolute Clock Jitter	±100pSeconds Maximum
One Sigma Clock Jitter	±25pSeconds Maximum

**MECHANICAL DIMENSIONS**

**Marking Guide on page F3-F4**



Pin 1: No Connect or Tri-State Pin 7: Case Ground	Pin 8: Output Pin 14: Supply Voltage	Pin 1: No Connect or Tri-State Pin 4: Case Ground	Pin 5: Output Pin 8: Supply Voltage
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