

Data Sheet January 2000 File Number 3400.3

### 30A, 1200V Ultrafast Dual Diode

The RURG30120CC is an ultrafast dual diode with soft recovery characteristic ( $t_{rr}$  < 110ns). It has low forward voltage drop and is silicon nitride passivated ion-implanted epitaxial planar construction.

This device is intended for use as a freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and ultrafast recovery with soft recovery characteristic minimize ringing and electrical noise in many power switching circuits, reducing power loss in the switching transistors.

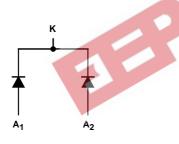
Formally developmental type TA49031.

## **Ordering Information**

PART NUMBER	PACKAGE	BRAND
RURG30120CC	TO-247	URG30120C

NOTE: When ordering, use the entire part number.

# Symbol



#### **Features**

Ultrafast with Soft Recovery	<110ns
Operating Temperature	175 <sup>0</sup> C
Reverse Voltage	1200V

- · Avalanche Energy Rated
- · Planar Construction

### **Applications**

- · Switching Power Supplies
- · Power Switching Circuits
- · General Purpose

#### Package



<b>Absolute Maximum Ratings</b> (Per Leg) T <sub>C</sub> = 25°C, Unless Otherwise Specified							
	RURG30120CC	UNITS					
Peak Repetitive Reverse VoltageV <sub>RRM</sub>	1200	V					
Working Peak Reverse Voltage	1200	V					
DC Blocking VoltageV <sub>R</sub>	1200	V					
Average Rectified Forward Current $I_{F(AV)}$ ( $T_C = 110^{\circ}C$ )	30	Α					
Repetitive Peak Surge Current	60	Α					
Nonrepetitive Peak Surge Current	300	Α					
Maximum Power Dissipation	125	W					
Avalanche Energy (See Figures 7 and 8)	30	mJ					
Operating and Storage Temperature	-65 to 175	οС					

#### RURG30120CC

### **Electrical Specifications** (Per Leg) $T_C = 25^{\circ}C$ , Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V <sub>F</sub>	I <sub>F</sub> = 30A	-	-	2.1	V
	$I_F = 30A, T_C = 150^{\circ}C$	-	-	1.9	V
I <sub>R</sub>	V <sub>R</sub> = 1200V	-	-	250	μΑ
	V <sub>R</sub> = 1200V, T <sub>C</sub> = 150°C	-	-	1	mA
t <sub>rr</sub>	$I_F = 1A$ , $dI_F/dt = 100A/\mu s$	-	-	110	ns
	$I_F = 30A$ , $dI_F/dt = 100A/\mu s$	-	-	150	ns
ta	$I_F = 30A$ , $dI_F/dt = 100A/\mu s$	-	90	-	ns
t <sub>b</sub>	$I_F = 30A$ , $dI_F/dt = 100A/\mu s$	-	45	-	ns
$R_{ heta JC}$		-	-	1.2	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300 $\mu$ s, D = 2%).

 $I_R$  = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time (See Figure 6), summation of  $t_a$  +  $t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 6).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 6).  $I_{RM}$  = Thermal resistance junction to case.

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

## **Typical Performance Curves**

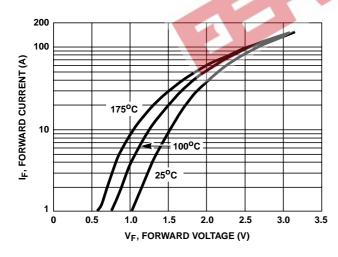


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

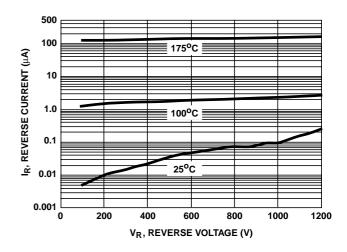


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

### Typical Performance Curves (Continued)

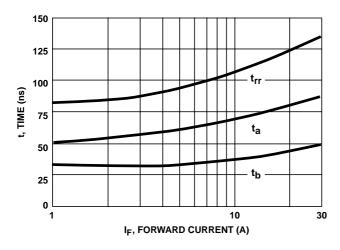


FIGURE 3. t<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

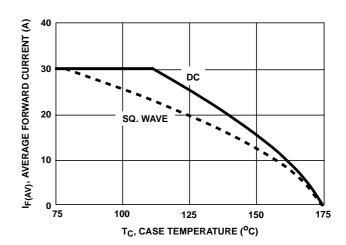


FIGURE 4. CURRENT DERATING CURVE

### Test Circuits and Waveforms

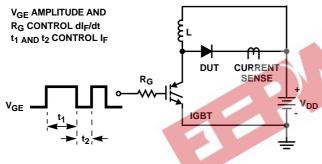


FIGURE 5. t<sub>rr</sub> TEST CIRCUIT

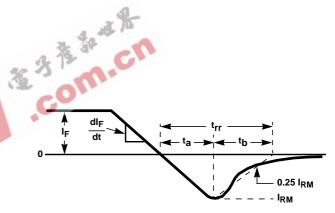


FIGURE 6. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

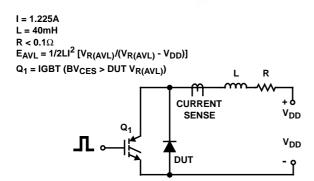


FIGURE 7. AVALANCHE ENERGY TEST CIRCUIT

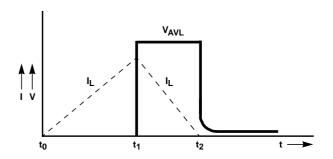


FIGURE 8. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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