

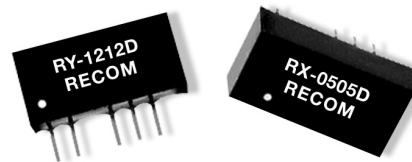
ECONOLINE - DC/DC-Converter

RX and RY Series, 1 Watt, DIP14/SIP7, Regulated (Dual Output)

RECOM

Features

- Controllable Output
- 1kVDC Isolation
- UL94V-0 Package Material
- No Heatsink Required
- Fully Encapsulated
- No External Components
- Toroidal Magnetics

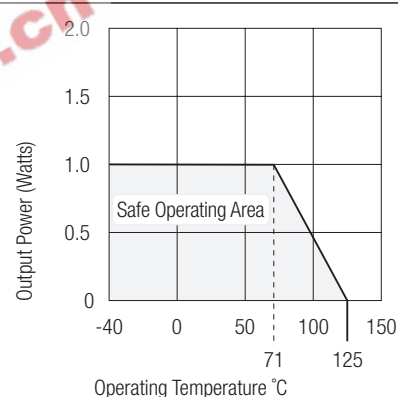


Selection Guide

Part Number DIP14	Part Number SIP7	Input Voltage (VDC)	Output Voltage (VDC)	Output Current (mA)	Efficiency (%)
RX-XX05D	RY-XX05D	5, 9, 12, 15, 24	±5 V	±100	54
RX-XX09D	RY-XX09D	5, 9, 12, 15, 24	±9 V	±55	58
RX-XX12D	RY-XX12D	5, 9, 12, 15, 24	±12 V	±42	62
RX-XX15D	RY-XX15D	5, 9, 12, 15, 24	±15 V	±33	65
RX-XX24D	RY-XX24D	5, 9, 12, 15, 24	±24 V	±21	65

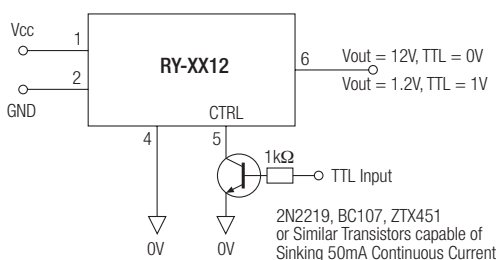
Specifications (Core Operating Area) and Operating Temperature / Derating-Graph

Input Voltage	±5%
Input Filter	Capacitor Type
Output Voltage Accuracy	±0.5%
Line Voltage Regulation	±0.05% / ±1.0%
Load Voltage Regulation (20% to 100% full load)	±0.5% / ±1.0%
Ripple and Noise (20MHz limited)	100mVp-p max.
Efficiency at Full Load	50% min.
Isolation Voltage	1.000VDC min.
Isolation Resistance (Viso = 500VDC)	10 GΩ min.
Isolation Capacitance	40pF min./72pF max.
Short Circuit Protection	1 Second
Switching Frequency at Full Load	30kHz min. / 88kHz max.
Operating Temperature	-40°C to +71°C (see Graph)
Storage Temperature	-55°C to +125°C
Package Weight	RX types 2.7g RY types 2.8g

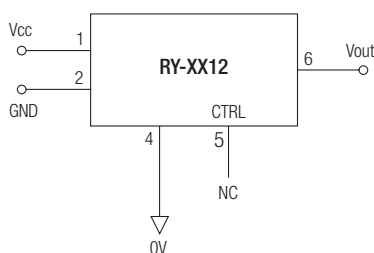


Typical Applications

Flash PROM Programming Voltage Control



Normal Isolated Regulated Output



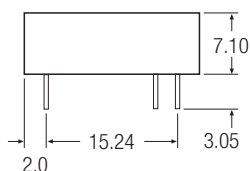
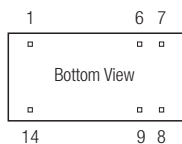
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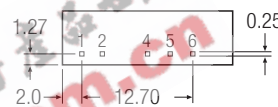
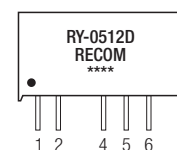
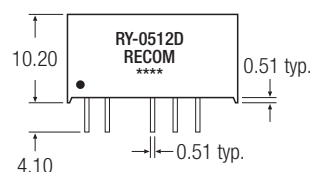
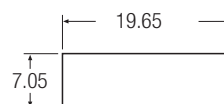
Package Style and Pinning (mm)

14 PIN DIP Package

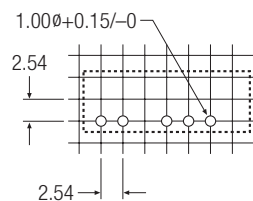
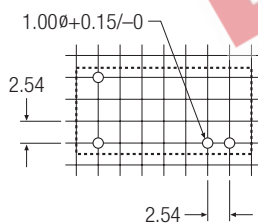


7 PIN SIP Package

3rd angle projection



Recommended Footprint Details



Pin Connections

Pin #	RX	RY
1	-Vin	+Vin
2	No Pin	-Vin
4	No Pin	-Vout
5	No Pin	Com
6	+Vout	+Vout
7	Com	No Pin
8	-Vout	No Pin
9	Com	No Pin
14	+Vin	No Pin

XX.X \pm 0.5 mm
XX.XX \pm 0.25 mm