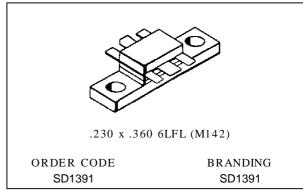


SD1391

RF & MICROWAVE TRANSISTORS UHF BASE STATION APPLICATIONS

PRELIMINARY DATA

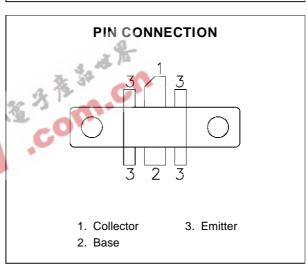
- 470 MHZ
- 24 VOLTS
- EFFICIENCY 50% MIN.
- P_{OUT} = 15 W WITH 11.0 dB MIN. GAIN
- CLASS AB
- COMMON EMITTER





The SD1391 is a gold metallized NPN planar transistor using diffused emitter ballast resistors for reliability and ruggedness.

The SD1391 is specifically designed as a low power, high gain driver and can be operated in Class A, B or C.



ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C)

Symbol	Parameter	Value	Unit	
V_{CBO}	Collector-Base Voltage	V		
Vceo	Collector-Emitter Voltage	25	V	
V _{EBO}	Emitter-Base Voltage	3.5	V	
Ic	Collector Current	2.5	Α	
P _{DISS}	Power Dissipation (+25°C)	29	W	
TJ	Junction Temperature	+200	°C	
T _{STG}	Storage Temperature	- 65 to +150	°C	

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance 6.0 °C/V
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August 23, 1996 1/5

ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

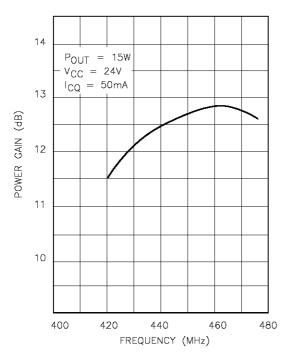
Symbol	Test Conditions	Value			Unit		
3 yiii bor	Symbol Test Conditions		Min.	Typ.	Max.	Onn	
BV _{CBO}	$I_C = 50 \text{ mA}$	$I_E = 0 \text{ mA}$		48	_		V
BV _{CEO}	$I_C = 20 \text{ mA}$	$I_B = 0 \text{ mA}$		25	_		V
BV _{EBO}	$I_E = 5 \text{ mA}$	$I_C = 0 \text{ mA}$		3.5	_	_	V
I _{CBO}	$V_{CB} = 24 \text{ V}$	$I_E = 0 \text{ mA}$		_	_	1.0	mA
hfe	$V_{CE} = 10 \text{ V}$	$I_{\rm C} = 0.1 {\rm A}$		10	_	100	_

DYNAMIC

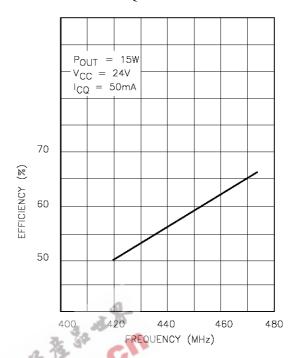
C vom h o 1	Took Conditions		Value			T.T.: '4		
Symbol Test Conditions		Min.	Тур.	Max.	Unit			
Pout	f = 470 MHz	$P_{IN} = 6.3 \text{ W}$	$V_{CC} = 24V$	$I_{CQ} = 50 \text{ mA}$	15	_	_	W
ης	f = 470 MHz	$P_{IN} = 6.3 \text{ W}$	$V_{CC} = 24V$	Icq = 50 mA	50	60		%
R _{TL}	f = 470 MHz	$P_{IN} = 6.3 \text{ W}$	$V_{CC} = 24V$	$I_{CQ} = 50 \text{ mA}$	10	_		dB
Сов	f = 1 MHz	V _{CB} = 24 V		237	$C_{T_{\mu}}$	_	24	pF
				.com				



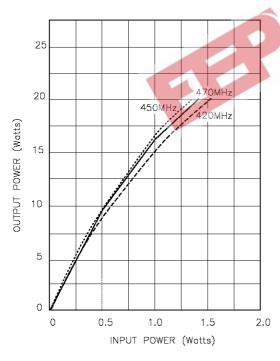
POWER GAIN vs FREQUENCY



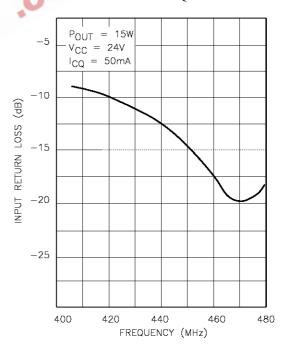
EFFICIENCY vs FREQUENCY



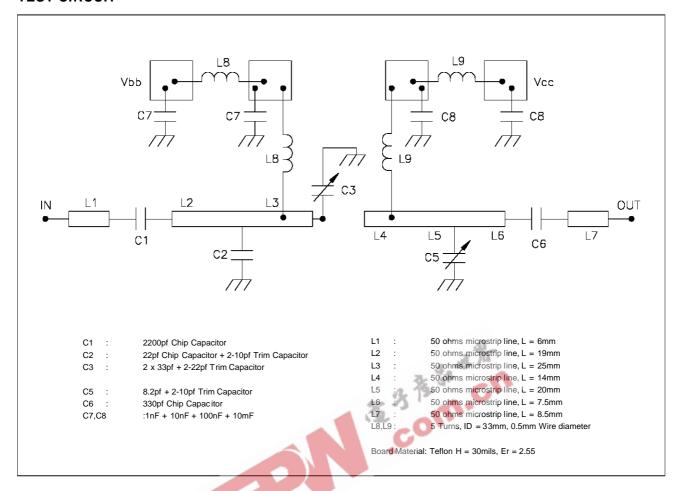
OUTPUT POWER vs INPUT POWER



INPUT RETURN LOSS vs FREQUENCY

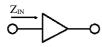


TEST CIRCUIT



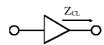
IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE

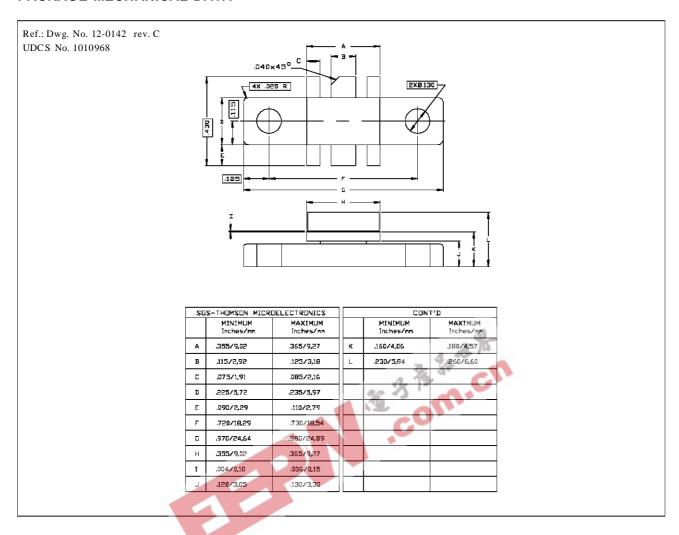


FREQ.	$Z_{IN}(\Omega)$	$Z_CL(\Omega)$		
420 MHz	4.0 + j 2.2	7.2 + j 1.0		
450 MHz	5.4 + j 3.9	6.8 + j 3.0		
470 MHz	4.9 + j 5.7	6.6 + j 4.3		

TYPICAL COLLECTOR LOAD IMPEDANCE



PACKAGE MECHANICAL DATA



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