

OVERVIEW

The SM6503A is the Diode array for ESD protection. 5 circuits consisted of diode element are integrated into ultra-small 6 pin plastic package. It is the most appropriate to protect the external interface circuit of portable equipment which requires miniaturization from ESD.

FEATURES

- ± 12kV ESD immunity level (IEC61000-4-2 contact discharge)
- Clamp voltage: 7.5V typ.Low capacitance: 10pF typ.
- 5 circuits integrated into one package
- Ultra-small package
 - SM6503AD: 6-pin SONSM6503AH: SOT23-6

APPLICATIONS

- Cellular phone
- PDA
- Set Top Box
- Digital Still Camera

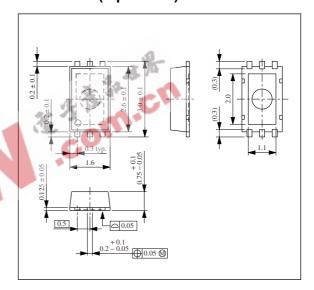
ORDERING INFORMATION

Device	Package		
SM6503AD	6-pin SON		
SM6503AH	SOT23-6		

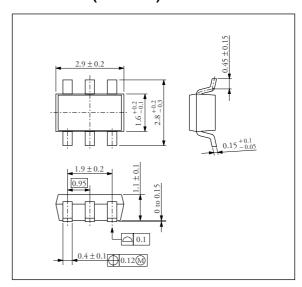
PACKAGE DIMENSIONS

(Unit: mm)

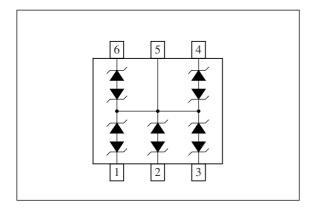
SM6503AD (6-pin SON)



SM6503AH (SOT23-6)



PINOUT (Top view)



PIN DESCRIPTION

Number	Name	Description
1	DIO1	Diode 1
2	DIO2	Diode 2
3	DIO3	Diode 3
4	DIO4	Diode 4
5	VSS	Ground
6	DIO5	Diode 5

SPECIFICATIONS

Absolute Maximum Ratings

 $Ta = 25^{\circ}C$

Parameter	Symbol	Condition	Rating	Unit
Power dissipation	P _D		220	mW
Storage temperature range	T _{stg}	-3	- 55 to 125	°C

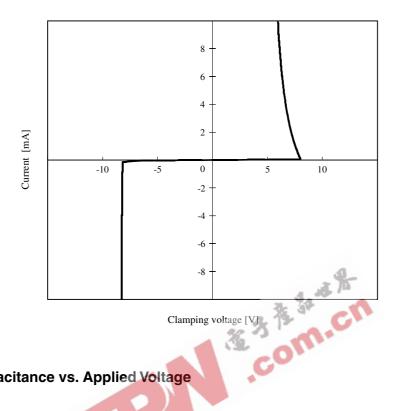
Electrical Characteristics

 $Ta = 25^{\circ}C$

Parameter Sy	Symbol	Condition	Rating			Unit
	Syllibol		min	typ	max	Oilit
Diode maximum leakage current	I _{LEAK}	6V applied	-	_	1	μΑ
Forward clamp voltage V _{ZF}	$I_Z = 1\mu A$	6.5	7.5	8.5	V	
	▼ZF	$I_Z = 5mA$	-	6.5	I	V
Reverse clamp voltage V _{ZR}	V	$I_Z = -1\mu A$	- 8.5	- 7.5	- 6.5	V
	VZR	$I_Z = -5mA$	_	- 8.0	ı	V
Terminal capacitance	C _T	f = 1MHz, between DIO1 to DIO5 and VSS	_	10	-	pF
Maximum operating voltage	V _{MAX}		-	_	6	V

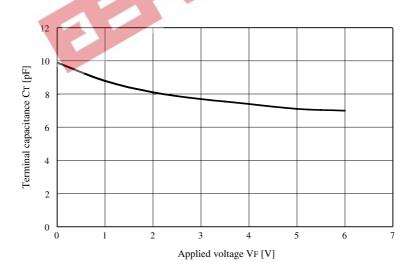
TYPICAL PERFORMANCE

V-I Characteristics (between DIO1 to DIO5 and VSS)



Terminal Capacitance vs. Applied Voltage

f = 1MHz





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