

# **STA3350F**

**PNP Silicon Transistor** 

### **Applications**

- Power amplifier application
- High current switching application

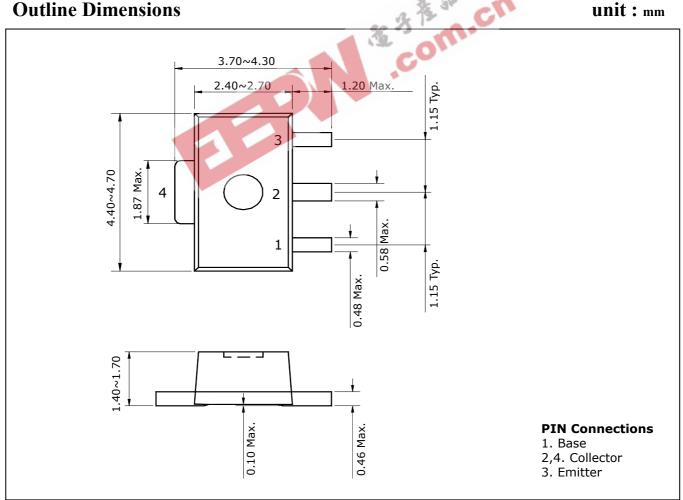
#### **Features**

- Low saturation voltage:  $V_{\text{CE(sat)}}$ =-0.15V Typ. @  $I_{\text{C}}$ =-1A,  $I_{\text{B}}$ =-50mA
- Large collector current capacity: I<sub>C</sub>=-3A
- Small and compact SMD type package
- Complementary pair with STC4350F

## **Ordering Information**

Type NO.	Marking	Package Code		
STA3350F	HW7	SOT-89		

## **Outline Dimensions**



KSD-T5B006-000

**Absolute Maximum Ratings** 

[Ta=25℃]

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	$V_{CEO}$	-50	V
Emitter-base voltage	$V_{EBO}$	-6	V
Collector current	$I_{C}$	-3	Α
Collector Dower dissipation	P <sub>C</sub>	0.5	W
Collector Power dissipation	P <sub>C</sub> *	1	W
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature range	$T_{stg}$	-55~150	°C

<sup>★</sup> Device mounted on ceramic substrate (250mm² x 0.8t)

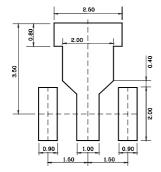
## **Electrical Characteristics**

[Ta=25℃]

Charae	cteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-emitter I	oreakdown voltage	BV <sub>CEO</sub>	$I_C$ =-1mA, $I_B$ =0	-50	-	ı	٧
Collector cut-off c	urrent	$I_{CBO}$	V <sub>CB</sub> =-50V, I <sub>E</sub> =0	~	-	-1	μА
Emitter cut-off cui	rent	$I_{EBO}$	$V_{EB} = -6V$ , $I_{C} = 0$	_	-	-1	μА
DC current gain		h <sub>FE</sub>	V <sub>CE</sub> =-2V, I <sub>C</sub> =-0.5A*	120	-	240	
		h <sub>FE</sub>	V <sub>CE</sub> =-2V, I <sub>C</sub> =-2A*	40	-	-	
Collector-emitter	saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =-1A, I <sub>B</sub> =-0.05A*	-	-	-0.35	V
Base-emitter satu	ration voltage	$V_{BE(sat)}$	I <sub>C</sub> =-2A, I <sub>B</sub> =-0.1A*	-	-0.97	-1.2	V
Transition frequen	су	f <sub>T</sub>	V <sub>CE</sub> =-10V, I <sub>C</sub> =-0.05A	-	250	-	MHz
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz	-	28	-	pF
Switching Time	Turn-on Time	t <sub>on</sub>	Iss   INPUT   Iss   OUTPUT	-	100	-	
	Storage Time	$t_{stg}$		-	300	-	ns
	Fall Time	t <sub>f</sub>		-	50	-	

<sup>\*:</sup> Pulse test :  $t_P \le 300 \mu s$ , Duty cycle  $\le 2\%$ 

#### **\*** Recommend PCB solder land [Unit: mm]



### **Electrical Characteristic Curves**



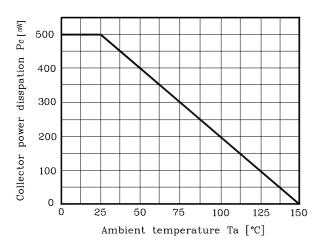


Fig. 2  $I_{C}\;$  -  $V_{BE}$ 

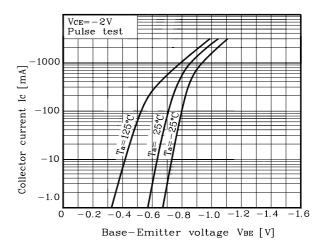


Fig. 3  $I_{C}\;$  -  $V_{CE}$ 

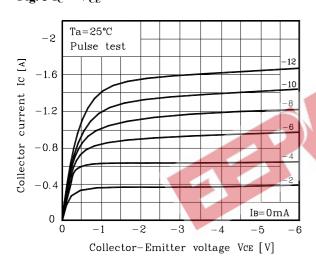


Fig. 4  $h_{FE}$  -  $I_C$ 

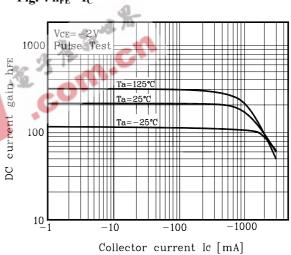


Fig. 5  $V_{\text{CE(sat)}}$  -  $I_{\text{C}}$ 

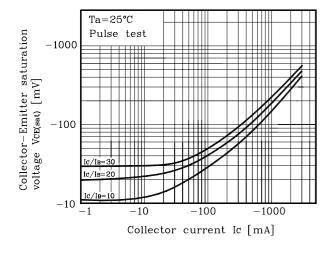
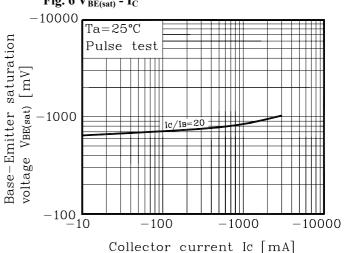
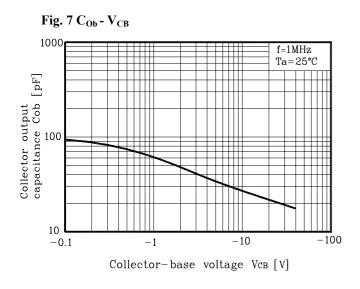


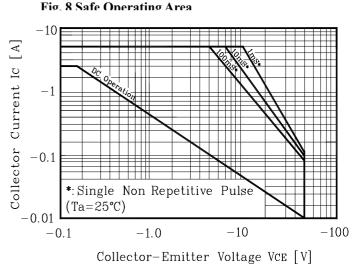
Fig. 6  $V_{BE(sat)}$  -  $I_{C}$ 



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#### **Electrical Characteristic Curves**







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