05/00



T300A SERIES (ECL) STANDARD SPECIFICATIONS

FREQUENCY RANGE 10 MHz to 240 MHz FREQUENCY ACCURACY @ + 25 °C ± 0.0015% (± 15 PF 0.200" Max. FREQUECY STABILITY Vs. TEMPERATURE See Options Below 0.250" 0.200" OPERATING TEMPERATURE RANGE See Options Below INPUT VOLTAGE (See Note Below) - 4.5 VDC ± 5% Ceramic Stand-off (Optional) Pin 1 is identified with a Sq. Corner INPUT CURRENT @ - 4.5 VDC 50 mA Max. 0.439" Max. ⊚ 7 OUTPUT 0.254 100K Compatible 0.507" LOAD 100 Ω to - 2.0 VDC ⊚ ₈ 140 60/40% @ 50% Leve **SYMMETRY** RISE & FALL TIMES (10% to 90% Level) 2 nS Max. START-UP TIME 15 mS Max. 0.877 FREQUENCY STABILITY Vs. VOLTAGE ± 0.0002% (± 2 PPM) Max Pin Connections (for 5% change in Voltage) 14 **GND/CASE** -4.5 VDC AGING @ +25 °C ± 0.0005% (± 5 PPM)/year OUTPUT 8 N/C 1

NOTE: For PECL applications, Xsis 300 Series ECL oscillators can be operated with +5 VDC ± 10% on Pin 14 and power supply return on Pin 7. The output logic levels will still be referenced to +5 VDC and the case will be at +5 VDC, however, 0.8 V peak to peak output signal can be AC or DC coupled as necessary.

of MIL-PRF-55310

Conforms with the Requirements

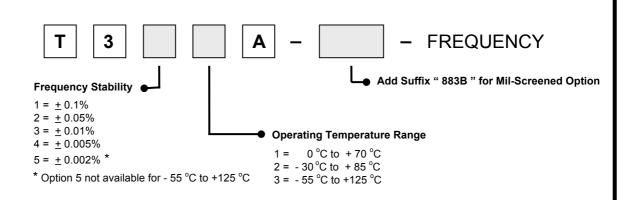
All Others

MISSING

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

ORDERING INFORMATION (Select from options below):

PACKAGE, SEAL & LEAD FINISH



EXAMPLE: T343A - 883B - 24.000 MHz = 14 Pin Package, 100K ECL, ± 0.005% over -55 °C to +125 °C, Mil-Screened, and 24.000 MHz