

# **TIP42 SERIES(TIP42/42A/42B/42C)**

## **Medium Power Linear Switching Applications**

• Complement to TIP41/41A/41B/41C



1.Base 2.Collector 3.Emitter

# **PNP Epitaxial Silicon Transistor**

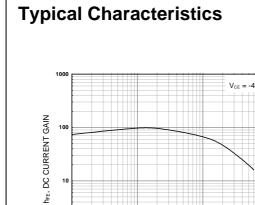
# Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage : TIP42	40	V
	: TIP42A	- 60	V
	: TIP42B	- 80	V
	: TIP42C	-100	V
V <sub>CEO</sub>	Collector-Emitter Voltage: TIP42	- 40	V
	: TIP42A	- 60	V
	: TIP42B	- 80	V
	: TIP42C	- 100	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current (DC)	- 6	A
I <sub>CP</sub>	Collector Current (Pulse)	-10	A
I <sub>B</sub>	Base Current	-2	A
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	65	W
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)	2	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 ~ 150	°C

# **Electrical Characteristics** $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage				
	: TIP42	$I_{C} = -30 \text{mA}, I_{B} = 0$	-40		V
	: TIP42A		-60		V
	: TIP42B		-80		V
	: TIP42C		-100		V
I <sub>CEO</sub>	Collector Cut-off Current				
	: TIP42/42A	$V_{CE} = -30V, I_{B} = 0$		-0.7	mA
	: TIP42B/42C	$V_{CE} = -60V, I_{B} = 0$		-0.7	mA
I <sub>CES</sub>	Collector Cut-off Current				
	: TIP42	$V_{CE} = -40V, V_{EB} = 0$		-400	μΑ
	: TIP42A	$V_{CE} = -60V, V_{EB} = 0$		-400	μΑ
	: TIP42B	$V_{CE} = -80V, V_{EB} = 0$		-400	μΑ
	: TIP42C	$V_{CE} = -100V, V_{EB} = 0$		-400	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		-1	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.3A$	30		
		$V_{CE} = -4V, I_{C} = -3A$	15	75	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	$I_C = -6A, I_B = -600 \text{mA}$		-1.5	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	$V_{CE} = -4V, I_{C} = -6A$		-2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -500mA$	3.0		MHz
Pulse Test: PW≤3	300μs, Duty Cycle≤2%	·	•		•

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1 └ -0.01 -0.1 -1

Figure 1. DC current Gain

Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

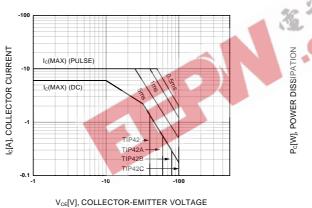


Figure 3. Safe Operating Area

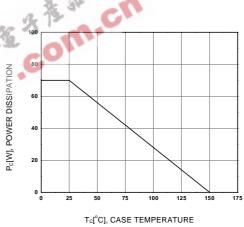
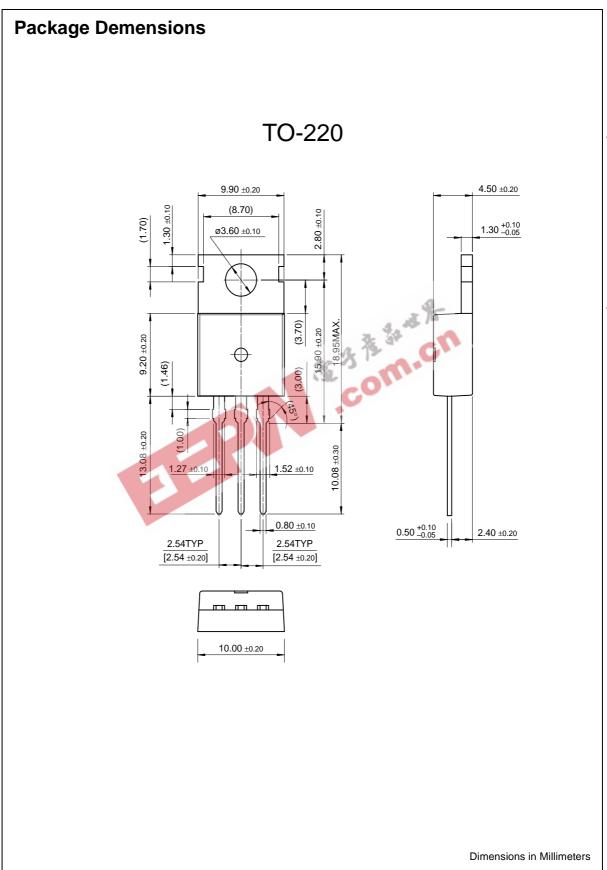


Figure 4. Power derating

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