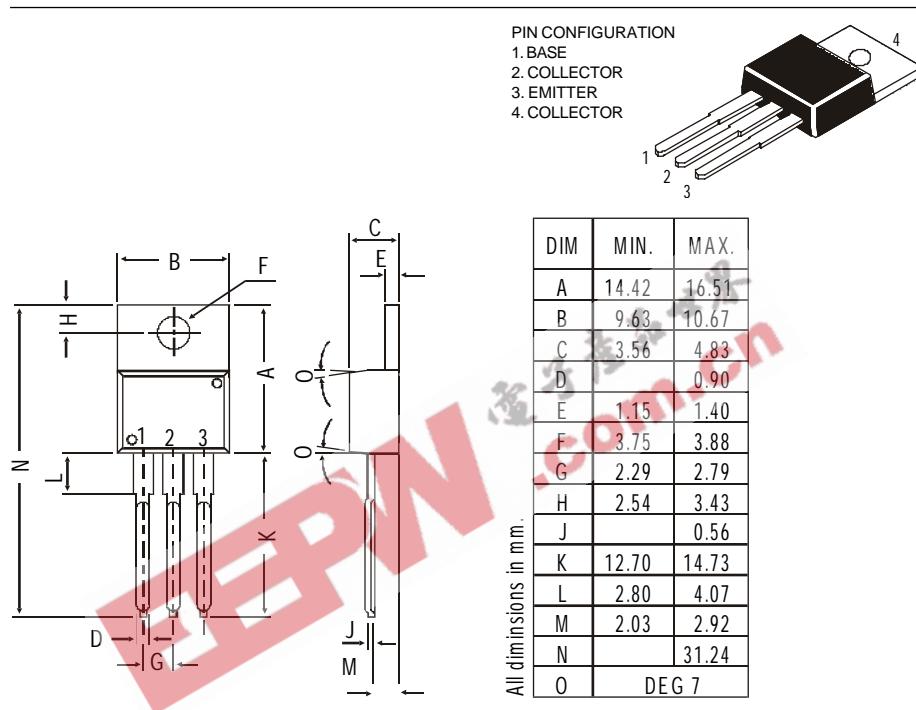


TO-220 Plastic Package

**TIP120, TIP121, TIP122
TIP125, TIP126, TIP127**

TIP120, 121, 122 NPN PLASTIC POWER TRANSISTORS
TIP125, 126, 127 PNP PLASTIC POWER TRANSISTORS
Power Darlingtons for Linear and Switching Applications



ABSOLUTE MAXIMUM RATINGS

	120	121	122			
	125	126	127			
Collector-base voltage (open emitter)	V_{CBO}	max.	60	80	100	V
Collector-emitter voltage (open base)	V_{CEO}	max.	60	80	100	V
Collector current	I_C	max.		5.0		A
Total power dissipation up to $T_C = 25^\circ\text{C}$	P_{tot}	max.		65		W
Junction temperature	T_j	max.		150		$^\circ\text{C}$
Collector-emitter saturation voltage $I_C = 3 \text{ A}; I_B = 12 \text{ mA}$	V_{CESat}	max.		2.0		V
D.C. current gain $I_C = 0.5 \text{ A}; V_{CE} = 3 \text{ V}$	h_{FE}	min.		1.0		

RATINGS (at $T_A=25^\circ\text{C}$ unless otherwise specified)

	120	121	122			
	125	126	127			
Collector-base voltage (open emitter)	V_{CBO}	max.	60	80	100	V
Collector-emitter voltage (open base)	V_{CEO}	max.	60	80	100	V
Emitter-base voltage (open collector)	V_{EBO}	max.		5.0		V

**TIP120, TIP121, TIP122
TIP125, TIP126, TIP127**

<i>Collector current</i>	I_C	<i>max.</i>	5.0	A
<i>Collector current (peak)</i>	I_{CM}	<i>max.</i>	8	A
<i>Base current</i>	I_B	<i>max.</i>	120	mA
<i>Total power dissipation up to $T_C = 25^\circ C$</i>	P_{tot}	<i>max.</i>	65	W
<i>Derate above $25^\circ C$</i>		<i>max.</i>	0.52	$W^\circ C$
<i>Total power dissipation up to $T_A = 25^\circ C$</i>	P_{tot}	<i>max.</i>	2	W
<i>Derate above $25^\circ C$</i>		<i>max.</i>	0.016	$W^\circ C$
<i>Junction temperature</i>	T_j	<i>max.</i>	150	$^\circ C$
<i>Storage temperature</i>	T_{stg}		-65 to +150	$^\circ C$

THERMAL RESISTANCE

<i>From junction to ambient</i>	R_{thj-a}		62.5	$^\circ CW$
<i>From junction to case</i>	R_{thj-c}		1.92	$^\circ CW$

CHARACTERISTICS

$T_{amb} = 25^\circ C$ unless otherwise specified		120	121	122	
		125	126	127	
<i>Collector cutoff current</i>					
$I_E = 0; V_{CB} = 60 V$	I_{CBO}	<i>max.</i>	0.2	-	- mA
$I_E = 0; V_{CB} = 80 V$	I_{CBO}	<i>max.</i>	-	0.2	- mA
$I_E = 0; V_{CB} = 100 V$	I_{CBO}	<i>max.</i>	-	-	0.2 mA
$I_B = 0; V_{CE} = 30V$	I_{CEO}	<i>max.</i>	0.5	-	- mA
$I_B = 0; V_{CE} = 40V$	I_{CEO}	<i>max.</i>	-	0.5	- mA
$I_B = 0; V_{CE} = 50V$	I_{CEO}	<i>max.</i>	-	-	0.5 mA
<i>Emitter cut-off current</i>					
$I_C = 0; V_{EB} = 5 V$	I_{EBO}	<i>max.</i>		2.0	mA
<i>Breakdown voltages</i>					
$I_C = 100 mA; I_B = 0$	$V_{CEO(sus)}^*$	<i>min.</i>	60	80	100 V
$I_C = 1 mA; I_E = 0$	V_{CBO}	<i>min.</i>	60	80	100 V
$I_E = 1 mA; I_C = 0$	V_{EBO}	<i>min.</i>		5.0	V
<i>Saturation voltages</i>					
$I_C = 3.0 A; I_B = 12 mA$	V_{CEsat}^*	<i>max.</i>		2.0	V
$I_C = 5.0 A; I_B = 20 mA$	V_{CEsat}^*	<i>max.</i>		4.0	V
<i>Base-emitter on voltage</i>					
$I_C = 3A; V_{CE} = 3V$	$V_{BE(on)}^*$	<i>max.</i>		2.5	V
<i>D.C. current gain</i>					
$I_C = 0.5A; V_{CE} = 3V$	h_{FE}^*	<i>min.</i>		1.0	
$I_C = 3A; V_{CE} = 3V$		<i>min.</i>		1.0	
<i>Small signal current gain</i>					
$I_C = 3A; V_{CE} = 4V; f = 1 MHz$	$ h_{fE} $	<i>min.</i>		4.0	
<i>Output capacitance at $f = 0.1$ MHz</i>					
$I_E = 0; V_{CB} = 10V$	PNP	C_o	<i>max.</i>	300	pF
	NPN	C_o	<i>max.</i>	200	pF

* Pulse test: pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.