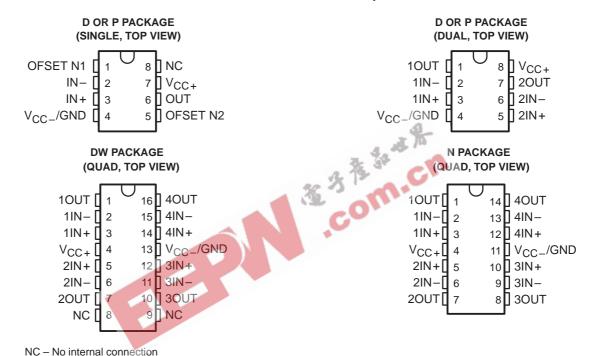
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- Wide Gain-Bandwidth Product . . . 4.5 MHz
- High Slew Rate . . . 13 V/μs
- Fast Settling Time . . . 1.1 μs to 0.1%
- Wide-Range Single-Supply Operation 4 V to 44 V
- Wide Input Common-Mode Range Includes Ground (V<sub>CC</sub>)
- Low Total Harmonic Distortion . . . 0.02%

- Low Input Offset Voltage . . . 3 mV Max (A Suffix)
- Large Output Voltage Swing
   -14.7 V to 14 V (With ±15-V Supplies)
- Large Capacitance Drive Capability 10,000 pF
- Excellent Phase Margin . . . 60°
- Excellent Gain Margin . . . 12 dB
- Output Short-Circuit Protection



### AVAILABLE OPTIONS

		PACKAGE							
TA	COMPLEXITY	PLAS <sup>-</sup>	TIC DIP	SMALL OUTLINE					
'A	COMIT ELXITT	STANDARD GRADE	PRIME GRADE	STANDARD GRADE	PRIME GRADE				
0°C	Single	TL34071P	TL34071AP	TL34071D	TL34071AD				
to	Dual	TL34072P	TL34072AP	TL34072D	TL34072AD				
70°C	Quad	TL34074N	TL34074AN	TL34074DW	TL34074ADW				
-40°C	Single	TL33071P	TL33071AP	TL33071D	TL33071AD				
to	Dual	TL33072P	TL33072AP	TL33072D	TL33072AD				
105°C	Quad	TL33074N	TL33074AN	TL33074DW	TL33074ADW				
-55°C	Single	TL35071P	TL35071AP	TL35071D	TL35071AD				
to	Dual	TL35072P	TL35072AP	TL35072D	TL35072AD				
125°C	Quad	TL35074N	TL35074AN	TL35074DW	TL35074ADW				

D and DW packages are available taped and reeled. Add R suffix to device type (e.g., TL34071ADR).



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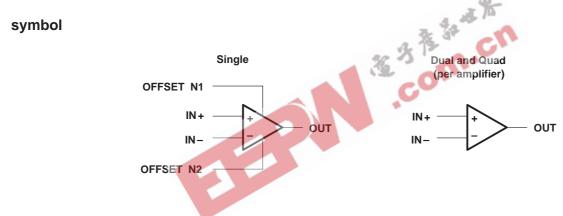


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#### description

Quality, low cost, bipolar fabrication with innovative design concepts are employed for the TL33071/2/4, TL34071/2/4, and TL35071/2/4 series of monolithic operational amplifiers. This series of operational amplifiers offers 4.5 MHz of gain bandwidth product, 13 V/µs slew rate, and fast settling time without the use of JFET device technology. Although this series can be operated from split supplies, it is particularly suited for single-supply operation since the common-mode input voltage range includes ground potential (V<sub>CC</sub>). With a Darlington transistor input stage, this series exhibits high input resistance, low input offset voltage, and high gain. The all-npn output stage, characterized by no dead-band crossover distortion and large output voltage swing, provides high-capacitance drive capability, excellent phase and gain margins, low open-loop high-frequency output impedance, and symmetrical source/sink ac frequency response.

The TL34071/2/4 devices are avaliable in standard or prime performance (A-suffix) grades and are specified over the commercial (0°C to 70°C) temperature range. The TL33071/2/4 devices are available in standard or prime performance (A-suffix) grades and are specified over industrial/vehicular (-40°C to 105°C) temperature range. The TL35071/2/4 devices are avaliable in standard or prime performance (A-suffix) grades and are specified over the military (-55°C to 125°C) temperature range. These low-cost amplifiers are available in single, dual, and quad configurations and are pin compatible with the MC33071/2/4, MC34071/2/4, and MC35071/2/4 series of amplifiers. Packaging options include standard plastic DIP and SO packages.





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### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC+</sub> (see Note 1)	
Supply voltage, V <sub>CC</sub>	22 V
Differential input voltage, V <sub>ID</sub> (see Note 2)	
Input voltage, V <sub>I</sub> (any input)	V <sub>CC±</sub>
Input current, I <sub>I</sub> (each input)	
Output current, IO	$\pm 80 \text{ mA}$
Total current into V <sub>CC+</sub>	80 mA
Total current out of V <sub>CC</sub>	80 mA
Duration of short-circuit current at (or below) 25°C (see Note 3)	unlimited
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T <sub>A</sub> : TL3307x	40°C to 105°C
TL3407x	0°C to 70°C
TL3507x	–55°C to 125°C
Storage temperature range, T <sub>stq</sub>	65°C to 150°C
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds: D, D	W, N, or P package 260°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between V<sub>CC+</sub> and V<sub>CC-</sub>

- 2. Differential voltages are at the noninverting input with respect to the inverting input. Excessive current flows if input is brought below VCC\_-0.3 V.
- 3. The output can be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.

### **DISSIPATION RATING TABLE**

PACKAGE	T <sub>A</sub> ≤ 25°C POWER RATING	DERATING FACTOR ABOVE T <sub>A</sub> = 25°C	T <sub>A</sub> = 70°C POWER RATING	T <sub>A</sub> = 105°C POWER RATING	T <sub>A</sub> = 125°C POWER RATING
D	725 mW	5.8 mW/°C	464 mW	261 mW	145 mW
DW	1025 mW	8.2 mW/°C	656 mW	369 mW	205 mW
N	1150 mW	9.2 mW/°C	736 mW	414 mW	230 mW
Р	1000 mW	8.0 mW/°C	640 mW	360 mW	200 mW

### recommended operating conditions

		TL33	07x	TL34	07x	TL35	07x	UNIT
		MIN	MAX	MIN	MAX	MIN	MAX	UNIT
Supply voltage, V <sub>CC±</sub>	±2	±22	±2	±22	±2	±22	V	
Common mode input voltage V	V <sub>CC</sub> = 5 V	0	2.7	0	2.9	0	2.7	V
Common-mode input voltage, V <sub>IC</sub>	$V_{CC\pm} = \pm 15 \text{ V}$	-15	12.7	-15	12.9	-15	12.7	V
Operating free-air temperature, T <sub>A</sub>			105	0	70	-55	125	°C

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## electrical characteristics at specified free-air temperature, $V_{CC\pm}$ = $\pm 15$ V (unless otherwise noted)

	PARAMETER		TEST CONDITIONS		Т	L3x07x	١	٦	TL3x07x			
	PARAMETER	TEST CO	SNOTTIONS	T <sub>A</sub> †	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT	
			$V_{CC} = 5 V$	25°C		0.5	3		1.5	5		
VIO	Input offset voltage		V <sub>CC</sub> = ±15 V	25°C		0.5	3		1.0	5	mV	
			AGG = ∓ 12 A	Full range			5			7		
αVIO	Temperature coefficient of input offset voltage	V <sub>IC</sub> = 0,	V <sub>CC</sub> = ±15 V	Full range		10			10		μV/°C	
li a	Innut offeet ourrent	$V_O = 0$ , $R_S = 50 \Omega$	V 145V	25°C		7	100		7	100	A	
ΙΟ	Input offset current	11.3 - 00 11	$V_{CC} = \pm 15 \text{ V}$	Full range			250			250	nA	
		]	V00 - F V	25°C		-0.8	-2		-0.8	-2		
1	Innut high current		V <sub>CC</sub> = 5 V	Full range			-2.3			-2.3		
ΙΒ	Input bias current		V 145V	25°C		-0.7	-1.5		-0.7	-1.5	μΑ	
			$V_{CC} = \pm 15 \text{ V}$	Full range			-1.8			-1.8		
	Common-mode input	,		25°C	-15 to 13.2		9	-15 to 13.2				
VICR	voltage range	R <sub>S</sub> = 50 Ω		Full range	-15 to 12.8	30.10	C.V.	-15 to 12.8			V	
		V <sub>CC+</sub> = 5 \ R <sub>L</sub> = 2 kΩ	/, V <sub>CC</sub> = 0,	25°C	3.7	4		3.7	4			
VOH	VOH High-level output voltage		$R_L = 10 \text{ k}\Omega$		13.6	14		13.6	14		V	
		$R_L = 2 k\Omega$		Full range	13.4			13.4				
		$V_{CC+} = 5 \text{ V},  V_{CC-} = 0,$ $R_L = 2 \text{ k}\Omega$ $R_L = 10 \text{ k}\Omega$		25°C		0.1	0.3		0.1	0.3		
VOL	Low-level output voltage			25°C		-14.7	-14.3		-14.7	-14.3	V	
		$R_L = 2 k\Omega$		Full range			-13.5			-13.5		
Δ	Large-signal differential	Vo = ±10 V	, R <sub>L</sub> = 2 kΩ	25°C	50	100		25	100		V/mV	
AVD	voltage amplification	VO = ± 10 V	, KL = 2 KS2	Full range	25			20			V/IIIV	
loo	Short-circuit output current	Source: $V_{ID} = 1 \text{ V}, V_O = 0$		25°C	-10	-30		-10	-30		mA	
los	Short-circuit output current	Sink: V <sub>ID</sub> =	$-1 \text{ V}, \text{ V}_{0} = 0$	25 0	20	30		20	30		IIIA	
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}$ min, RS = 50 $\Omega$		25°C	80	97		70	97		dB	
ksvr	Supply-voltage rejection ratio ( $\Delta V_{CC\pm}/\Delta V_{IO}$ )	$V_{CC\pm} = \pm 1$ to $\pm 16.5 \text{ V}$ ,		25°C	80	97		70	97		dB	
		\/- 0	Nolood	25°C		3.5	4.5		3.5	4.5		
	Supply current	$V_{O} = 0,$	No Load	Full range			4.7			4.7	A	
ICC	(per channel)	V <sub>CC+</sub> = 5 \	/, V <sub>CC</sub> = 0,	25°C		3.4	4.4		3.4	4.4	mA	
		$V_{O} = 0$ ,					4.6			4.6		

<sup>†</sup> Full range is 0°C to 70°C for the TL3407x devices, and -40°C to 105°C for the TL3307x devices, and -55°C to 125°C for the TL3507x devices. ‡ All typical values are at  $T_A = 25$ °C.



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## operating characteristics, $V_{CC\pm}$ = $\pm 15$ V, $T_A$ = $25^{\circ}C$

	DADAMETED	TEST SOUD	TEST CONDITIONS			TL3x07xA			TL3x07x		
	PARAMETER	IESI COND	IIIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT	
SR+	Positive slew rate	$V_{I} = -10 \text{ V to } 10 \text{ V},$	A <sub>V</sub> = 1	8	8 10		8	10	\//		
SR-	Negative slew rate	$R_L = 2 k\Omega$	$A_V = -1$		13			13		V/μs	
	Settling time	A <sub>VD</sub> = -1, 10-V step	To 0.1%		1.1			1.1			
t <sub>S</sub>	Settling time	AVD = -1, 10-v step	To 0.01%		2.2			2.2		μs	
Vn	Equivalent input noise voltage	f = 1 kHz, R	S = 100 Ω		32			32		nV/√Hz	
In	Equivalent input noise current	f = 1 kHz			0.22			0.22		pA/√Hz	
THD	Total harmonic distortion	$V_O = 2 \text{ V to } 20 \text{ V}, \text{ R}$ $A_{VD} = 10, \text{ f}$	L = 2 kΩ, = 10 kHz		0.02			0.02		%	
GBW	Gain-bandwidth product	f =100 kHz		3.5	4.5		3.5	4.5		MHz	
BW	Power bandwidth	$R_L = 2 k\Omega,$ V $A_{VD} = 1,$ T	O(PP) = 20 V, HD = 5.0%		200			200		kHz	
		$R_L = 2 k\Omega$ , $C$	L = 0		60°			60°			
φm	Phase margin	$R_L = 2 \text{ k}\Omega$ , $C_L = 300 \text{ pF}$			40°			40°		1	
	Coin morain	$R_L = 2 k\Omega$ , $C$	L = 0		12			12		dB	
	Gain margin	$R_L = 2 k\Omega$ , $C$	L = 300 pF	4.0	4	A .		4		UB	
rį	Differential input resistance	VIC = 0	a	3	150			150		ΜΩ	
Ci	Input capacitance	V <sub>IC</sub> = 0	180	. ~	2.5			2.5		pF	
	Channel separation	f = 10 kHZ	1	0,,	120			120		dB	
z <sub>O</sub>	Open-loop output impedance	f = 1 MHZ			30			30		Ω	
	1										





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### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TL33071AP	NRND	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL33071D	NRND	SOIC	D	8		Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1YEAR/ Level-1-220C-UNLIM
TL33071P	NRND	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL33072AD	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072ADR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072AP	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL33072DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072P	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL33074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL33074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL33074AN	NRND	PDIP	N	14		None	Call TI	Call TI
TL33074DW	NRND	SOIC	DW	16	- de	None None	Call TI	Call TI
TL33074DWR	OBSOLETE	SOIC	DW	16	273	None	Call TI	Call TI
TL33074N	NRND	PDIP	N	14		None	Call TI	Call TI
TL34071AP	OBSOLETE	PDIP	Р	8	CO	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34071D	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34071DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34071P	OBSOLETE	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34072AD	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072ADR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072AP	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL34072D	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072P	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL34074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074AN	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34074DW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074DWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074N	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35072P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35072PE4	ACTIVE	PDIP	Р	8	50	None	Call TI	Call TI
TL35074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL35074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL35074AN	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35074DW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI



### PACKAGE OPTION ADDENDUM

11-Mar-2005

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TL35074DWR	OBSOLETE	SOIC	DW	16	None	Call TI	Call TI
TL35074N	OBSOLETE	PDIP	N	14	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

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PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

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(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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