7003

HIGH-VOLTAGE, HIGH-CURRENT DARLINGTON ARRAY

Note the ULN7003A (DIP) and the ULN7003LW (SOIC) are electrically identical and share a common terminal number assignment.

ABSOLUTE MAXIMUM RATINGS at $T_A = +25^{\circ}C$

Output Voltage, V _{CEX}
Output Sustaining Voltage,
V _{CE(sus)} 90 V
Output Current, I _C 300 mA
Input Current, I _{IN} 25 mA
Package Power Dissipation,
P _D See Graph
Operating Temperature Range,
T _A -20°C to +85°C
Storage Temperature Range,
T _S 55°C to +150°C

Output current may be limited by duty cycle, number of drivers operating, ambient temperature, and heat sinking. Under any set of conditions, do not exceed the specified maximum current rating or a junction temperature of 150°C.

Integrating seven high-voltage, high-current npn Darlingtons into a monolithic power array, the ULN7003A AND ULN7003LW are designed for interfacing between TTL or CMOS logic and a variety of peripheral loads. The seven open-collector Darlington outputs are specified for 135 V minimum breakdown and 90 V minimum sustaining. Included are integral power diodes for switching inductive loads. Typical applications include relays, lamps, print heads and hammers, solenoids, and level shifting to power discretes.

The ULN7003A/LW include input current-limiting resistors compatible with the drive capabilities of TTL and (most) CMOS operating at a nominal logic supply of 5 V. Operation with 12 V CMOS may require additional input current limiting.

The high sustaining voltage rating of this power array makes it ideal for inductive load applications where Zener diode flyback techniques are used. The increased flyback voltage provides a much faster inductive load turn-off current decay that is especially useful with dc stepper motors, solenoids, and print heads.

Both devices are pinned with outputs opposite inputs to facilitate ease of circuit board layout. The ULN7003A is supplied in a 16-pin plastic dual in-line package with a copper lead frame to maximize device power dissipation capabilities. The ULN7003LW is furnished in a 16-lead small-outline wide-body package for surface-mount applications.

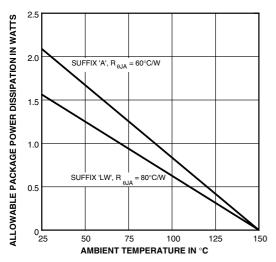
FEATURES

- 135 V Minimum Output Breakdown
- 90 V Minimum Sustaining Voltage
- 300 mA Output Current
- Internal High-Current Clamp Diodes
- Logic-Compatible Inputs

Always order by complete part number:

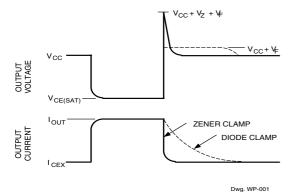
Part Number	Package
ULN7003A	16-Pin DIP
ULN7003LW	16-Lead SOIC





Dwg. GP-018B

A Zener diode can be used to increase the flyback voltage. This gives a much faster inductive load turn-OFF current decay. The maximum Zener voltage plus the load supply voltage plus the internal diode forward voltage must not exceed the device's rated sustaining voltage.



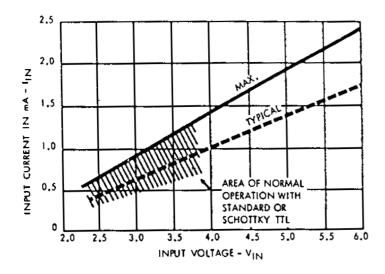
PARTIAL SCHEMATIC (one of seven drivers) ◆ COM 2.7K Dwg. No. A-9651 AY TEST CIRCUIT 50% TURN ON TURN OFF v_{out} 50% Dwg. WP-010 $V_{in} = 3.5 \text{ V for ULN7003A}$ Dwg. No. WP-010 **PULSE** 93Ω **GENERATOR** PRR = 10 kHz DC = 50% 50 Ω 50 pF Dwg. EP-020



ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Leakage Current	I _{CEX}	V _{CE} = 135 V	_	_	50	μА
		V _{CE} = 135 V, T _A = +70°C	_	_	100	μА
Output Sustaining Voltage	V _{CE(sus)}	I _C = 250 mA, L = 2 mH	90	_	_	V
Output Saturation Voltage	V _{CE(SAT)}	I _C = 100 mA, I _{IN} = 250 μA	_	1.1	1.3	V
		I _C = 250 mA, I _{IN} = 350 μA	_	1.3	1.6	V
Input Current	I _{IN(ON)}	V _{IN} = 3.85 V	_	0.93	1.35	mA
	I _{IN(OFF)}	I _C = 500 μA, T _A = +70°C	50	65	_	μА
Input Voltage	V _{IN(ON)}	V _{CE} = 2.0 V, I _C = 200 mA	U -	_	2.4	V
		$V_{CE} = 2.0 \text{ V}, I_{C} = 250 \text{ mA}$	_	_	2.7	V
Input Capacitance	C _{IN}	CO.	_	15	25	pF
Switching Delay	t _{pd}	Turn On, I _C = 250 mA	_	0.05	1.0	μs
		Turn Off, I _C = 250 mA	_	0.5	1.0	μs
Clamp Diode Leakage Current	IR	V _R = 150 V	_	_	50	μΑ
		V _R = 150 V, T _A = +70°C	_	_	100	μΑ
Clamp Diode Forward Voltage	V _F	I _F = 250 mA	_	1.7	2.0	V

Typical Data is for design information only.

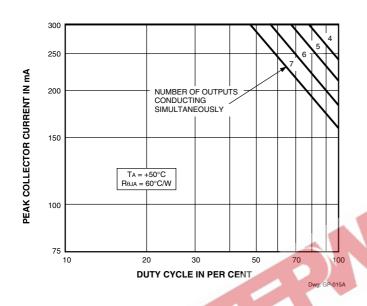


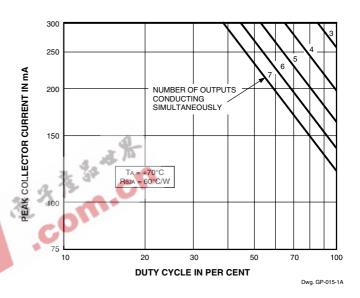
TYPICAL INPUT CURRENT AS A FUNCTION OF INPUT VOLTAGE at T_A = +25°C

ALLOWABLE PEAK COLLECTOR CURRENT AS A FUNCTION OF DUTY CYCLE

ULN7003A at $T_A = +50$ °C

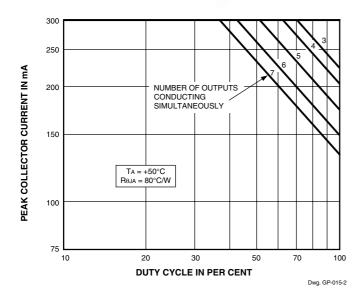
ULN7003A at $T_A = +70^{\circ}C$

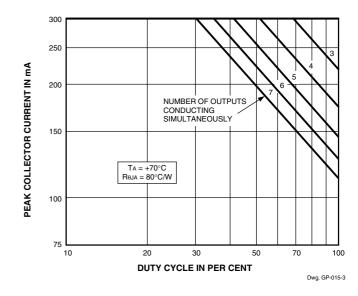




ULN7003LW at $T_A = +50$ °C

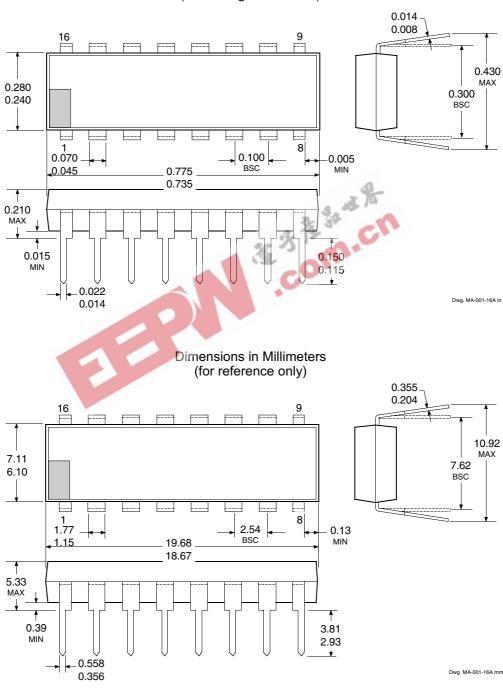
ULN7003LW at $T_A = +70^{\circ}C$





ULN7003A

Dimensions in Inches (controlling dimensions)



NOTES: 1. Lead thickness is measured at seating plane or below.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.
- 4. Supplied in standard sticks/tubes of 25 devices.

ULN7003LW Dimensions in Inches (for reference only) 0.0125 0.0091 0.2992 0.419 0.394 0.2914 0.050 Н Н Н Н 0.020 0.013 0.4133 0.3977 0.0926 0.1043 T _{0.0040 MIN} **Dimensions in Millimeters** (controlling dimensions) 0.32 0.23 10.65 7.60 10.00 7.40 0.40 - 2 3 0° то 8° **↓** 10.10 2.65 2.35 L _{0.10 MIN} Dwg. MA-008-16A mm

NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Supplied in standard sticks/tubes of 47 devices or add "TR" to part number for tape and reel.





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HIGH-VOLTAGE (≥60 V) PERIPHERAL POWER AND DISPLAY DRIVERS

IN ORDER OF 1) OUTPUT VOLTAGE, 2) OUTPUT CURRENT, 3) NUMBER OF DRIVERS

Oı	Output Ratings*		Features					
v	mA	#	Serial Input	Latched Drivers	Diode Clamp	Saturated Outputs	Internal Protection	Part Number †
60	-25	8	_	Х	_	_	_	5815
	-25	10	X	X	Active Pull-Dow	/n –	_	5810-F and 6809/10
	-25	12	Χ	X	Active Pull-Dow	/n – "	_	5811 and 6811
	-25	20	X	X	Active Pull-Dow	vn –	_	5812-F and 6812
	-25	32	X	X	Active Pull-Dow	/n ⊢		5818-F and 6818
	300	4	_	_	X.	Х	X	2557
	600	4	_	_	1/2-13 '	X	X	2547
	600	4	_	-	X	X	X	2549
	700	4	_		X	X	X	2559
	700	4	_		X	Χ	X	2543
	4000	4	_		X	_	_	2944
80	-350	8	_	_	X	_	_	2983 and 2984
	350	8	X	X	_	_	_	5822
	350	8	X	Х	Х	_	_	5842
	-350	8	X	X	X	_	_	5890
	1500	4	_	_	_	_	_	2065 and 2069
	4000	4	_	_	Χ	_	_	2879
85	-25	8	_	_	_	-	_	6118
95	300	7	_	_	X	_	_	2023
	300	8	_	_	X	_	_	2823
	350	7	_	_	X	_	_	2024
	350	8	_	_	X	_	_	2824

^{*} Current is maximum test condition; voltage is absolute maximum allowable. Negative current is defined as coming out of (sourcing) the output.



[†] Complete part number includes additional characters to indicate operating temperature range and package style.