

10 BASE-T, SIL FILTER (WITH 2 COMMON CHOKE)

P/N: FD2046: APPLY TO GENERIC IC

A. GENERAL SPECIFICATIONS:

1. IEEE 802.3 AND IEEE 802.5 COMPATIBLE.
2. DESIGNED TO MEET AMD AM79C98, AT&T T7220 & NS DP83922 CHIPSET REQUIREMENTS AND TO MEET FCC CLASS B.
3. INTEGRATED FILTER AND ISOLATION FUNCTION IN 10 PIN SIL PACKAGE.
4. LOW INSERTION LOSS CHARACTERISTICS.
5. COST EFFECTIVE DESIGN.
6. OPERATING TEMP RANGE: 0°C TO +70°C.
7. STORAGE TEMP RANGE: -25°C TO +125°C.
8. RECEIVING & TRANSMITTING SIDE BOTH WITH COMMON MODE CHOKE.

B. ELECTRICAL SPECIFICATIONS @ 25°C:

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|----------------|-------|-------|---------------|
| 1. ATTENUATION | 20MHz | 30MHz | 40MHz |
| TRANSMITTER | -5 | -27 | -35 (dB MIN.) |
| RECEIVER | -5 | -20 | -31 |
2. INSERTION LOSS (1-10MHz): TRANSMITTER & RECEIVER -1.0 dB MAX.
 3. CROSS TALK (1-10MHz): -30 dB MIN.
 4. GROUP DELAY CHANGE: 4.0 nS MAX.
 5. RETURN LOSS (1-10 MHz): -15 dB MIN.
 6. HI-POT: 2000 VRMS(PRI./SEC.).
 7. CUT-OFF FREQUENCY: 17+/-1.5 MHz
 8. CMRR (dB MIN.): TX: -40 AT 30-70MHz;RX:-30 AT 70-100MHz

C. SCHEMATICS & DIMENSIONS & MARKING (unit mm):

