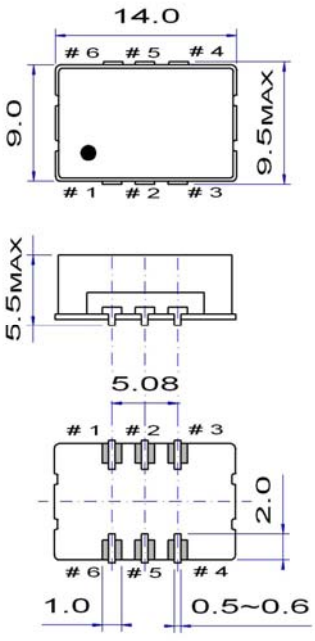
PXO-P9-3PEH-6J

Low jitter
LVPECL SMD Clock Oscillator

Features

- Applications: SONET / SDH / ATM, Gigabit Ethernet, switching system and telecom infrastructure
- Output frequency up to 800 MHz
- Low jitter < 1 ps
- Low phase noise

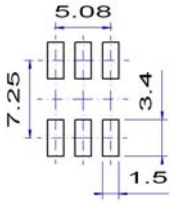
Parameter	Specification	
Frequency range	100 ~ 800 MHz	
Standard frequencies	120.00, 122.88, 155.52, 307.2, 320, 460.80, 491.52 & 622.08 MHz	
Supply voltage	+3.3 V ±5 %	other supply voltage on request
Supply current	75 ~ 100 mA	
Frequency stability (✳)	< ±25 ppm < ±50 ppm	over -20 ~ +70 °C over -40 ~ +85 °C
Output signal	LVPECL	
Output voltage	$V_{OH} \geq$ 2.275 V	$V_{OL} \leq$ 1.680 V
Output load	50 Ω	@ Vdc -2.0 V
Jitter (rms) 1σ	< 1 ps	@ 12 kHz ~ 20 MHz from carrier frequency
Symmetry	45 ~ 55 %	@ ½ Vdc
Rise / fall time	< 0.4 ns	20 % to 80 % of amplitude
Tri-state function Pin #1 = high or open Pin #1 = low	Pin #4 & #5 ➔ Pin #4 & #5 ➔	(E) signal (D) high impedance
Operating temperature range	-20 ~ +70 °C -40 ~ +85 °C	standard application industrial application
Storage temperature range	-55 ~ +125 °C	
Packaging unit	tape & reel	500 or 1'000 pieces
(✳) All inclusive: Stability vs. temperature, tolerance, aging, supply & load variation		
Customer specifications on request		



Pin function

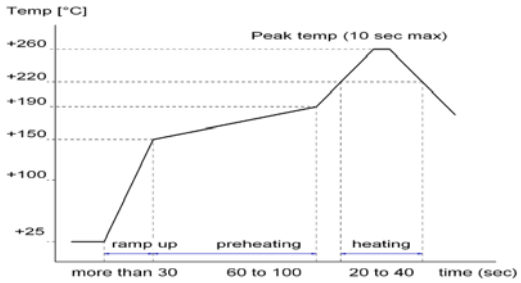
# 1	E/D or not connected
# 2	Not connected or E/D
# 3	GND
# 4	Output
# 5	Complementary output
# 6	Vdc

Example for solder pattern



Do not design any conductive path between the pattern

Example for IR reflow soldering temperature



2002/95/EC RoHS compliant

18 Mar. 10

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